SOFTWARE REFERENCE MANUAL

H8 COMPUTER

FRONT PANEL MONITOR PAM-8

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INTRODUCTION

This Manual describes the functions and operations of the Heath H8 Panel Monitor Program, PAM-8, which resides permanently in a ROM on the H8 CPU board. PAM-8 provides a sophisticated front panel display and keyboard emulation as well as handling master clear and interrupt operations. Some of the major features of PAM-8 are:

- Memory contents display and alteration.
- Register contents display and alteration.
- Program execution control (both breakpoint and single instruction operation).
- Self-contained bootstraps for program loading and dumping.
- Port input and output routines.

In addition to the above features, PAM-8 can be instructed (by means of a flag byte contained in H8 RAM) to bypass some or all of its normal functions so the sophisticated user can augment or totally replace them.

Communication with the Panel Monitor is accomplished through three devices: the keypad, the 7-segment displays, and the audio alert. The user enters commands and values through the 16-key keypad, and PAM-8 responds visually through the front panel displays. In addition to the front panel displays, PAM-8 provides the keypad entry and function feedback to the built-in speaker. Appropriate signals (short, medium, and long beeps) indicate that commands and data are accepted or rejected.

THEORY OF OPERATION

This section will supplement the information contained in the "Operation" and "Circuit Description" sections of your H8 Operation Manual. In order to fully understand how PAM-8 operates, you must be familiar with the H8 front panel and CPU. A thorough knowledge of the 8080 instruction set and its architecture is also essential.

Power Up and Master Clear

PAM-8 initializes the H8 whenever you power-up or master clear (RST). You initiate the power-up operation by turning on the rear panel Power switch. You can master clear by simultaneously depressing both the lower right-hand (RSTØ) and lower left-hand (Ø) keys of the H8 front panel keypad. Both power-up and RST cause a level zero (highest priority) interrupt and result in a long beep from the audio alert.

During initialization, PAM-8 enters a routine which determines the high limit of continuous RAM. Once the high limit of available RAM is determined, the H8 stack pointer (SP) is set to this value and control is passed to the front panel command loop. Using this feature, you can immediately determine the total amount of continuous memory above 8K by displaying stack pointer value.

Clock Interrupts

The Clock Interrupt is a crucial element in the operation of the H8 front panel system. This level one interrupt is generated by the front panel hardware every 2,000 μ S. PAM-8 uses this interrupt to check for some keyboard commands, to check for user program breakpoints, and to refresh the front panel displays.

PAM-8 performs these functions using a series of subroutines which are executed as necessary when indicated by the interrupts. For this reason, all user programs must maintain a valid stack (at high memory) containing at least 80 free bytes at all times. If this stack space is not available and PAM-8 is running (it can be disabled; see the Advanced Control Section), unpredictable software damage can occur in your program. In the same manner, if your program should execute a DI (Disable Interrupt) instruction, no front panel services including the RTM (Return To Monitor) function are available until an EI (Enable Interrupt) instruction is executed or until a master clear (RST/Ø) is performed.

PAM-8 Modes/Using RST and RTM

PAM-8 is always in either the monitor mode or the user mode. In the monitor mode no user program is executing, PAM-8 loops reading the keypad and refreshing the displays. All commands entered via the keypad are valid; however, the RTM command is meaningless.

When your program is being executed, PAM-8 is in the user mode and the MON LED on the front panel is extinguished. Only two keyboard commands are valid in this mode: RST (master clear) and RTM (Return To Monitor). NOTE: Both of these commands are dual key commands. No single key command is recognized, so a user program may have free use of the entire keypad.

You can return PAM-8 to the monitor mode by using the RTM command (simultaneously press the Ø and the # keys). This command stops program execution at the end of the current instruction, stores the current value of each register, and returns PAM-8 to the monitor mode. You can then continue your program by pressing the GO key. The RST command (simultaneously press the 0 and the / keys) performs the master clear operation described earlier and does not save any register values.

Normally, when a user program is running, PAM-8 is also running. Thus, if PAM-8 is displaying the contents of the HL register pair and the user program is started, it continues to display the contents of this register pair as the program is run. If the user program changes the contents of the HL pair, the change is immediately reflected in the front panel displays. In a similar manner, if a memory location is displayed when a user program is started, it is displayed during the time the user program is run. If the user program changes the contents of the displayed memory location, the front panel display changes.

Since PAM-8 does not recognize keypad commands in the user mode, the RTM command must be used before the memory location or register being displayed is changed to a new location or a different register. Once you select the new location or different register, you can resume program execution by pressing GO.

NOTE: PAM-8 requires about 10% of the H8 CPU's resources to process the display interrupts. Programs which are compute-bound may be slowed down by simultaneous operation of PAM-8. In this situation, you may wish to turn off the clock interrupts to improve execution time. See "Using Interrupts" on Page 1-24.

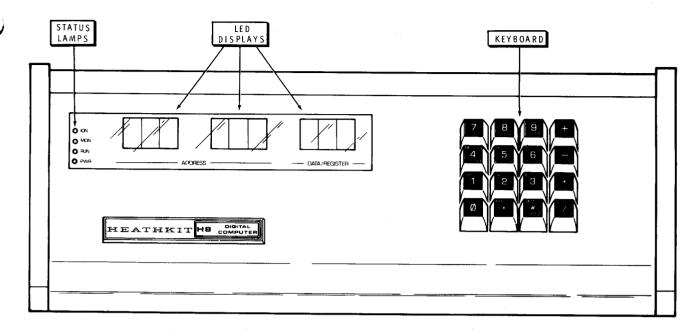


Figure 1-1

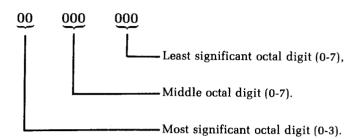
H8 Displays

You must understand the H8 front panel presentation in order to use PAM-8. The display is made up of 9 digits, in three groups of three digits each. See Figure 1-1. Each group of three digits displays one byte (eight bits) of information. This information may be the contents of a designated register or memory location, or it may be the address of a memory location itself. The register names are also displayed.

All binary numbers are converted to octal format for display on the H8 front panel. The following table shows binary to octal conversion.

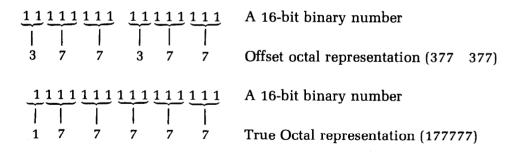
BINARY NUMBER	OCTAL NUMBER
000	
001	1
010	2
011	∄ .
100	Ч
101	5
110	6
111	7

Each byte is displayed as two-and-one-half octal digits. The octal numbers lie in the range of 000 to 377 for binary numbers in the range 00000000 to 111111111, as shown below.



NOTE: As there are only eight bits in a byte, the most significant octal digit only represents two bits and is therefore displayed as 0 to 3. If the user should inadvertently enter the octal digits 4 to 7 into the most significant digit, the most significant bit is lost. Losing this bit converts 4 through 7 into the digits 0 through 3 respectively.

Also note that 16-bit numbers, such as memory addresses and certain register contents, are still displayed as two eight-bit numbers. Therefore, the H8 front panel representation of the number is made up of **two** groups of three octal numbers in the range of 000 to 377. This representation of 16-bit binary numbers is known as **offset octal**, and is used consistantly throughout all H8 displays of 16-bit numbers. Offset octal must not be confused with octal. For example:



The lower example shows true octal representation of a 16-bit binary number. This is **not** used by the H8 front panel displays or any H8 software. Occasionally you will see offset octal numbers printed with a decimal point separating the upper and lower bytes. For example:

377.377

Hi Byte Lo Byte

H8 Keypad

The H8 Keypad consists of 16 keys, as shown in Figure 1-1. When the keypad is operating under the control of PAM-8, it exhibits a number of unique properties.

- Each keystroke is verified by a short beep from the audio alert.
- Octal digits are entered using the keys 0 through 7.
- Holding a key down continuously repeats the key's function.
- The + key increments memory port or register locations.
- The − key decrements memory port or register locations.
- The * key cancels previous keypad entries.
- The ALTER key causes PAM-8 to enter the alter mode.
- The MEM key causes PAM-8 to enter the display memory mode.
- The REG key causes PAM-8 to enter the register mode.

Many of the keys on the keypad have multiple functions, depending on the PAM-8 mode being used. In the register mode, for example, the numeric keys (1-6) call the register indicated in the upper left-hand corner of the key. When the PAM-8 is in neither the register nor the memory mode, the keys perform the functions indicated in the lower right-hand corner of the key.

The # and / keys have additional special functions, as indicated earlier. When the / key is pressed simultaneously with the 0 key, the RST (master clear) sequence is initiated. When the # sign key is depressed simultaneously with the 0 key, the RTM (Return To Monitor) function is initiated, the user program is stopped, and PAM-8 regains control.

Each key is covered in greater detail as the various function are discussed.

DISPLAYING AND ALTERING MEMORY LOCATIONS

One of the major features of PAM-8 is its ability to examine the contents of any H8 memory location and to modify the contents of that memory location if it is RAM.

When the H8 is first powered up, PAM-8 is in the display memory mode. This mode is indicated by all digits displaying octal numbers and no decimal points being on.

Specifying a Memory Address

If you wish to display or alter the contents of a memory location. You must first place PAM-8 in the memory address mode and then enter the desired memory address. Place PAM-8 in the memory address mode (if not already there) by pressing the MEM (Memory) key. Specify the address to be displayed or altered by entering the 6-digit address (offset octal).

When you press the MEM key, all the decimal points will light. This indicates that the address may now be entered. Once the full 6-digit address is entered, the decimal points turn off, indicating that address entry is completed. After all 6 digits are entered, the address is displayed in the left-most six displays, and the contents of the addressed memory location are displayed in the right-hand 3 digits.

NOTE: As you press each key, including the MEM key, a short beep indicates successful entry. As each group of three octal digits is successfully entered, a medium beep is sounded. The sequence by which you specify a memory address is shown in Figure 1-2.

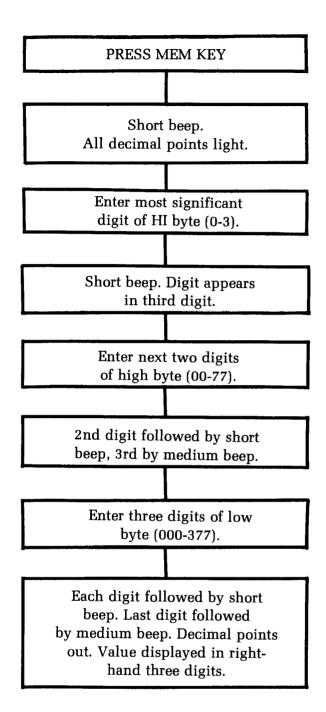


Figure 1-2 Entering a memory address through PAM-8.

NOTE: If you press a non-octal digit key as one of the six address digits, an error is flagged (a long beep). Once this error is flagged, the PAM-8 considers the address complete and extinguishes the decimal points. The entire sequence must be repeated.

Altering a Memory Location

Before you can alter a memory location, you must first display the contents of the memory location by specifying the memory address as described in the preceding paragraphs. After you specify the memory address, press the ALTER key. This will cause PAM-8 to enter the memory alter mode.

When PAM-8 enters the memory alter mode, a single decimal point rotates from right to left through all 9 digits. You can now alter the contents of the displayed location by entering the new octal value (three digits on the keypad). When the three digits have been entered, acoustical verification (a short beep) is given and the memory address is incremented. You can then alter this new location by entering three more digits or pressing one of the following keys, causing the monitor to perform the indicated function:

KEY	FUNCTION
+	Increment the address.
_	Decrement the address.
MEM	Specify a new memory address (leave memory alter mode).
REG ALTER	Specify a register for display (leave memory alter mode). Exit from the alter mode (into the display mode).

NOTE: PAM-8 automatically increments the memory address as each entry (3 octal digits) is complete. Therefore, you may load a program in sequential locations very rapidly. Each location is modified by simply entering the three octal digits.

The following example reviews each step as the H8 is turned on; the memory address mode is entered; and the location 040 123 is addressed, altered to 345, checked, and closed.

	DISPLAY	-	<u>COMMENTS</u>
x x x	x x x	x x x	Random memory display at power up (X=random number.)
X . X . X .	X . X . X .	X.X.X.	MEM key pressed. (In memory address mode, a short beep.)
X.X.0.	$X \cdot X \cdot X$.	X.X.X.	0 key pressed. (Short beep.)
X.0.4.	X . X . X .	X.X.X.	4 key pressed. (Short beep.)
0.4.0.	X . X . X .	X . X . X .	0 key pressed. (Medium beep.) Contents of location 040 XXX displayed.)
0.4.0.	X.X.1.	X . X . X .	1 key pressed. (Short beep. Contents of 040 XX1 displayed.)
0.4.0.	X.1.2.	X . X . X .	2 key pressed. (Short beep. Contents of 040 X12 displayed.)
040	1 2 3	ххх	3 key pressed. (Medium beep. Contents of desired location 040 123 displayed, decimal points out.)
0.4.0	1.2.3	X . X . X	ALTER key pressed. (Short beep. Decimal points rotate.)
0.4.0.	1.2.3.	X.X.3.	3 key pressed. (Short beep. Decimal points rotate .)
0.4.0.	1.2.3.	X.3.4.	4 key pressed. (Short beep. Decimal points ro-tate .)
0.4.0.	1.2.4.	X . X . X .	5 key pressed. (Medium beep. Address increments one location. Decimal points rotate .)
0.4.0	1.2.3	3.4.5	-key pressed. (Short beep. Address decrements one location. Decimal points rotate .)
0 4 0	1 2 3	3 4 5	ALTER key pressed. (Short beep. Decimal points go out.)

Stepping Through Memory

When PAM-8 is either in the display memory or alter memory modes, the +and -keys increment and decrement the memory address. Each time you press the key, PAM-8 increments (or decrements) the memory address one location. If you hold the key down, the auto-repeat function of PAM-8 causes the memory address to increment or decrement repeatedly (approximately one location every second).

DISPLAYING AND ALTERING REGISTERS

PAM-8 can display and alter the contents of the 8080 CPU registers, just as it displays and alters the contents of H8 memory locations. Although the process is quite similar, a few special features should be noted.

Specifying a Register for Display

Press the REG key to specify that a register is to be displayed. After you press the REG key, press a second key (SP through PC, see the Table below) to specify the desired register or register pair.

When the REG key is pressed, six decimal points light, indicating that you must now select a register. NOTE: Simply pressing the REG key causes a register name to appear in the right-hand digits. However, you must select a register using the Register Select key before a register is definitely selected and its true contents are displayed. Once a register is selected, the decimal points are extinguished.

The contents of the selected register pair are displayed in the six left-most displays. The register name (or names) are displayed in the two right-most digits of the right-hand three displays. The registers are selected and displayed in accordance with the following table:

KEY	LEFT 3 DIGITS	MIDDLE 3 DIGITS	RIGHT PAIR	COMMENTS
SP (1)	000 to 377	000 to 377	5 <i>P</i>	Stack pointer
AF (2)	000 to 377	000 to 377	ЯF	AF Register pair
BC (3).	000 to 377	000 to 377	ЬΓ	BC Register pair
DE (4)	000 to 377	000 to 377	дĒ	DE Register pair
HL (5)	000 to 377	000 to 377	ΗĹ	HL Register pair
PC (6)	000 to 377	000 to 377	Pc	Program counter

NOTE: The contents of any single eight-bit register may lie in the range of 000 to 377 octal. The stack pointer (SP) and the program counter (PC) are 16-bit registers and are displayed as two sets of three octal numbers. Each 3-digit grouping corresponds to one byte (8 bit number). When a register pair is displayed, the left three digits correspond to the left register and the middle three digits correspond to the right register. For example:

256 3 12 AF

Register A contains 256 and F contains 312.

Altering the Contents of a Selected Register

To alter the contents of a register (or register pair), you must first specify it as described in the preceding paragraphs. After you select the register or register pair, press the ALTER key. This will cause the six left-hand decimal points to rotate right to left, indicating that you may enter 6 digits to alter the contents of the indicated register or register pair.

Alternatively, you may press one of the following command keys:

KEY	FUNCTION
+	Changes the register pair being displayed.
_	Changes the register pair being displayed.
MEM	Specify a new memory address (leave the alter register mode).
REG	Specify a new register for display (leave alter register mode).
ALTER	Exit the register alter mode.

NOTE: Stack pointer register (SP) is not a direct display of the real stack pointer register, but simply a copy of the real stack pointer register and is used for display purposes only. The stack pointer cannot be altered from the front panel. To alter the stack pointer register, an SPHL (SPHL = 371) instruction must be written into memory. The desired new stack pointer value is then placed in the HL register pair. PAM-8's single instruction mode is used to execute the SPHL swap instructions, loading the stack pointer with the contents loaded in the HL register pair.

Stepping Through the Registers

Use + and - keys to change the register pair being displayed. For example, if the DE register pair is being displayed, press the + key causes the next sequential register pair to be displayed (the HL pair). In the same manner, pressing the - key causes the register to decrement to the preceding pair. For example, if the DE pair is being displayed, pressing the - key displays the BC register pair. NOTE: Holding down either the + key or the - key causes the display to continuously increment or decrement through all the six registers/register pairs.

PROGRAM EXECUTION CONTROL

PAM-8 supports three basic program execution control facilities:

- Beginning or starting execution.
- Breakpointing.
- Single instruction.

Each of these execution controls permits the programmer to execute the desired portions of a program and examine its effects. He may execute the entire program, or a small group of instructions, or a single program instruction.

Initiating Program Execution

To begin the execution of a program residing in H8 memory, place the address of the first instruction to be executed in the PC (program counter). Use the methods described in "Displaying and Altering Registers" (Page 1-14). Once the address of this first instruction is placed in the program counter, press the GO key and program execution will begin. NOTE: Unless the program disables the front panel, the display continues to be actively updated, although the front panel commands are no longer active (except for RST and RTM). If the program counter is displayed when you press the GO key, PAM-8 continuously monitors the program counter.

Breakpointing

Breakpointing permits the programmer to execute small portions of a program and then return to PAM-8. Breakpointing is especially useful when a program is being "debugged." Small portions of the program may be executed and their results observed. If there is an error, it may be corrected before an entire program is involved.

When the H8 executes a program and encounters a halt instruction, it re-enters PAM-8 and sounds the alarm. All of the registers are preserved and the program counter points to the address **following** the address of the halt instruction. Thus, you can breakpoint a program from the front panel by inserting halt instructions (HLT = 166) at the desired points throughout the program. When a particular

section of the program is tested and the breakpoint feature is no longer required, you can change the halt to a NOP (NOP = 000). Once the halts are changed to NOPs, execution of the NOP simply passes control to the next successive instruction. Program execution for breakpointing uses the GO key as described above.

NOTE: If you temporarily replace an existing instruction with a halt, you must restore the instruction before resuming program execution. The contents of the program counter point to the address **following** the halt. Therefore, if the instruction which replaced the halt is to be executed, when the program continues, the contents of the program counter must be decremented one location before execution is resumed.

Single Instruction Operation

Any user program may be operated in the single instruction mode. This procedure is identical to the GO command, except that the SI key is pressed rather than the GO key. When the SI key is pressed, a single **instruction** (not a single machine cycle) is executed and then control is returned to PAM-8. Single instruction operation is available for careful inspection of program results and for executing special programs, such as swapping the HL register pair with the stack pointer as discussed in "Altering the Contents of a Selected Register" (Page 1-15).

Interrupting a Program During Execution

You can interrupt a running program (with all registers preserved at the point of interruption) by pressing RTM & 0. You can then examine and/or alter the contents of various memory locations and all the registers as required. Resume execution of the program at the next sequential instruction by simply pressing the GO key. NOTE: Although all registers and memory locations are preserved when RTM & 0 are pressed, it is very difficult to stop a program at an exact location. Therefore, use the breakpoint feature if you want to stop the program at an exact location.

LOAD/DUMP ROUTINES

PAM-8 contains a routine that lets you load and dump memory contents from or to a tape. This feature is especially important, as most computers require one or two successive "boot strap" routines to be hand-loaded before a desired program can be loaded into the main memory. All these "boot strap" routines are contained within the PAM-8 ROM, and use sophisticated error checking techniques. Thus, a program can be loaded or dumped by simply pressing a single key.

Loading From Tape

To load from a tape, ready the reader device with the tape to be loaded prior to executing the load command. Place PAM-8 in the display memory mode and press the LOAD key. Once the LOAD key is pressed, PAM-8 starts the tape transport and scans the tape for the first file record.

No change will be seen on the front panel displays until PAM-8 finds the first file. When the first file record is located, PAM-8 checks it to see if it is the first (or only) record in a sequence, and the record is a memory dump record. If it is not a memory dump record, a number two error is flagged (see "Tape Errors" on Page 1-20).

Once a correct record is found, loading proceeds. The loading procedure places the entry point address of the program being loaded in the H8 program counter. The H8 memory is then loaded. The displays continuously show the address being loaded and the data being loaded at these addresses. When the load is complete, PAM-8 sounds a long beep and displays the final memory address. If the load is faulty, a number one error is displayed and the audio alert continuously beeps. (See "Tape Errors," Page 1-20.)

NOTE: You may abort a partial load by using the CANCEL key. Naturally, the load image resulting from this action is incorrect, and should not be executed.

Dumping to Tape

Before dumping a memory image onto tape, the following three dump parameters are required:

- The entry point address (the program starting address).
- The dump starting address.
- The dump ending address.

Set the desired entry point address by placing this value in the program counter (PC). This value will be placed in the program counter whenever you load the program so execution will begin at this address when you press the GO key.

Place the dump starting address into the first two H8 RAM cells. These are: 040 000 (offset octal) and 040 001 (offset octal). NOTE: The low order byte of the address should be placed into location 040 000 and the high order byte of the starting address should be placed into location 040 001.

Enter the dump ending address as a memory address using the # (MEM) key. Then ready the tape transport and press the DUMP key. As the tape dump takes place, the number of bytes left to be dumped and the contents of the memory location being dumped are displayed on the front panel. You can abort a dump by using the CANCEL key. If the CANCEL key is used, an incomplete dump image is left on the tape. This cannot be loaded at a future date. NOTE: A successful load automatically sets up the following three dump parameters:

- A. The program starting locations are stored in locations 040 000 and 040 001.
- B. The program ending location is displayed.
- C. The program counter contains the program entry point.

Figure 1-3A shows the steps of a typical dump sequence and Figure 1-3B shows the steps of a typical load sequence.

- 1. Set PC to 040 100; $(040\ 100 = entry\ address)$.
- 2. Set 040 000 to 100 (100 = low byte of dump start).
- 3. Set 040 001 to 040 (040 = high byte of dump start).
- 4. Enter memory address 052 340 (052 340 = end address of dump).
- 5. Be sure tape is ready.
- 6. Press DUMP.

 $\label{eq:Figure 1-3A}$ The H8 memory image dump.

- Be sure tape is ready.
- 2. Press LOAD.

Figure 1-3B
The H8 memory image load.

Copying a Tape

The beginning and final address of the load image are placed at the appropriate points. Thus, to copy a tape, simply load the tape as described in "Loading From Tape" (Page 1-18). Then ready the dump tape drive and press the DUMP key. A dump then takes place, including entry point, initial address, and final address.

In a similar manner, to load, alter, and then dump, enter only the ending address. The other paramters are unchanged from the load if locations 040 000, 040 001 or the program counter have not been modified during the altering procedure.

Tape Errors

PAM-8 detects two types of tape errors: record errors and checksum errors. In either case, when an error is detected, the tape transport is halted. The error number is then displayed in the center three digits (001 for a checksum error, 002 for a record error) and the alarm is repeatedly sounded. To halt the alarm and return to the command mode, press the CANCEL key.

RECORD ERRORS

The following are typical causes of record errors.

- Attempting to load a file which is not a memory image. For example, loading an editor text file or a BASIC program file.
- Attempting to start a load in the middle of a load image. Therefore
 missing the initialization information at the start of the file.
- A tape error which causes a portion of the load image to be missed so the next record read is not in the proper sequence.

CHECKSUM ERRORS

A checksum error is flagged when the CRC (Cyclical Redundancy Check) checksum following a record does not match the CRC calculated by PAM-8. This error means that the record is either incorrectly recorded or the load is faulty. In either case, the load should be attempted again. If successive loads result in repeated failures, the original tape must be suspected as faulty.

I/O FACILITIES

PAM-8 supports two commands that allow you to perform input and output functions on H8 I/O ports. These front panel instructions permit simple manipulation of the H8 I/O ports without your having to write extensive routines to perform these functions.

Inputting From a Port

To input from a port, press the # key. Then enter three zero digits and the three-digit address (octal) of the desired port. NOTE: The front panel should now display 000 AAA, where AAA is the port address and 000 is meaningless. Press the IN key to read the port, the value is displayed in the three left-most digits of the front panel display.

Outputting to a Port

To output to a specified port, press the # key. Then enter the value to be supplied to the port in the three left-most displays. The port address is entered into the middle three displays. The display is of the form VVV AAA, where V stands for value, and A for address. Pressing the OUT key causes the value to be outputted to the indicated port.

Addressing Port Pairs

Frequently, ports are assigned in pairs, where one of the two port addresses is the control and status register and the other port is the data port. Address port pairs by using the + and - key to change ports. Once the initial port has been defined, the + key increments the port address to a new higher numbered port, and the - key is used to decrement to a lower numbered port.

ADVANCED CONTROL

One of the advanced features of PAM-8 is its provisions allowing sophisticated users to augment or replace PAM-8's functions. Augmenting or replacing PAM-8 functions is usually done in conjunction with assembly language programs. Sometimes it is possible to implement these features by using the POKE and PEEK commands in BASIC. The sample exercise in "Appendix B" (Page 1-64) uses several PAM-8 functions, including the clock, I/O, and the audio alarm.

The following discussion refers to symbols and locations defined in the PAM-8 program listing, given in its complete form as "Appendix A." It is recommended that you review the PAM-8 listing in order to become familiar with its various features. This can be done in conjunction with reading the following section, or independently. In either case, a first overview followed by a detailed analysis of the listing is probably necessary for a complete understanding.

16-Bit Tick Counter (TICCNT)

PAM-8 maintains a 16-bit (2 byte) tick counter known as TICCNT. The value of this counter is incremented each time a clock interrupt is processed. As an interrupt occurs once every 2 mS, the counter is incremented once every 2 mS. As long as clock interrupts are not disabled, this value can be used by any program to compute elapsed time. The tick counter may be set to any desired value, but it should not be frequently reset, as this interferes with the front panel refresh cycle. The contents of the tick counter are contained in memory locations 040 033 (the least significant byte) and 040 034 (the most significant byte).

Using the Keypad

When your program is running, PAM-8 does not recognize any single key command. Thus, all single key patterns are available for your program. To read keypad patterns, you can use one of two routines. First, you may take an input from port IP. PAD; or second, your program may use PAM-8's RCK routine. The input port IP. PAD is permanently assigned to port location 360. Inputting a binary number from this port detects which of the 16 keys are depressed. These results are shown in the table on Page 1-57 of "Appendix A."

A far more sophisticated keypad routine is available to you in the RCK (read Console Keypad) routine. This is also described in "Appendix A" (see Page 1-57). RCK provides keypad decoding, keypad debounce routines, auto-repeat routines, and acoustical feedback.

NOTE: If you use two key combinations, each key must reside in a separate bank. The first bank includes keys 0-7 and the second bank includes keys 8-#. RCK cannot decode two key combinations.

Display Usage

When a user program is running, PAM-8 normally displays the contents of the selected register or memory location. However, you may disable this process and display any arbitrary segment pattern, or completely disable the display to provide greater computational through-put. The display usage is primarily controlled by setting various bits in the .MFLAG memory cell. This memory cell is found at location 040 010.

MANUAL UPDATING

By setting the UO.DDU (see "Appendix A," Page 1-25, for an explanation of the user option bits, UO.XXX) bit in the .MFLAG memory location, you can instruct PAM-8 to continue refreshing the front panel displays and to disable updating. When this is done, PAM-8 continues to refresh the LED's from a 9-byte block of RAM cells found at locations 040 013 through 040 023. A description of these front panel LED's (FPLEDS) is found in "Appendix A" (see Page 1-60). When the UO.DDU bit is set in .MFLAG, the contents of these bytes are not altered in any manner by PAM-8.

You can use this technique to display numbers, letters, or arbitrary bar patterns (see Page 1-58) on the front panel displays. For instance, your program may alter the display by inserting any value into FPLEDS. The front panel LED segments will display a decimal integer if you use the octal to 7-segment pattern (DODA) display.

MANUAL DISPLAY REFRESHING

By setting the UO.NFR (User Option.No Front Panel Refresh) bit in the .MFLAG memory cell, you can instruct PAM-8 to stop refreshing the front panel displays. Setting the UO.NFR bit does not disable the clock interrupts; therefore, the tick counter (TICCNT) is still incremented. But PAM-8 does not refresh the displays from the information contained in the FPLEDS bytes.

NOTE: If you desire, you may write a program to refresh the front panel LED displays. Usually this is done using the clock interrupts. If you undertake an independent front panel refresh program, take extreme care to avoid burning the displays due to excessive refreshing. The total power dissipated in the LEDs is determined by the refresh cycle, and too frequent refreshing will result in excessive display heating.

Using Interrupts

All H-8 interrupts cause control to be transferred into the low 64 bytes of memory. PAM-8 occupies this memory space so all interrupts are first processed by PAM-8. Except for level zero interrupts, which are used as master clears, you can supply an interrupt processing routine for each of the seven additional interrupts. The following sections explain the use of each of these interrupts.

I/O INTERRUPTS

Interrupts numbered 3 through 7 are I/O interrupts. PAM-8 does not process these interrupts in any way. When a level 3 through level 7 interrupt is received, PAM-8 immediately transfers to the user interrupt vectors contained in memory locations 040 037 through 040 064. These locations are listed in "Appendix A" (see Page 1-60). Each location must contain a jump instruction pointing to the appropriate program location which processes these interrupts.

NOTE: If any of these interrupts occur, you must supply a processing routine for them. This routine must be complete including both entry and exit processing. When you use H8 interrupts, you must use only the available vector which is 6 to insure compatability with future H8 products. You may also use 2 if you will not be using BUG-8.

CLOCK INTERRUPTS

The level one interrupts are generated by the front panel hardware every 2 mS. PAM-8 normally processes these interrupts. However, by setting a processing vector in UIVEC and setting the UO.INT bit in the MFLAG cell, PAM-8 enters the users routine each time a clock interrupt is generated. "Appendix A" (see Page 1-31) gives the required entry and exit conditions for processing clock interrupts.

SINGLE INSTRUCTION AND BREAKPOINT INTERRUPTS

Level two interrupts are generated by the single instruction hardware contained on the CPU card. When a single instruction is requested, the result of the interrupt is processed by PAM-8. If the single instruction interrupt was generated by PAM-8 in response to a Monitor Mode Single Instruction register condition, PAM-8 processes it. Otherwise, PAM-8 jumps to the user level two interrupt vector (UIVEC). Since the level two interrupt does not affect PAM-8, a level two restart instruction can be used as a breakpoint instruction by the user programs.

APPENDIX A

Panel Monitor Listing

This appendix contains a complete listing of the PAM-8 front panel monitor program. PAM-8 resides in the low 1,024 bytes of the H8 computer. It provides all the control for front panel operation, and cassette or paper tape load and dump facilities. It also provides for master clear and front panel interrupt processing. PAM-8 presumes RAM cells are available for its use in locations 040 000 through 040 077 and 80 bytes are available in high memory for a stack. The use of these RAM cells is described on Page 1-60 of this Appendix and in the memory map on Page 0-36.

Pages 1-61, 1-62, and 1-63 of this Appendix are a symbolic reference table. Use this table to find the program locations where each symbolic address is used. Symbolic addresses are listed in alphabetical sequence.

		PAM/8 - H8 FRONT FANEL MONITOR.
	:	JGL, 05/01/74,
	:	FOR *EINTER* INC.
	**	76, WINTEK
# #	* *	902 N. 9TH ST. LAFAYETTE, IND.
74 -	* *	PAM/8 - H8 FRONT PANEL MONITOR.
2.T	:	THIS PROGRAM RESIDES (IN ROH) IN THE LOW 1024 BYTES OF THE HEATH
기 대 기 기 기 기 기 기 기 기 기 기 기 기 기 기 기 기 기 기	*	ROUTINES: A TASK-TIME PROGRAM WHICH PROVIDES SOPHISTICATED
20 20 21	: :	FRONT PANEL MONITOR SERVICE, AND AN INTERRUPT-TIME PROGRAM WHICH PROVIDES BOTH A REAL-TIME CLOCK AND EMULATES AN EFFECTIVE HARDWARE FRONT FANEL,
53	* * *	INTERRUPTS.
4 10 - 0 1	:	PAM/8 IS THE PRIMARY PROCESSOR FOR ALL INTERRUPTS. THEY ARE PROCESSED AS FOLLOWS:
		RST USE
7 OW M	:	O MASTER CLEAR. (NEVER USED FOR 170 OR RST)
4 (4) (4)	:	1 CLOCK INTERRUPT, NORMALLY TAKEN BY PAM/8,
0. 전 M	• * *	USER PROCESSING (UIA A UNA THROUGH KULVECK).
36	:	CONTAINS:
700	:	(STACKE2) = (STACKPTR+14)
40	:	(STACK+6) = (BC)
44.00 A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A A C A C A A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C A C	* *	(STACK+8) = (DE) (STACK+10) = (H)
<u> </u>	:	(STACK+12) = (PC)
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46	* >	TOTAL STREET STREET STREET STREET
48	:	BY PAM/8 ARE PROCESSED BY PAM/8.
9.45. O.f.		ANY SINGLE STEP INTERRUPT RECEIVED WHEN IN
		USEK MULE CAUSES A JUMP THROUGH *UIVEC*+3.
ល M		(STACK+0) = (STACKPTR+12)
	6	

25. (STREAM) (CO. 1. (R. 1. (R		\	PAGE 2
*******		(\$IACK+6) = (DE) (STACK+8) = (HL)	
******	: :	(STACK+10) = (PC) THE USER'S ROUTINE SHOULD HANDLE ITS OW FROM THE INTERRUPT.	IN RETURN
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****** 1/0 5.	:	1/0 4.	[VEC*+9
*** 2 1/0 6.	:	5 1/0 5.	[VEC*+12
* 7 1/0 7.	:	I/0 6.	[VEC*+15]
	:	1/0 7.	[VEC*+18

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90 *** ASCII CHARACTERS. 91 A.STX EQU 0260 SYNC CHARACTER 92 A.STX EQU 0260 SYNC CHARACTER 92 A.STX EQU 0260 SYNC CHARACTER 94 A.STX EQU 020000B SINGLE STEP INTERRUPT EN 95 CB.STI EQU 0200000B CLOCK INTERRUPT EN 100 CB.STK EQU 12000000B CLOCK INTERRUPT EN 101 A.M. EQU 12000000B CLOCK INTERRUPT EN 102 ** DISPLAY HODE FLAGS (IN *DSPHOD*) 103 M.M. EQU 1 10000000B CLOCK INTERRUPT EN 104 M.M. EQU 1 10000000B CLOCK INTERRUPT 105 M.M. EQU 1 10000000B CLOCK INTERRUPT 106 M.M. EQU 1 10000000B CLOCK INTERRUPT 107 M.M. EQU 1 10000000B CLOCK INTER 110 M.M. EQU 1 10000000B CLOCK INTER 111 M.M. EQU 1 10000000B CLOCK INTER 111 M.M. EQU 1 10000000B CLOCK INTER-PRODUCT COMMUNICATION, 111 M.M. EQU 2 1000000 COMMUNICATION, 111 M.M. EQU 2 10000000 COMMUNICATION, 111 M.M. EQU 2 100000000 COMMUNICATION, 111 M.M. EQU 2 100000000 COMMUNICATION, 111 M.M. EQU 2 100000000 CM	90 *** ASCII CHARACTERS. 91 A.STX EQU 0260 SYNC CHARACTER 92 A.STX EQU 0260 SYNC CHARACTER 93 A.STX EQU 0260 SYNC CHARACTER 94 A.STX EQU 02600 STNGLE STEP INTERRUPT EN 95 CB.SSI EQU 0000000 CLOCK INTERRUPT EN 100 CB.STI EQU 0000000 SPEARE ENABLE 101 DIN HM EQU 1 10000000 SPEARE ENABLE 102 ** DISPLAY HODE FLAGS (IN *DSPHOD*) 103 DM.HM EQU 2 REGISTER METTE 104 DM.HM EQU 2 REGISTER METTE 105 DM.HM EQU 2 REGISTER METTE 106 DM.HM EQU 2 REGISTER WATTE 110X ** TAPE EQUIVALENCES. 1110X ** TAPE EQUIVALENCES. 1111X KT.HI EQU 1 RECORD TYPE - HEHOS 113X RT.BF EQU 2 RECORD	0.78.	88	OP. TPD		3700	IAPE. DATA, QUI.
92 A.STN ERU 0260 SYNC CHARACTER 95 ** FRONT PANEL HARDWARE CONTROL BITS. 96 CB.STI ERU 00010000B SINGLE STEP INTERRY 97 CB.STI ERU 00100000B CLOCK INTERRUPT EN 98 CB.STK ERU 1000000B CLOCK INTERRUPT EN 99 CB.STK ERU 1000000B CLOCK INTERRUPT EN 100 CB.SFK ERU 1000000B CLOCK INTERRUPT EN 101 CB.SFK ERU 2 CHOKY WRITE 102 M. M. ERU 2 CHOKY WRITE 103 DM. M. ERU 3 KEGISTER METE 104 DM. KW ERU 3 KEGISTER METE 105 DM. KW ERU 3 KEGISTER WRITE 110X ** TAPE ERULVALENCES. 111X T.* TAPE ERULVALENCES. 111X M. ERU 3 RECORD TYPE - MEMORY INTER-PRODUCT COMMUNICATION. 113X RT. BE ERU 3 RECORD TYPE - CHOKY INTER-PRODUCT COMMUNICATION. 113X M. ERU 3 KEGORD TYPE - CHOKY INTER-PRODUCT COMMUNICATION. 113X M. ERU 3 SIZE FOR INTER-PRODUCT COMMUNICATION. 113X M. ERU 512	92 A.STN EBU 0260 STNC CHARACTER 93 A.STX EBU 0020 STNC CHARACTER 95 ** FRONT FANEL HARDWARE CONTROL BITS. 96 CB.SSI EBU 00000000 HONITOR LIGHT 97 CB.CLI EBU 01000000 CLOCK INTERREFE ENDELE 102 ** DISPLAY MODE FLAGS (IN *DSPMOD*) 103 DM.HR EBU 0 HENDRY READ 104 DM.HR EBU 0 HENDRY READ 105 DM.HR EBU 1 TAPE EBUIVALENCES. 110X ** TAPE EBUIVALENCES. 1110X ** TAPE EBUIVALENCES. 1111X RT.HI EBU 1 RECORD TYPE - HENDR 113X RT.HE EBU 2 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 1 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 1 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 2 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 1 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 1 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 2 RECORD TYPE - DEFINITIONS 115X RT.HI EBU 2 SIZE FOR INTER-PRODUCT COMMUNICATION. 115X RT.HI EBU 512		0.6	*	SCII	: E	
92 A.STN EQU 0260 SYNC CHARACTER 93 A.STX EQU 00200 STX CHARACTER 95 ** FRONT PANEL HARDWARE CONTROL BITS. 96 CB.CLI EQU 000100000B SINGLE STEP INTERRUPT EN 97 CB.CLI EQU 00010000B SINGLE STEP INTERRUPT EN 98 CB.CLI EQU 01000000B SINGLE FRANCE LIGHT 102 ** DISPLAY HODE FLAGS (IN *DSPMOD*) 103 M.RR EQU 1 10000000B SIEARER ENABLE 104 DM.RR EQU 2 REGISTER READ 105 DM.RR EQU 2 106 DM.RR EQU 2 107 DM.RR EQU 2 108 XTEXT TAPE EQUIVALENCES. 110X ** TAPE EQUIVALENCES. 1110X ** TAPE EQUIVALENCES. 1111X RIGH 2 1111X RIGH 3 111X RIGH 60 SIZE FOR INTER-PRODUCT COMMUNICATION. 113X RIGH 60 SIZE FOR INTER-PRODUCT COMMUNICATION.	92 A.STN EQU 0020 SYNC CHARACTER 93 A.STX EQU 00010000B SINGLE STEP INTERR 96 Y CB.SSI EQU 00010000B SINGLE STEP INTERR 97 CB.STI EQU 00010000B SINGLE STEP INTERRUPT EN 98 CB.CLI EQU 00010000B SINGLE LIGHT 102 ** DISPLAY MODE FLAGS (IN *DSPMOD*) 103 DM.NR EQU 1 10000000B SFEAKE ENABLE 104 DM.NR EQU 2 NEWNORY READ 105 DM.NR EQU 2 NEWNORY READ 106 DM.NR EQU 2 NEWNORY READ 107 DM.NR EQU 3 REGISTER MEITE 108 XIEXT TAPE EQUIVALENCES, 1110X ** TAPE EQUIVALENCES, 1111X TAPE EQUIVALENCES, 1111X TAPE EQUI VALENCES, 1111X TAPE EQUI VALENCES, 1111X TAPE EQUI VALENCES, 1111X TAPE EQUI VALENCES, 1111X TAPE EQUI SIZE FOR INTER-PRODUCT COMMUNICATION, 1111X TAPE TAPE TAPE TAPE TAPE TAPE TAPE TAPE		91		:	:	***************************************
95 ** FRONT PANEL HARDWARE CONTROL BITS, 96 CB SSI EQU 00010000B SINGLE SITEF INTERR 97 CB SSI EQU 0010000B GLOCK INTERRUPT EN 98 CB CLI EQU 0100000B CLOCK INTERRUPT EN 100 CB SFR EQU 1000000B SFEAKE ENABLE 103 DM.MW EQU 1 NEBERIA NEITE 104 DM.MR EQU 2 REGISTER READ 105 DM.MW EQU 2 REGISTER READ 105 DM.MW EQU 3 REGISTER WRITE 106 DM.RW EQU 3 REGISTER WRITE 107 DM.RW EQU 3 RECORD TYPE - HEMOS 1110X ** TAPE EQUIVALENCES, 1111X ** TAPE EQUIVALENCES, 1115X RT.MI EQU 3 RECORD TYPE - HEMOS 1115X RT.MI EQU 3 RECORD TYPE - HEMOS 1115X RT.MI EQU 3 RECORD TYPE - COMPINITATIONS 1115X RT.MI EQU 3 RECORD TYPE - COMPINITATIONS 1115X RT.MI EQU 3 RECORD TYPE - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 3 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 5 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 5 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 5 RECORD TYPE - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 5 RECORD TYPE - NEW TITER - NEW TITER - COMPINITATIONS 1115X RT.BE EQU 5 RECORD TYPE - NEW TITER	95 ** FRONT PANEL HARDWARE CONTROL BITS. 96 97 CB.SSI EQU 00010000B SINGLE STEP INTERR 97 CB.STI EQU 0010000B SINGLE STEP INTERR 98 CB.FIL EQU 0100000B CLOCK INTERRUPT EN 100 CB.SPK EQU 1000000B SPEAKER ENABLE 103 MM. EQU 1 SPEAKER ENABLE 104 DM. HK EQU 0 HEMORY READ 105 DM. HW EQU 3 105 DM. HK EQU 0 HEMORY READ 106 DM. HW EQU 3 107 MM. EQU 3 108 M. STEXT TAPE EQUIVALENCES. 110X ** TAPE EQUIVALENCES. 1115X RT. HF EQU 3 115X RT. HF EQU 3 115	000,026	92	* *	EQU	026a 002a	ž×
97 CB.SSI EQU 00010000B SINGLE STEP INTERRE 98 CB.MIL EQU 00010000B CLOCK INTERRUPT EN 99 CB.CLI EQU 01000000B CLOCK INTERRUPT EN 100 CB.SFK EQU 10000000B SFEAKER ENABLE 103 DM.MR EQU 0 MEMORY READ 104 DM.MR EQU 2 REGISTER READ 105 DM.MR EQU 2 REGISTER READ 106 DM.RR EQU 2 REGISTER MRITE 107 DM.RR EQU 3 REGISTER WRITE 108 XIEXT IAPE EQUIVALENCES. TAPE DEFINITIONS 111X XT.CT EQU 3 RECORD TYPE - MEMOR 115X XT.BP EQU 3 RECORD TYPE - COMPUNICATION, 115X XT.CT EQU 3 STEEFOR INTER-PRODUCT COMPUNICATION, 115X XT.CT EQU 512	76 CB.SSI EQU 00010000B SINGLE STEP INTERN 99 CB.CLI EQU 00100000B CLOCK INTERNET EN 1000 CB.SFK EQU 1000000B CLOCK INTERNET EN 1000 CB.SFK EQU 10000000B CLOCK INTERNET EN 100 CB.SFK EQU 1000000B SFEAKER ENABLE 100 DM.MR EQU 1 MEMORY READ 100 DM.MR EQU 2 REGISTER MATTE 100 DM.RR EQU 3 REGISTER WRITE 100 DM.RR EQU 3 REGISTER WRITE 100 M.RM EQU 3 RECORD TYPE - MEMOR 1110 X XX TAPE EQUIVALENCES. 110 X XX TAPE EQUIVALENCES. 111 X KT.MI EQU 2 RECORD TYPE - MEMOR 115 X KT.MI EQU 2 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD TYPE - MEMOR 115 X KT.CT EQU 3 RECORD		56	*	FRONT		CONTROL BITS.
99 CB.MTL ERU 0010000B MONITOR LIGHT 99 CB.CLI ERU 01000000B CLOCK INTERRUPT EN 100 CB.SPK ERU 10000000B SFEAKER ENABLE 102 ** DISPLAY MODE FLAGS (IN *DSPMOD*) 103 DM.MR ERU 0 MEHORY READ 104 DM.MR ERU 2 105 DM.RW ERU 2 107 DM.RW ERU 3 107 DM.RW ERU 2 108 XTEXT TAFE TAFE DEFINITIONS 1110X ** TAPE ERUIVALENCES, 1111X X** TAPE ERUIVALENCES, 1111X X** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 1115X M. SIZE FOR INTER-PRODUCT COMMUNICATION, 1115X X** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION,	98 CB.MTL ERU 0000000B MONITOR LIGHT ERU 01000000B CLOCK INTERRUPT EN 199 CB.CLI ERU 01000000B CLOCK INTERRUPT EN 2 EAGE EN 200000B SPEAKER. ENABLE 102 bh.MR ERU 1 REGIOTER READ 105 bh.MR ERU 2 REGISTER READ 105 bh.MR ERU 2 REGISTER READ 106 bh.MR ERU 3 REGISTER MAITE 108 MAITE 100 bh.RR ERU 3 REGISTER MAITE 100 bh.RR ERU 3 RECORD TYPE - MEMONI 112X RT.HT ERU 1 AFE DETINITIONS 112X RT.HT ERU 1 RECORD TYPE - MEMONI 115X RT.HT ERU 2 RECORD TYPE - MEMONI 115X RT.HT ERU 3 RECORD TYPE - BASIC 116X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 115X RT.SIZ ERU 512	000.020	•	CB.SSI	EQU	0001000B	SINGLE STEP INTERRUPT
99 CB.CLI ERU 0100000B CLOCK INTERRUPT EN 100 CB.SPK ERU 1000000B SFEAKER ENABLE SFEAKER ENABLE 103 DM.MR ERU 1 MENGKY WAITE 104 DM.MR ERU 2 MERGKY WAITE 105 DM.KW ERU 3 REGISTER WAITE 108 XTEXT TAPE TAPE DEFINITIONS 111X ** TAPE ERUIVALENCES. TAPE DEFINITIONS 113X RT.MF ERU 1 RECORD TYPE - MENGINAL INTERPRODUCT COMMUNICATION. 115X M. BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 115X M. BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 115X M. MR ERU 512	99 CB.CLI EQU 01000000B CLOCK INTERRUPT EN 102 ** DISFLAY MODE FLAGS (IN *DSPMOD*) 103 103 104 DM.MR EQU 1 NEMORY READ 105 DM.RW EQU 3 REGISTER READ 107 DM.RW EQU 3 REGISTER READ 108 XTEXT TAFE TAFE DEFINITIONS 112X RT.MI EQU 1 RECORD TYPE - HEMOS 115X RT.BF EQU 2 RECORD TYPE - HEMOS 115X RT.BF EQU 3 RECORD TYPE - HEMOS 115X RT.BF EQU 3 RECORD TYPE - EGN 115X RT.CT EQU 3 RECORD TYPE - COMMUNICATION, 115X RT.CT EQU 3 RECORD TYPE - COMPANION 115X RT.CT EQU 3 RT	000.040	:	CB.MTL	EGU	0010000E	MONITOR LIGHT
102 ** DISPLAY MODE FLAGS (IN *DSPMOD*) 103 104 DM.MR EQU 0	102 ** DISPLAY MODE FLAGS (IN *DSPMOD*) 104	000,100	 000 66	CB.CLI CB.SPK	EQU	01000000B 100000000B	CLOCK INTERRUPT ENABLE SPEAKER ENABLE
104 DM.MR EQU 0 MEMORY READ 105 DM.ML EQU 1 MEMORY READ 106 DM.RR EQU 2 REGISTER WRITE 108 XTEXT TAPE TAPE DEFINITIONS 1110X ** TAPE EQUIVALENCES. 112X RT.MI EQU 1 RECORD TYPE - MEMORI 114X RT.CT EQU 2 RECORD TYPE - MEMORI 115X RT.CT EQU 3 RECORD TYPE - DEFINITIONS 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 118X BLNSIZ EQU 512	104 DM.MR EQU 0 MEMORY READ 105 DM.ML EQU 1 MEMORY READ 105 DM.RR EQU 2 106 DM.RR EQU 2 107 DM.RW EQU 3 107 DM.RW EQU 3 110X ** TAFE REGISTER WRITE 110X ** TAFE RECORD TYPE - MEMORY 111X RT.CT EQU 3 114X RT.CT EQU 2 115X RT.BF EQU 2 115X RT.BF EQU 2 115X RT.BF EQU 2 115X RT.BF EQU 3 115X RT.BF EQU 2 115X RT.BF EQU 3 115X RT.BF EQU 3 115X RT.BF EQU 3 115X RT.BF EQU 2 115X RT.BF EQU 3 115X RT.BF EQU 5 115X RT.BF EQU 7 115X RT.BF		102	*	DISFLA	MODE FLAGS	• ,
105 DH.MW EQU I MEMORY WRITE 106 DM.RR EQU 2 107 DM.RW EQU 2 108 XTEXT TAPE REGISTER READ 110X ** TAPE EQUIVALENCES. 1110X ** TAPE EQUIVALENCES. 1111X RT.CT EQU 2 1114X RT.CT EQU 3 115X RT.BF EQU 2 115X RT.BF EQU 3 115X RT.CT EQU 512	105 DH.MW EQU 1 MEMORY WRITE 106 DM.RR EQU 2 REGISTER READ 107 DM.RW EQU 3 REGISTER WRITE 108 XTEXT TAFE TAFE DEFINITIONS 111X X** TAFE EQUIVALENCES. 113X RT.BF EQU 2 RECORD TYPE - MEMOR 114X RT.CT EQU 3 RECORD TYPE - DEFINITIONS 115X X** BLOCK SIZE FOR INTER-FRODUCT COMMUNICATION, 118X BLNSIZ EQU 512	000.000		DM.MR	FOII		A CAN
106 DM.RR EQU 3 REGISTER READ 107 DM.RW EQU 3 REGISTER WRITE 1100 **	106 DM.RR EQU 3 REGISTER READ 107 DM.RW EQU 3 REGISTER WRITE 108 XTEXT TAPE TAPE DEFINITIONS 1110X ** TAPE EQUIVALENCES. TAPE DEFINITIONS 1111X RT.RF EQU 3 RECORD TYPE - MEMOR 1114X RT.CT EQU 3 RECORD TYPE - BASIC 1116X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 1118X BLNSIZ EQU 512	000.001	:	IN. ME	ÉGÜ		MEMORY WRITE
107 DM.RW EQU 3 108 XTEXT TAPE TAPE DEFINITIONS 110X ** TAPE EQUIVALENCES. 111X RT.H EQU 1 RECORD TYPE - HEMOF 113X RT.BF EQU 2 RECORD TYPE - BASIC 114X RT.CT EQU 3 RECORD TYPE - COMPANIONICATION. 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 118X BLKSIZ EQU 512	107 DM.RW EQU 3 108 XTEXT TAPE TAPE DEFINITIONS 110X ** TAPE EQUIVALENCES. 112X RT.BF EQU 2 113X RT.BF EQU 2 114X RT.CT EQU 3 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 118X BLNSIZ EQU 512	NO0*000	:	DM . R.R.	EQU	N	REGISTER READ
110X ** TAPE EQUIVALENCES. 112X RT.HI EQU 1 113X RT.BF EQU 2 114X RT.CT EQU 3 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 118X BLKSIZ EQU 512	110X ** TAPE EQUIVALENCES. 111X RT.MI EQU 1 RECORD TYPE - MEMON 1 13X RT.BP EQU 2 RECORD TYPE - BASIC 115X RT.CT EQU 3 RECORD TYPE - COMPANION 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION. 118X BLKSIZ EQU 512	000°000	: :	DM.RE	EQU	3 TAPE	REGISTER WRITE TAFE DEFINITIONS
111X KT.HI EQU 1 RECORD TYPE - WEMON 113X RT.BF EQU 2 RECORD TYPE - BASIC 114X RT.CT EQU 3 RECORD TYPE - COMPANION 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 118X BLKSIZ EQU 512	111X KT.MI EQU 1 RECORD TYPE - MEMON 1 13X KT.BP EQU 2 114X KT.CT EQU 3 RECORD TYPE - BASIC 115X KT.CT EQU 3 RECORD TYPE - COMPILEX ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 118X BLKSIZ EQU 512		110X	*	APE	UIVALENCES.	
112X KM1 ERU 1 RECORD TYPE - MEMOR 1 114X RT.BP ERU 2 RECORD TYPE - BASIC 114X RT.CT ERU 3 RECORD TYPE - BASIC 116X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 117X 118X BLKSIZ ERU 512	112X N. M.1 ERU 1 RECORD TYPE – MEMOR 114X RT.BP ERU 2 RECORD TYPE – MEMOR 114X RT.CT ERU 3 RECORD TYPE – BASIC 115X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 118X BLKSIZ ERU 512		XIII		:		
114X RT.CT EQU 3 RECORD TYPE COMPONICATION, 116X ** BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 118X BLKSIZ EQU 512	114X RT.CT EQU 3 RECORD TYPE COMPONICATION, 115X BLOCK SIZE FOR INTER-PRODUCT COMMUNICATION, 118X BLKSIZ EQU 512	000,000	J (E GC	~ ; (TYPE - MEMOR
115X ** BLOCK SIZE 117X 118X BLKSIZ EQU 51 119X	116X ** BLOCK SIZE 117X 118X BLKSIZ EQU 512 119X			ŔŢ.ĊŢ	EQU	V.PO	TYPE - COMPR
11/X 118X BLKSIZ EQU 51 11/8%.	117X 118X BLKSIZ EQU 51 119%		J:-	*	BLOCK S	IZE FOR INTER-P	
118X BLKSIZ EQU 51.18X BLKSIZ EQU 51.	118X BLKSIZ EQU 51.419%.	****	ું.				
		000.500	ن ن	BLKSIZ	EQU	512	

000.166 000.311 000.333 000.323 000.523				15:44:56 01-AFK-77 FROE
000.166 000.311 000.333 000.323	121 **		MACHINE INSTRUCTIONS.	
000,311 000,333 000,323 000,072	123 MI.H	T ERU	01110110B	HALT
000,333 000,323 000,072	124 MI.R	T EQU	11001001E	RETURN
000,323 000,072 000,344	125 MI.I	D E	11011011B	INFUL
000.072	126 MI+0		11010118	UUITUI
772 000	127 MI+L	D E E E E	0017100	
000.021	129 MI.LXID	XID EQU	00010001B	LXI b
	;			
	131 **	USER	USER OFTION BITS.	
	133 ×	THES	THESE BITS ARE SET IN	CELL .WFLAG.
	•			TICABLE UNIT BOOTESTING
000.200			100000008	DIODRICH TROUBLE TANGEN TO THE TANGEN THE TANGEN TO THE TANGEN TO THE TANGEN THE TAN
000 100	137 UD: DDU	FOUR EQU.	00000010B	DISABLE DISPLAY UPDATE
000,001	•	:	0000001E	ALLOW CLOCK INTERRUPT PROCESSING
000.000	140	XTEXT	T U8251	DEFINE 8251 USART BITS

143X ## 8251 USART BIT DEFINITIONS. 144X #	143X ** 8251 USART BIT DEFINITION 144X # 145X #	1433 ** 8251 USART BIT DEFINITION 1442 ** 4654 1453 1465 ** 400 01000000B 1473 1482 UM1.1B EQU 1000000B 1474 1482 UM1.1B EQU 10000000B 1503 UM1.PE EQU 1000000B 1513 UM1.PE EQU 0000000B 1524 UM1.PE EQU 0000000B 1535 UM1.PE EQU 0000000B 1554 UM1.PE EQU 0000000B 1555 UM1.PE EQU 0000000B 1555 UM1.PE EQU 0000000B 1554 UM1.RE EQU 0000000B 1555 UM1.RE EQU 0000000B 1564 UM1.AE EQU 0000000B 1565 UM1.AE EQU 0000000B 1565 UM1.AE EQU 0000000B 1573 UM1.AE EQU 0000000B 1585 UM1.AE EQU 0000000B 1585 UM1.AE EQU 0000000B 1587 UM1.AE EQU 0000000B 1587 UM1.AE EQU 0000000B 1588 UM1.AE EQU 0000000B 1588 UM1.AE EQU 0000000B 1573 USR.FE EQU 0000000B 1731 USR.FE EQU 0000000B 1732 USR.FE EQU 0000000B 1734 USR.FE EQU 00000000B 1735 USR.FE EQU 0000000B 1735 USR.FE EQU 0000000B 1735 USR.FE EQU 0000000B 1735 USR.FE EQU 00000000B 1735 USR.FE EQU 0000000B 1735 USR.FE EQU 00000000B 1735 USR.FE EQU 00000000B	144 X * 144 X * 145 X ** 146 X ** 146 X ** 146 X ** 147 Unit is equ 01000008 148 X Unit is equ 01000008 151 X Unit is equ 000100008 152 X Unit is equ 000100008 153 X Unit is equ 00010008 153 X Unit is equ 00010008 155 X Unit is equ 00000008 158 X Unit is equ 00000008 158 X Unit is equ 00000008 159 X Unit is equ 00000008 159 X Unit is equ 00000008 150 X Unit is equ 000000018 170 X USR IFE Equ 0000000018 175 X USR IFE Equ 0000000018 175 X USR IFE Equ 0000000008 175 X USR IFE Equ 0000000008 175 X USR IFE Equ 0000000008 175 X USR IFE Equ 0000000000000000000000000000000000	143. ** 144. * 145. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 146. ** 147. ** 148. ** 148. ** 159. ** 151. ** 153. ** 153. ** 161. ** 153. ** 154. ** 153. ** 154. ** 155. ** 157. ** 156. ** 156. ** 157. ** 157. ** 158. ** 158. ** 161. ** 162. ** 163. ** 163. ** 163. ** 164. ** 165. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167. ** 167.	144X # 8251 USART BIT DEFINITION 144X # HUDE INSTRUCTION CONTROL 145X HIT BE END 0100000B 145X UNIT.BE END 1100000B 155X UNIT.BE END 000000B 155X UNIT.BE END 0000000B 155X UNIT.BE UN				BIT	
144X # 145X ** HODE INSTRUCTION CONTROL 147X 148X UNI.1B EQU 01000000B 151X UNI.PE EQU 1000000B 152X UNI.PE EQU 0000000B 153X UNI.PE EQU 0000000B 153X UNI.LS EQU 00001000B 154X UNI.LS EQU 0000100B 155X UNI.LS EQU 00001000B 155X UNI.LS EQU 00001000B 155X UNI.LS EQU 00001000B 155X UNI.LS EQU 0000010B 155X UNI.AX EQU 0000001B 155X UNI.AX EQU 0000001B 155X UNI.AX EQU 0000000B 155X UNI.AX EQU 0000000B 155X UNI.ER EQU 0000000B 175X UNI.ER EQU 00000000B 175X UNI.ER EQU 00000000B	144X # 145X ## HODE INSTRUCTION CONTROL 145X UNI.1B EQU 01000000B 149X UNI.1B EQU 1000000B 150X UNI.2B EQU 1000000B 151X UNI.PA EQU 0000000B 153X UNI.PE EQU 0000000B 154X UNI.LS EQU 0000000B 155X UNI.S EQU 0000000B 175X USR.FE EQU 0000000B	1444 ** 1465 ** HODE INSTRUCTION CONTROL 148X UM1.1B EQU 1000000B 150X UM1.PE EQU 1000000B 151X UM1.PE EQU 0001000B 152X UM1.PE EQU 0001000B 153X UM1.LE EQU 0000100B 154X UM1.LE EQU 0000100B 155X UM1.LE EQU 0000010B 155X UM1.AAX EQU 0000011B 155X UM1.AAX EQU 00000011B 160X W1. EQU 0000001B 160X W1. EQU 0000001B 160X W1. EQU 0000000B 160X W1. EQU 0000000B 160X W1. EQU 0000000B 160X W1. EQU 0000000B 170X W1. EQU 0000000B	144X ** 146X ** HODE INSTRUCTION CONTROL 148X UNIT. IB EQU 10000000B 150X UNIT. PE EQU 01000000B 151X UNIT. PE EQU 00100000B 153X UNIT. PE EQU 0000000B 153X UNIT. PE EQU 0000000B 154X UNIT. PE EQU 0000000B 155X UNIT. PE EQU 00000010B 155X UNIT. PE EQU 00000001B 155X UNIT. PE EQU 00000001B 155X UNIT. PE EQU 00000001B 175X USR. PE EQU 000000001B 175X USR. PE EQU 000000001B	144X ** 146X *** NUME INSTRUCTION CONTROL 148X UNIT IB EQU	144X ** 146X ** HODE INSTRUCTION CONTROL 148X UHT IB ERU 1000000B 150X UHT IB ERU 1000000B 151X UHT IF ERU 0100000B 153X UHT I.E ERU 000000B 155X UHT I.E ERU 0000000B 175X USR FE ERU 0000000B					ITIONS.
146X ** MODE INSTRUCTION CONTROL 147X UMI.1B EQU 1000000B 149X UMI.1B EQU 11000000B 150X UMI.2B EQU 11000000B 151X UMI.PE EQU 00010000B 152X UMI.PE EQU 00001000B 153X UMI.LS EQU 00001000B 155X UMI.LS EQU 00001000B 155X UMI.LB EQU 00001100B 155X UMI.LB EQU 00001100B 155X UMI.LB EQU 00001100B 155X UMI.AA EQU 0000010B 155X UMI.AA EQU 00000011B 158X UMI.AA EQU 00000001B 158X UCI.FE EQU 00000001B 158X UCI.FE EQU 00000001B 158X UCI.FE EQU 00000001B 170X ** STATUS READ COMMAND ITX 171X USR.FE EQU 0001000B 173X USR.FE EQU 0000100B 173X USR.FE EQU 00000100B 175X USR.FE EQU 00000100B 175X USR.FE EQU 00000001B	146X ** MODE INSTRUCTION CONTROL 147X UMI.1B EQU 10.000000B 149X UMI.1B EQU 10.000000B 150X UMI.2B EQU 11000000B 151X UMI.PE EQU 00.000000B 152X UMI.PE EQU 00.000000B 153X UMI.LS EQU 00.000000B 155X UMI.LS EQU 00.0000100B 155X UMI.LB EQU 00.0000100B 155X UMI.LB EQU 00.0000100B 155X UMI.LB EQU 00.000010B 155X UMI.LB EQU 00.000010B 155X UMI.AX EQU 00.000011B 160X UMI.AX EQU 00.000001B 163X UMI.AX EQU 00.000001B 164X UMI.AX EQU 00.000001B 165X UMI.AX EQU 00.000001B 170X ** STATUS READ COMMAND ITS 170X ** STATUS READ COMMAND ITS 170X WAN USR.FE EQU 00.0000010B 175X USR.FE EQU 00.0000010B 175X USR.FX EQU 00.0000010B	146X ** HODE INSTRUCTION CONTROL 147X UMI.1B EQU 10000006B 149X UMI.1B EQU 110000006B 150X UMI.2B EQU 110000006B 155X UMI.1C EQU 00010000B 155X UMI.1C EQU 00001000B 155X UMI.1C EQU 00001000B 155X UMI.1C EQU 00001000B 155X UMI.1C EQU 00001100B 155X UMI.1A EQU 00000100B 155X UMI.1A EQU 00000100B 155X UMI.1A EQU 00000100B 155X UMI.1A EQU 00000100B 155X UMI.6A EQU 00000010B 155X UMI.6A EQU 00000010B 155X UMI.6A EQU 00000001B 155X UMI.6A EQU 00000000B 175X USR.FE EQU 00000000B	146X *** HODE INSTRUCTION CONTROL 148X UMI.1B EQU 01000000B 150X UMI.HB EQU 1000000B 151X UMI.FE EQU 0100000B 153X UMI.FE EQU 0000000B 153X UMI.LS EQU 000000B 153X UMI.LS EQU 000000B 155X UMI.S EQU 000000B 155X UMI.E EQU 000000B 175X USR.FE EQU 0000000B	146X ## HODE INSTRUCTION CONTROL 148X UHI 1B EQU 01000000B 159X UHI 1B EQU 1000000B 151X UHI 1E EQU 0100000B 153X UHI 1E EQU 00010000B 153X UHI 1. E EQU 000000B 155X UHI 1. E EQU 0000000B 155X UHI 1. E EQU 0000000B 155X UHI 1. E EQU 0000000B 155X UHI 1. E EQU 00000010B 155X UHI 1. E EQU 0000000B 155X UHI 1. E EQU 000	146X *** HODE TNSTRUCTION CONTROL 148X UHT.1B EQU 01000000B 148X UHT.1B EQU 1000000B 153X UHT.1B EQU 0100000B 153X UHT.1C EQU 0100000B 153X UHT.1C EQU 0000000B 154X UHT.1C EQU 0000000B 155X UHT.1X EQU 0000000B 155X UHT.1 EQU 00000000B 155X UHT.1 EQU 0000000B 155X UHT.1 EQU 00000000B 155X UHT.1 EQU 0000000B 155X UHT.1 EQU 00000000B					
147X UNI.1B EQU 01000000B 149X UNI.1B EQU 11000000B 150X UNI.2B EQU 11000000B 151X UNI.PE EQU 00100000B 153X UNI.LS EQU 00000000B 155X UNI.LS EQU 00000100B 155X UNI.LS EQU 00000100B 155X UNI.LS EQU 0000010B 155X UNI.LS EQU 0000011B 158X UNI.1X EQU 00000011B 158X UNI.1X EQU 00000011B 158X UNI.64X EQU 00000011B 165X UCI.RE EQU 00000011B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000000B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000000B 165X UCI.RE EQU 00000000B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000000B 177X USR.FE EQU 0000100B 177X USR.FE EQU 00000000B 175X USR.FXE EQU 00000000B	149 X UNI.1B EQU 01000000B 149 X UNI.1B EQU 10000000B 150 X UNI.2B EQU 11000000B 152 X UNI.1F EQU 00100000B 153 X UNI.1F EQU 00001000B 153 X UNI.1F EQU 00001000B 155 X UNI.1 EQU 00001000B 155 X UNI.1 EQU 0000010B 158 X UNI.1 EQU 00000011B 150 X W EQU 00100000B 164 X UCI.FE EQU 00100000B 165 X UCI.FE EQU 00000010B 165 X UCI.FE EQU 00000010B 165 X UCI.FE EQU 00000000B 165 X UCI.FE EQU 00000000B 167 X UCI.FE EQU 00000000B 168 X UCI.FE EQU 00000000B 172 X USF.FE EQU 00000000B 172 X USF.FE EQU 00000000B 173 X USF.FE EQU 00000000B 176 X USF.FE EQU 00000000B 176 X USF.FE EQU 00000000B 176 X USF.FE EQU 00000000B	147X UMI.1B EQU 01000000B 149X UMI.HB EQU 10000000B 150X UMI.PE EQU 11000000B 151X UMI.PE EQU 00100000B 153X UMI.LG EQU 00001000B 155X UMI.LG EQU 00001100B 155X UMI.LY EQU 00001100B 155X UMI.1X EQU 00001100B 155X UMI.1X EQU 0000110B 155X UMI.1X EQU 0000001B 155X UMI.1X EQU 0000001B 155X UMI.1X EQU 0000001B 155X UMI.64X EQU 00100000B 155X UCI.RE EQU 00100000B 165X UCI.RE EQU 0010000B 165X UCI.RE EQU 00100000B 172X USR.FE EQU 0001000B 172X USR.TXE EQU 00000100B 172X USR.TXE EQU 00000010B 172X USR.TXE EQU 00000010B	149X UMI 1B EGU 01000000B 149X UMI 1B EQU 1000000B 151X UMI 1F EQU 000000B 153X UMI 1F EQU 000000B 154X UMI 1.5 EQU 000000B 155X UMI 1.6 EQU 000000B 155X UMI 1.7 EQU 000000B 155X UMI 1.8 EQU 000000B 155X UMI 1.8 EQU 000000B 155X UMI 1.8 EQU 000000B 155X UMI 1.6 EQU 000000B 155X UMI 1.8 EQU 000000B 175X USR.FE EQU 0000000B	149X UMI 1B EGU 01000000B 149X UMI 1B EGU 1000000B 151X UMI 1E EGU 1000000B 152X UMI 1E EGU 0000000B 152X UMI 1E EGU 0000000B 153X UMI 1E EGU 0000000B 155X UMI 1E EGU 0000100B 155X UMI 1X EGU 000000B 155X UMI 1X EGU 000000B 155X UMI 64X EGU 000000B 155X UMI 64X EGU 000000B 165X UCI 1E EGU 000000B 175X USR FE EGU 0000000B 175X USR FE EGU 0000000B 175X USR FIXE EGU 0000000B	148X UHI.1B EQU 01000000B 149X UHI.HB EQU 1000000B 151X UHI.FE EQU 0100000B 153X UHI.FE EQU 000000B 153X UHI.LS EQU 000000B 153X UHI.LS EQU 000000B 155X UHI.LS EQU 000000B 155X UHI.LS EQU 000000B 155X UHI.LS EQU 0000001B 155X UHI.LS EQU 0000001B 155X UHI.LE EQU 0000001B 155X UHI.LE EQU 0000001B 155X UHI.LE EQU 000000B 155X UHI.LE EQU 000000B 155X UHI.LE EQU 000000B 155X UCI.RE EQU 000000B 175X USR.RE EQU 00000010B 175X USR.RE EQU 00000010B 175X USR.RE EQU 0000010B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000010B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B 175X USR.RE EQU 00000100B			MODE I	NSTRUCTION CONT	TROL BITS.
149X UMI.HB EQU 10000006 150X UMI.2B EQU 110000006 150X UMI.PE EQU 001000006 155X UMI.PE EQU 000000006 155X UMI.PE EQU 000000000 155X UMI.LS EQU 00000000 155X UMI.LS EQU 00001000 155X UMI.IX EQU 00000010 165X UMI.REQU 00000000 165X UMI.RE EQU 00010000 173X USR.RE EQU 00010000 173X USR.RE EQU 000000000 165X USR.REQU 000000000 165X USR.REQU 000000000 165X USR.REQU 0000000000 165X USR.REQU 0000000000 165X USR.REQU 00000000000 165X USR.REQU 00000000000 165X USR.REQU 000000000000000000000000000000000000	149X UMI.HB EQU 10000006 150X UMI.2B EQU 110000006 150X UMI.PE EQU 010000006 151X UMI.PE EQU 0001000000 0001000000 153X UMI.LS EQU 000000000 155X UMI.LS EQU 00001000 155X UMI.LY EQU 000010000 155X UMI.LY EQU 00000010 155X UMI.IX EQU 00000010 155X UMI.IX EQU 00000010 155X UMI.IX EQU 00000010 155X UMI.IX EQU 00000010 161X ** COMMAND INSTRUCTION BITS 162X UCI.FE EQU 000100000 165X UCI.FE EQU 000100000 173X USF.FE EQU 000100000 173X USF.FE EQU 000100000 173X USF.FE EQU 000100000 174X USF.FE EQU 000010000 174X USF.FE EQU 000000100 175X USF.FE EQU 0000000100 175X USF.FE EQU 000000100 175X USF.FE EQU 000000100 175X USF.FE EQU 000000100 175X USF.FE EQU 000000100 175X USF.FE EQU 0000000100 175X USF.FE EQU 000000000 175X USF.FE EQU 0000000000000000000000000000000000	149% UMI, HB EQU 10000006 150% UMI, 2B EQU 110000006 151% UMI, PE EQU 001000006 152% UMI, PE EQU 000000000 001000000 153% UMI, LS EQU 00000000 154% UMI, LS EQU 00000100 155% UMI, LS EQU 00000100 155% UMI, LS EQU 00000010 165% UMI, LS EQU 00000010 165% UMI, LS EQU 00100000 165% UMI, LS EQU 00100000 165% UMI, LE EQU 001000000 165% UMI, LE EQU 001000000 165% UMI, LS EQU 000000100 165% UMI, LS EQU 00000010 165% UMI, LS EQU 000000000 165% UMI, LS EQU 000000000 170% WMI, LS EQU 000000000 170% USE, FE EQU 00010000 170% USE, FE EQU 00001000 170% USE, FE EQU 000000000 170% USE, FE EQU 0000000000 170% USE, FE EQU 0000000000 170% USE, FE EQU 0000000000 170% USE, FE EQU 00000000000 170% USE, FE EQU 0000000000000000000000000000000000	149% UMI_HB_EQU 10000008 150. UMI_PE_EQU 01000008 153. UMI_PE_EQU 00000008 153. UMI_LE_EQU 00000008 154. UMI_LE_EQU 00000008 155. UMI_LE_EQU 00001008 155. UMI_LE_EQU 00000008 156. UMI_LE_EQU 000000008 158. UMI_LE_EQU 000000000000000000000000000000000000	149% UMI HB EQU 11000000B 150.X UMI PE EQU 0100000B 152.X UMI PE EQU 0000000B 152.X UMI LG EQU 0000000B 153.X UMI LG EQU 00001000B 155.X UMI LZ EQU 0000100B 155.X UMI LZ EQU 000000B 156.X UMI LA EQU 000000B 158.X UMI LA EQU 000000B 178.X USA FE EQU 0000000B 178.X USA FE EQU 0000000B 178.X USA FE EQU 0000000B 178.X USA FIXE EQU 0000000B	149% UMI HB EQU 10000008 150. WHI FE EQU 00000008 153. WHI FE EQU 00000008 153. WHI LS EQU 0000000B 154. WHI LS EQU 0000000B 155. WHI LS EQU 0000000B 155. WHI LS EQU 00000010B 155. WHI LS EQU 00000010B 155. WHI LS EQU 0000001B 155. WHI LS EQU 0000000B 155. WILL E EQU 0000001B	000,100	14/X 148X UMI.1B	Eau	010000008	STOP
150x UMI.2B EQU 11000000B 151x UMI.PE EQU 00100000B 152x UMI.PE EQU 00010000B 153x UMI.LS EQU 0000100B 155x UMI.LS EQU 00001100B 155x UMI.LS EQU 00001100B 155x UMI.1X EQU 0000011B 158x UMI.1X EQU 0000011B 158x UMI.1X EQU 0000001B 159x UMI.64x EQU 0000001B 160x ** COMMAND INSTRUCTION BITS 161x ** COMMAND INSTRUCTION BITS 165x UCI.RE EQU 00100000B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 0000000B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000010B 170x ** STATUS READ COMMAND BITS 170x USR.FE EQU 0001000B 170x USR.FE EQU 0001000B 170x USR.FE EQU 0001000B 170x USR.FE EQU 0000100B 170x USR.FE EQU 0000100B 170x USR.FE EQU 0000010B	150x UMI.2B EQU 11000000B 151x UMI.PE EQU 0010000B 152x UMI.PE EQU 0001000B 153x UMI.L6 EQU 0000100B 155x UMI.L7 EQU 0000100B 155x UMI.L7 EQU 0000100B 155x UMI.18 EQU 000001B 158x UMI.18 EQU 0000001B 158x UMI.16x EQU 0000001B 159x UMI.16x EQU 0000001B 160x UMI.18 EQU 0000001B 165x UMI.18 EQU 0000001B 165x UCI.RE EQU 00100000B 165x UCI.RE EQU 00100000B 165x UCI.RE EQU 00100000B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000010B 165x UCI.RE EQU 00000000B 165x UCI.RE EQU 00000000B 165x UCI.RE EQU 00000000B 170x WX STATUS REAU CDMMAND BITS 170x WS.FE EQU 00000000B 170x USR.FE EQU 00000000B	150x UMI.2B EQU 11000000B 151x UMI.PE EQU 00100000B 152x UMI.LS EQU 0000000B 153x UMI.LS EQU 0000100B 155x UMI.LS EQU 0000100B 155x UMI.LS EQU 000010B 155x UMI.1X EQU 000010B 155x UMI.1X EQU 0000001B 155x UMI.1X EQU 0000001B 155x UMI.1X EQU 0000001B 155x UMI.1X EQU 0000001B 165x UMI.1X EQU 0000001B 165x UMI.1X EQU 0010000B 172x USR.FE EQU 0001000B 173x USR.FE EQU 0001000B 173x USR.FE EQU 00000100B 175x USR.FE EQU 00000100B 175x USR.FE EQU 00000100B 175x USR.FE EQU 00000100B 175x USR.FE EQU 00000100B	150x UM1 2B EGU 11000000B 152X UM1.PA EGU 0001000B 153X UM1.L5 EGU 0000100B 153X UM1.L5 EGU 0000100B 153X UM1.L7 EGU 0000100B 155X UM1.L7 EGU 0000100B 155X UM1.L7 EGU 000010B 158X UM1. 64X EGU 0000001B 163X UM1. 64X EGU 000000B 163X UM1. 64X EGU 000000B 163X UM1. 64X EGU 0000000B 163X UM1. 64X EGU 0000000B 163X UM1.FE EGU 0000000B 163X UM1.FE EGU 0000000B 163X UM1.FE EGU 0000000B 173X UM1.FE EGU 00000000B 173X UM1.FE EGU 000000000B 173	150X UMI 2B EGU 11000000B 152X UMI 1F EGU 0001000B 153X UMI 1.5 EGU 0000100B 155X UMI 1.7 EGU 0000100B 155X UMI 1.7 EGU 0000001B 155X UMI 1.8 EGU 0000001B 155X UMI 1.8 EGU 0000000B 175X UMI 1.8 EGU 0000000B 175X UMI 1.8 EGU 00000001B	150X UMI 2B EGU 11000000B 152X UMI 15A EGU 0001000B 153X UMI 1.5 EGU 0000100B 155X UMI 1.7 EGU 0000100B 155X UMI 1.7 EGU 0000011B 155X UMI 1.1 K EGU 0000001B 155X UMI 1.1 K EGU 0000001B 155X UMI 1.1 K EGU 0000001B 155X UMI 64X EGU 0000001B 155X UMI 64X EGU 0000001B 155X UMI 64X EGU 0000001B 165X UCI K EGU 0010000B 165X UCI K EGU 0010000B 165X UCI K EGU 0010000B 165X UCI K EGU 0000000B 175X USR F E EGU 0000000B	000,200	149X UMI.HB	EQU	100000001	II
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159X UMI.64X EQU 00000011B 160X 161X ** CDMMAND INSTRUCTION BITS 163X UCI.RC EQU 00100000B 164X UCI.RC EQU 00100000B 165X UCI.RC EQU 0001000B 168X UCI.RC EQU 0000010B 168X UCI.RC EQU 0000010B 169X UCI.TC EQU 00000010B 170X USR.FC EQU 0001000B 171X USR.FC EQU 0001000B 173X USR.PC EQU 0001000B 173X USR.PC EQU 0001000B 173X USR.PC EQU 0001000B 175X USR.FXE EQU 0000100B 175X USR.FXE EQU 0000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000100B	159X UMI.64X EQU 00000011B 160X 161X ** CDMMAND INSTRUCTION BITS 163X UCI.IR EQU 00100000B 165X UCI.RD EQU 0010000B 165X UCI.RE EQU 0001000B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 170X Y* STATUS READ CDMMAND BITS 170X Y* STATUS REQU 0010000B 173X USR.FE EQU 0001000B 173X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FE EQU 0000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000010B	159x UHI.64x EQU 00000011B 161x 161x 163x 163x 163x 164x UCI.RG EQU 00100000B 164x UCI.RG EQU 0010000B 165x UCI.RE EQU 0001000B 165x UCI.RE EQU 0000010B 165x UCI.RE EQU 00000010B 169x 170x ## STATUS READ CDMMAND BITS 171x 171x 172x USR.FE EQU 0010000B 173x USR.FE EQU 0001000B 173x USR.FX EQU 000100B 175x USR.FX EQU 00000010B 175x USR.FX EQU 00000010B 175x USR.FX EQU 00000010B	159X UMT.64X EQU 00000011B 161X ** COMMAND INSTRUCTION BITS 162X UCI.RC EQU 0100000B 164X UCI.RC EQU 0000000B 165X UCI.RC EQU 0000000B 165X UCI.RC EQU 0000010B 165X UCI.RC EQU 00000010B 170X ** STATUS READ COMMAND BITS 170X ** STATUS READ COMMAND BITS 170X USR.FC EQU 0000000B 172X USR.FC EQU 0001000B 173X USR.FC EQU 0000100B 175X USR.FC EQU 0000010B 175X USR.FC EQU 0000010B 175X USR.FC EQU 0000010B 175X USR.FC EQU 00000010B 175X USR.FC EQU 0000010B 175X USR.FC EQU 00000010B 175X USR.FC EQU 00000010B	159X UMT.64X EQU 00000011B 161X ** COMMAND INSTRUCTION BITS 162X UCT.RC EQU 0010000B 164X UCT.RC EQU 0010000B 164X UCT.RC EQU 0010000B 164X UCT.RC EQU 0000010B 165X UCT.RC EQU 00000010B 165X UCT.RC EQU 00000001B 175X USR.FC EQU 0000100B 175X USR.FC EQU 0000100B 175X USR.FC EQU 0000100B 175X USR.FC EQU 0000100B 175X USR.FC EQU 00000010B 175X USR.FC EQU 00000010B 175X USR.FC EQU 0000001B 175X USR.FC EQU 00000001B	159x UMI.64x EQU 00000011B 160x 161x 161x 162x 162x 161 160 163x UCI.FR EQU 00100000B 165x UCI.FR EQU 0000000B 175x USR.FF EQU 0001000B 175x USR.FF EQU 0001000B 175x USR.FF EQU 0000000B 175x USR.FF EQU 0000000B 175x USR.FF EQU 0000000B 175x USR.FF EQU 00000000B	000.002	IIMT 16X	FOR	000000108	`:^
160X 161X ** COMMAND INSTRUCTION BITS 162X 164X UCI.IR EQU 00100000B 165X UCI.ER EQU 0010000B 165X UCI.ER EQU 0010000B 165X UCI.ER EQU 0001000B 165X UCI.ER EQU 0001000B 165X UCI.ER EQU 0000001B 165X UCI.ER EQU 0000001B 170X USI.FE EQU 0001000B 171X USIR.FE EQU 0001000B 172X USIR.PE EQU 0001000B 173X USIR.PE EQU 0001000B 175X USIR.PE EQU 0001000B 175X USIR.FE EQU 0000100B 175X USIR.FE EQU 0000100B 175X USIR.FX EQU 00000100B 175X USIR.FX EQU 0000010B	160X 161X ** COMMAND INSTRUCTION BITS 163X UCI.IR EQU 0100000B 164X UCI.RO EQU 0010000B 165X UCI.ER EQU 0001000B 165X UCI.ER EQU 0000010B 165X UCI.RE EQU 0000010B 165X UCI.RE EQU 0000010B 165X UCI.RE EQU 0000001B 165X UCI.RE EQU 0000001B 170X ** STATUS READ COMMAND BITS 170X ** STATUS READ 001000B 173X USR.FE EQU 0001000B 173X USR.PE EQU 0001000B 173X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.TXE EQU 0000010B 175X USR.TXE EQU 00000010B	160X 161X ** COMMAND INSTRUCTION BITS 165X UCI.IR EQU 00100000B 164X UCI.RO EQU 0010000B 165X UCI.RO EQU 0010000B 165X UCI.RO EQU 0001000B 165X UCI.IE EQU 00000010B 169X UCI.IE EQU 00000010B 169X Y STATUS READ CUMMAND BITS 170X ** STATUS READ CUMMAND BITS 171X USR.FE EQU 0010000B 173X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FE EQU 000000100B 175X USR.FE EQU 00000001B	161X ** CDMMAND INSTRUCTION BITS 162X UCI.IR EQU 00100000B 164X UCI.RC EQU 0010000B 165X UCI.RC EQU 0000001B 165X UCI.RC EQU 0000001B 168X UCI.RC EQU 0000001B 169X ** STATUS READ CDMMAND BITS 170X ** STATUS READ CDMMAND BITS 170X USR.FC EQU 001000B 170X USR.FC EQU 0001000B 170X USR.FC EQU 0000001B 170X USR.FC EQU 0000001B 170X USR.FC EQU 00000010B 170X USR.FX EQU 00000010B 170X USR.FX EQU 00000010B 170X USR.FX EQU 00000010B	160X ** CDMMAND INSTRUCTION BITS 163.2. UCI.IR EQU 001000000B 164.X UCI.RC EQU 00100000B 165.X UCI.RC EQU 0010000B 165.X UCI.RC EQU 00000100B 168.X UCI.RC EQU 0000000B 172.X USR.FC EQU 00010000B 173.X USR.FC EQU 00010000B 173.X USR.FC EQU 00010000B 174.X USR.FC EQU 00001000B 175.X USR.FC EQU 00001000B 175.X USR.FC EQU 00000100B 175.X USR.TXE EQU 00000010B 175.X USR.TXE EQU 00000010B 177.X USR.TXE EQU 00000010B 177.X USR.TXE EQU 00000010B	160X ** COMMAND INSTRUCTION BITS 163X UCI.IR EQU 0100000B 164X UCI.RO EQU 0010000B 165X UCI.RE EQU 0010000B 165X UCI.RE EQU 0000010B 165X UCI.RE EQU 0000010B 169X ** STATUS READ COMMAND BITS 170X ** STATUS READ COMMAND BITS 171X USR.FE EQU 0000000B 173X USR.FE EQU 0001000B 175X USR.FE EQU 0000100B 175X USR.FE EQU 0000100B 175X USR.FE EQU 0000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000010B 175X USR.FXE EQU 00000010B	£00°000	IMT. 64X	F0:1	000000118	•
161X ** COMMAND INSTRUCTION BITS 162X 162X 164X UCI.IR EQU 00100000B 165X UCI.ER EQU 0001000B 165X UCI.ER EQU 0001000B 165X UCI.ER EQU 000100B 165X UCI.IE EQU 0000010B 165X UCI.IE EQU 0000001B 165X UCI.IE EQU 0000001B 165X UCI.IE EQU 0000001B 170X ** STATUS READ COMMAND BITS 171X USR.FE EQU 0001000B 173X USR.BE EQU 0001000B 173X USR.BE EQU 0001000B 175X USR.FE EQU 0000100B 175X USR.FXE EQU 0000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000100B	161X ** COMMAND INSTRUCTION BITS 162X 164X UCI.FR EQU 00100000B 165X UCI.FR EQU 00010000B 165X UCI.FR EQU 0001000B 165X UCI.FR EQU 00000100B 165X UCI.FR EQU 00000100B 165X UCI.FR EQU 00000010B 165X UCI.FR EQU 00000010B 170X ** STATUS READ COMMAND BITS 171X 172X USR.FE EQU 0001000B 173X USR.PE EQU 0001000B 173X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FE EQU 0001000B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 00000010B	161X ** COMMAND INSTRUCTION BITS 162X 163X UCI.IR EQU 164X UCI.RE EQU 165X UCI.RE EQU 166X UCI.RE EQU 167X UCI.RE EQU 166X UCI.RE EQU 176X USR.FE EQU 175X USR.FE EQU 175X USR.TXE EQU 175X USR.TXE EQU 175X USR.TXE EQU 175X USR.TXR EQU	161X #* COMMAND INSTRUCTION BITS 162X 163X UCI.IR EQU 00100000B 164X UCI.RD EQU 0010000B 165X UCI.RE EQU 0001000B 165X UCI.RE EQU 0000010B 165X UCI.RE EQU 00000010B 165X UCI.RE EQU 00000010B 172X USR.FE EQU 0001000B 173X USR.PE EQU 0001000B 175X USR.PE EQU 0001000B 175X USR.PE EQU 0000100B 175X USR.TXE EQU 0000100B 175X USR.TXE EQU 00000010B 175X USR.TXE EQU 00000010B 175X USR.TXE EQU 00000001B	161X #* COMMAND INSTRUCTION BITS 162X 164X UCI.IR EQU 00100000B 164X UCI.RE EQU 000000B 165X UCI.RE EQU 000000B 166X UCI.RE EQU 00000010B 168X UCI.RE EQU 00000010B 168X UCI.RE EQU 00000010B 170X #* STATUS READ COMMAND BITS 171X 171X 172X USR.FE EQU 0001000B 174X USR.FE EQU 00000100B 175X USR.FE EQU 00000100B 175X USR.FE EQU 00000100B 175X USR.FE EQU 0000010B 177X USR.FE EQU 00000010B 177X USR.FE EQU 00000010B 177X USR.FE EQU 00000010B	161X #* CDMMAND INSTRUCTION BITS 162X UCI.IR EQU 0100000B 164X UCI.IR EQU 0010000B 165X UCI.IR EQU 000000B 165X UCI.IR EQU 000000B 168X UCI.IR EQU 0000001B 168X UCI.IR EQU 0000001B 169X # STATUS READ CDMMAND BITS 170X #* STATUS READ CDMMAND BITS 171X USR.FE EQU 0000000B 173X USR.FE EQU 0001000B 173X USR.FE EQU 0000100B 175X USR.FE EQU 0000001B 175X USR.FK EQU 00000010B 175X USR.FK EQU 00000010B 177X USR.TXR EQU 00000010B					:
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164x UCI.RO ERU 00100000B 165x UCI.RE ERU 0001000B 165x UCI.RE ERU 00000010B 168x UCI.RE ERU 00000010B 169x 170x ** STATUS READ CDMMAND BITS 172x USR.FE ERU 0001000B 172x USR.FE ERU 0001000B 173x USR.PE ERU 0001000B 174x USR.PE ERU 00001000B 175x USR.FE ERU 00001000B 175x USR.FE ERU 00001000B 175x USR.FE ERU 00001000B 175x USR.FE ERU 0000100B 175x USR.FE ERU 0000100B 175x USR.FE ERU 00000100B	164X UCI.RO EQU 00100000B 165X UCI.RE EQU 0001000B 165X UCI.RE EQU 00000100B 167X UCI.IE EQU 00000010B 168X UCI.TE EQU 00000010B 170X ** STATUS REAU CDMMAND BITS 171X USR.FE EQU 0001000B 173X USR.FE EQU 0001000B 175X USR.TXE EQU 0000100B 175X USR.TXE EQU 00000100B 175X USR.TXE EQU 00000100B 175X USR.TXE EQU 00000100B	164X UCI.RO EQU 0010000B 165X UCI.ER EQU 0001000B 165X UCI.IR EQU 0000010B 165X UCI.IR EQU 0000010B 165X UCI.IR EQU 00000010B 168X UCI.IR EQU 0000001B 178X USR.FE EQU 0010000B 173X USR.FE EQU 0001000B 173X USR.FE EQU 0001000B 175X USR.TXE EQU 00000100B 175X USR.TXE EQU 00000100B 175X USR.TXR EQU 00000100B	164x UCI.FR EQU 0001000B 165x UCI.FR EQU 000100B 165x UCI.IE EQU 0000010B 165x UCI.IE EQU 0000010B 168x UCI.TE EQU 0000001B 169x ** STATUS REAU CUMMANU BITS 170x WSR.FE EQU 0001000B 173x USR.FE EQU 0001000B 175x USR.FE EQU 0000100B 175x USR.FE EQU 00000100B 175x USR.FE EQU 00000100B 175x USR.TXR EQU 00000100B 175x USR.TXR EQU 00000100B 177x USR.TXR EQU 00000100B	164x UCI.FG EQU 0001000B 165x UCI.FE EQU 000100B 165x UCI.FE EQU 0000010B 165x UCI.FE EQU 00000010B 165x UCI.FE EQU 00000010B 168x UCI.FE EQU 000000017 170x ¥* STATUS KEAU CUMMANU BITS 171x USR.FE EQU 0001000B 173x USR.FE EQU 0000100B 175x USR.FXE EQU 0000100B 175x USR.FXE EQU 00000010B 175x USR.FXE EQU 00000010B 175x USR.FXE EQU 00000010B	164X UCI.FR EQU 0010000B 166X UCI.FR EQU 000100B 166X UCI.FR EQU 0000010B 168X UCI.FR EQU 0000010B 168X UCI.FR EQU 0000010B 169X ** STATUS REAU CDMMAND BITS 170X USR.FF EQU 0001000B 173X USR.FF EQU 000100B 175X USR.FF EQU 0000100B 175X USR.FF EQU 0000100B 175X USR.FF EQU 0000010B 175X USR.FF EQU 0000010B 175X USR.FF EQU 0000010B	000.100	UCI.IR	EGU	01000000B	INTERNAL RESET
165X UCI.ER ERU 0001000B ERROR RESET 164X UCI.RE ERU 00000100B RECEIVE ENABLE 167X UCI.TE ERU 00000010B TRANSMIT ENABLE 169X 170X ** STATUS READ CDMMAND BITS. 171X USR.FE ERU 00010000B FRANTNG ERRUR 172X USR.DE ERU 00010000B FARITY ERROR 174X USR.FE ERU 00010000B FARITY ERROR 175X USR.FE ERU 00001000B FARITY ERROR 175X USR.FE ERU 00001000B FARITY ERROR 175X USR.FE ERU 0000100B FARITY ERROR 175X USR.FE ERU 0000100B FARITY ERROR 175X USR.FE ERU 0000100B FARITY ERROR 175X USR.FE ERU 0000010B FARITY ERROR 175X USR.FE ERU 00000010B FARITY ERPTY	165X UCI.ER EQU 0001000B ERROR RESET 164X UCI.RE EQU 00000100B RECEIVE ENABLE 167X UCI.TE EQU 00000010B TRANSMIT ENABLE 170X ** STATUS READ CDMMAND BITS. 171X USR.FE EQU 00010000B FRAMING ERRUR 173X USR.PE EQU 00010000B PARITY ERROR 175X USR.TXE EQU 0000100B FRAMING ERROR 175X USR.TXE EQU 0000100B TRANSMITTER EMPTY 175X USR.TXR EQU 0000010B TRANSMITTER EMPTY 175X USR.TXR EQU 0000010B TRANSMITTER READY	165X UCI.ER EQU 0001000B ERROR RESET 168X UCI.RE EQU 00000100B RECEIVE ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 159X ** STATUS READ COMMAND BITS. 170X USR.FE EQU 0010000B FRAMING ERROR 173X USR.DE EQU 0001000B FRAMING ERROR 173X USR.PE EQU 00001000B FRAMING ERROR 175X USR.TXE EQU 00000100B FRANSMITTER READY 175X USR.TXE EQU 00000100B TRANSMITTER READY 177X USR.TXR EQU 00000010B TRANSMITTER READY	165X UCI.FE EQU 00001000B ERROR RESET 165X UCI.TE EQU 0000010B ENABLE 167X UCI.TE EQU 0000010B ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 168X UCI.TE EQU 0000001B TRANSMIT ENABLE 170X WR STATUS READ CDHHAND BITS. 171X USR.FE EQU 0001000B FRANTNY ERROR 173X USR.FE EQU 0001000B FRANTITY ERROR 173X USR.FE EQU 00000010B FREELIVER READY 175X USR.TXE EQU 00000010B FREELIVER READY 175X USR.TXR EQU 00000010B TRANSMITTER FRANTITY 175X USR.TXR EQU 00000010B TRANSMITTER FRANTITY 175X USR.TXR EQU 0000001B TRANSMITTER FRANTITY 175X USR.TXR EQU 00000001B TRANSMITTER FRANTITY	165X UCI.FE EQU 00001000B ERROR RESET 166X UCI.FE EQU 00000100B ERROR FESET 168X UCI.TE EQU 00000010B ERROR FERENCE 159X XX STATUS READ COMMAND BITS. 170X XX STATUS READ COMMAND BITS. 171X USR.FE EQU 00010000B FRANTH ERROR 173X USR.FE EQU 0000100B FRANTH ERROR 173X USR.FE EQU 0000100B FRANTH ERROR 175X USR.TXE EQU 00000100B FRANTH ERROR 175X USR.TXE EQU 00000100B TRANSHITTER EMPTY 175X USR.TXE EQU 0000010B TRANSHITTER EMPTY 175X USR.TXE EQU 00000010B TRANSHITTER READY 177X USR.TXR EQU 00000010B TRANSHITTER READY	165X UCI.FR EQU 0001000B ERROR RESET 164X UCI.FE EQU 000010B RECETUVE ENABLE 165X UCI.TE EQU 00000010B ENABLE INTERRUPTS 165X UCI.TE EQU 00000010B TRANSHIT ENABLE 165X UCI.TE EQU 0001000B FRANTNG ERROR 173X USR.FE EQU 0001000B FRANTNG ERROR 173X USR.FE EQU 0001000B FRANTN ERROR 174X USR.FE EQU 0000100B FARITY ERROR 175X USR.TXE EQU 00000010B FARITY ERROR 175X USR.TXE EQU 00000010B TRANSHITTER READIN 177X USR.TXR EQU 0000001B TRANSHITTER READIN 177X USR.TXR EQU 0000001B TRANSHITTER READIN	000.040	UCI.RO	EQU	0010000B	READER-UN CONTRÓL FLAG
164X UCI.RE EQU 00000100B RECEIVE ENABLE 167X UCI.IE EQU 00000010B ENABLE INTERRUPTS 168X UCI.TE EQU 0000001B TRANSMIT ENABLE 170X ** SYATUS READ COMMAND BITS. 171X USR.FE EQU 00010000B FRAMING ERRUR 173X USR.PE EQU 00010000B PARITY ERROR 175X USR.FE EQU 00001000B FARITY ERROR 175X USR.TXE EQU 0000100B TRANSMITTER EMPTY 175X USR.TXE EQU 0000100B TRANSMITTER EMPTY 175X USR.TXE EQU 0000010B TRANSMITTER EMPTY 175X USR.TXE EQU 00000001B TRANSMITTER READY	166X UCI.RE EQU 00000100B RECEIVE ENABLE 167X UCI.IE EQU 00000010B ENABLE INTERRUPTS 168X UCI.TE EQU 0000001B TRANSMIT ENABLE 150X ** STATUS READ COMMAND BITS. 171X 0010000B FRAHING ERRUR 173X USR.FE EQU 00010000B DUERRUN ERROR 174X USR.FE EQU 00001000B PARITY ERROR 175X USR.TXE EQU 0000100B TRANSMITTER EMPTY 176X USR.TXR EQU 00000100B TRANSMITTER EMPTY 177X USR.TXR EQU 0000001B TRANSMITTER READY	166X UCI.RE EQU 00000100B RECEIVE ENABLE 167X UCI.IE EQU 00000010B ENABLE INTERBUPTS 168X UCI.TE EQU 00000010B TRANSHIT ENABLE 170X ** STATUS READ COMMAND BITS. 171X STATUS READ COMMAND BITS. 172X USR.PE EQU 0001000B DUERRUR 174X USR.PE EQU 00001000B PARITY ERROR 175X USR.TE EQU 00000100B RECEIVER READY 175X USR.TE EQU 00000100B RECEIVER READY 175X USR.TER EQU 00000010B RECEIVER READY 175X USR.TER EQU 00000010B RECEIVER READY	164X UCI.IE EQU 00000100B RÉCEIVE ENABLE 168X UCI.IE EQU 00000010B TRANSMIT ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 168X UCI.TE EQU 0000000B FRAMING ERRUR 173X USR.FE EQU 0001000B FRAMING ERRUR 173X USR.TXE EQU 00001000B TRANSMITTER EMPTY 175X USR.TXR EQU 0000000B FRANSMITTER EMPTY 175X USR.TXR EQU 00000001B TRANSMITTER READY 177X USR.TXR EQU 00000001B TRANSMITTER READY	165X UCI.IE EQU 00000010B FREET ENABLE 168X UCI.IE EQU 00000010B FRANSHIT ENABLE 168X UCI.IE EQU 00000010B TRANSHIT ENABLE 168X UCI.IE EQU 00000010B TRANSHIT ENABLE 176X XX 171X USR.FE EQU 0001000B FRATIVE FROE 173X USR.FE EQU 00000100B FRATIVE FROE 175X USR.FE EQU 00000100B TRANSHITER EMPTY 175X USR.FXE EQU 00000100B TRANSHITER EMPTY 175X USR.FXE EQU 00000001B TRANSHITTER FROEDY 177X USR.FXE EQU 00000001B TRANSHITTER FROEDY	166X UCI.RE EQU 00000010B RECETYE ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 168X UCI.TE EQU 00000010B TRANSMIT ENABLE 170X ** STATUS READ COMMAND BITS. 172X USR.FE EQU 0010000B FRAMING ERRUR 173X USR.E EQU 0001000B FRAMING ERRUR 175X USR.TXE EQU 0000100B TRANSMITTER EMPTY 175X USR.TXE EQU 0000010B TRANSMITTER EMPTY 175X USR.TXE EQU 0000010B TRANSMITTER EMPTY 175X USR.TXE EQU 00000010B TRANSMITTER FEADY 177X USR.TXE EQU 00000010B TRANSMITTER FEADY	000.020	UCI.ER	EQU	0001000B	ERROR RESET
167X UCI.IE ERU 00000010B ENABLE INTERRUPTS 168X UCI.TE ERU 00000001B TRANSMIT ENABLE 159X 170X ** STATUS KEAD COMMAND BITS. 171X USR.FE ERU 0010000B DVERRUN ERROR 174X USR.PE ERU 00010000B PARITY ERROR 175X USR.TXE ERU 00001000B TRANSMITTER EMPTY 175X USR.TXE ERU 0000100B TRANSMITTER EMPTY 175X USR.TXE ERU 00000100B TRANSMITTER EMPTY 175X USR.TXE ERU 00000010B TRANSMITTER EMPTY 177X USR.TXE ERU 00000001B TRANSMITTER READY	167X UCI.IE ERU 00000010B ENABLE INTERRUPTS 168X UCI.TE ERU 0000001B TRANSMIT ENABLE 159X 170X ** STATUS KEAD COMMAND BITS. 171X USR.FE ERU 0010000B FRAMING ERRUR 173X USR.FE ERU 00001000B PARITY ERROR 175X USR.FE ERU 00001000B TRANSMITTER EMPTY 175X USR.FE ERU 00000100B TRANSMITTER EMPTY 175X USR.TXR ERU 00000010B TRANSMITTER READY	167X UCI.IE EQU 00000010B ENABLE INTERRUPTS 168X UCI.TE EQU 00000001B TRANSMIT ENABLE 159X ** STATUS READ COMMAND BITS. 171X USR.FE EQU 0010000B FRAMING ERRUR 173X USR.BE EQU 00001000B FARITY ERRUR 175X USR.TXE EQU 00000100B RECEIVER ERPTY 176X USR.TXR EQU 00000100B RECEIVER READY 177X USR.TXR EQU 0000010B RECEIVER READY 177X USR.TXR EQU 00000010B TRANSMITTER READY	167X UCI.IE EQU 00000010B EAABLE INTERRUPTS 168X UCI.TE EQU 0000001B TRANSHIT ENABLE 170X ** STATUS READ CDANAND BITS. 170X USR.FE EQU 00010000B FRANING ERRUR 173X USR.FE EQU 0001000B FRANITY ERRUR 173X USR.FXE EQU 0000100B FRANITY ERRUR 175X USR.FXE EQU 0000010B FRANSHITTER EMPTY 175X USR.FXE EQU 00000010B FRANSHITTER READY 177X USR.FXE EQU 00000010B TRANSHITTER READY	167X UCI.IE EQU 00000010B EAABLE INTERRUPTS 168X UCI.TE EQU 00000010B TRANSHIT ENABLE 170X *** STATUS KEAD CDNHAND BITS. 171X USR.FE EQU 00010000B FRAHING ERURE 172X USR.FE EQU 00001000B FRAHING ERURE 172X USR.FE EQU 00000100B FREELIVER ERPT 172X USR.FIXE EQU 00000010B FREELIVER FRAHING 172X USR.FIXE EQU 00000001B TRANSHITTER FRAHING 172X USR.FIXE EQU 000000001B TRANSHITTER FRAHING 172X USR.FIXE EQU 000000001B TRANSHITTER FRAHING 172X USR.FIXE EQU 000000001B TRANSHITTER FRAHING 172X USR.FIXE EQU 0000000000000000000000000000000000	167X UCI.IE EQU 00000010B EAABLE INTERFUPTS 168X UCI.TE EQU 0000001B TRANSHIT ENABLE 171X USR.FE EQU 0001000B FRAHING ERNUR 173X USR.RE EQU 0000100B FRAHING ERNUR 175X USR.RE EQU 00000100B FRAHITE ENPTY 175X USR.TXR EQU 0000010B RECEIUER READY 177X USR.TXR EQU 00000001B TRANSHITTER READY 177X USR.TXR EQU 00000001B TRANSHITTER READY	000.004	UCI.RE	EGU	00000100B	RECEIVE ENABLE
168X UCI.TE EQU 00000001B 169X 170X ** STATUS READ CDMMAND BIT 171X 172X USR.FE EQU 00010000B 173X USR.DE EQU 00001000B 174X USR.FE EQU 00001000B 175X USR.FE EQU 00001000B 175X USR.FE EQU 00000100B 175X USR.TXE EQU 00000100B 175X USR.TXE EQU 00000100B	168X UCI.TE EQU 00000001B 169X ** STATUS READ CDMMAND BIT 171X USR.FE EQU 0010000B 173X USR.FE EQU 00001000B 175X USR.FE EQU 0000100B 175X USR.FE EQU 00000100B 175X USR.FXE EQU 00000100B 175X USR.FXE EQU 0000010B	168X UCI.TE EQU 00000001B 169X ** STATUS READ CDMMAND BIT 171X 172X USR.FE EQU 0010000B 173X USR.FE EQU 0001000B 173X USR.FE EQU 0000100B 175X USR.FE EQU 0000100B 175X USR.FXE EQU 0000010B 175X USR.FXE EQU 0000010B 177X USR.FXE EQU 0000010B	168X UCI.TE EQU 00000001B 169X ** STATUS READ CDMMAND BIT 171X USR.FE EQU 0001000B 173X USR.FE EQU 0000100B 174X USR.FE EQU 0000010B 175X USR.FXE EQU 0000010B 177X USR.TXR EQU 00000001B	168X UCI.TE EQU 00000001B 169X ** STATUS READ CDMMAND BIT 171X 171X 172X USR.FE EQU 0001000B 173X USR.FE EQU 0000100B 175X USR.FE EQU 0000010B 175X USR.TXR EQU 0000010B 177X USR.TXR EQU 0000010B	168X UCI.TE EQU 0000001B 169X ** STATUS READ CDMMAND BIT 171X USR.FE EQU 0010000B 173X USR.FE EQU 0000100B 175X USR.FE EQU 0000010B 175X USR.FXE EQU 0000010B 177X USR.FXE EQU 00000010B 177X USR.FXE EQU 00000001B	000,002	UCI.IE	EGU	00000010B	ENABLE INTERFUPTS FLAG
168% ** STATUS READ CUMMAND BITS 171X 172X USR.FE EQU 0010000B 173X USR.DE EQU 00010000B 174X USR.FE EQU 00001000B 175X USR.FE EQU 0000100B 175X USR.FXE EQU 0000010B 175X USR.FXE EQU 0000010B	168% ** STATUS READ CUMMAND BITS 171X	168% ** STATUS REAU CUMMANU BITS 171X USK.FE EQU 0010000B 173X USK.PE EQU 0001000B 173X USK.PE EQU 0001000B 174X USK.TXE EQU 00000100B 176X USK.RXE EQU 00000100B 176X USK.TXR EQU 0000010B	169X ** STATUS REAU CUMMANU BITS 171X USK.FE EQU 0010000B 173X USK.FE EQU 0001000B 174X USK.FE EQU 000100B 175X USK.TXE EQU 000010B 176X USK.RXE EQU 0000001B 177X USK.TXR EQU 0000001B	160X ** STATUS REAU CUMMANU BITS 171X USR.FE EGU 0010000B 173X USR.FE EGU 0001000B 174X USR.FE EGU 0000100B 175X USR.FXE EGU 0000010B 175X USR.FXE EGU 0000001B 177X USR.FXE EGU 0000001B	1687 ** STATUS REAU CUMMANU BITS 171X USR.FE EQU 0010000B 173X USR.PE EQU 0001000B 174X USR.FE EQU 0000100B 175X USR.FXE EQU 00000100B 175X USR.TXE EQU 0000001B	000.001	UCI.TE	EGU	00000001B	TRANSMIT ENABLE
171X ** STATUS KEAU CUMMAND BITS 171X USK.FE EQU 00010000B 173X USK.DE EQU 0001000B 174X USK.FE EQU 00001000B 175X USK.FXE EQU 00000100B 175X USK.FXE EQU 00000100B 175X USK.FXE EQU 0000010B	170x ** STATUS KEAU CUMMAND BITS 172X USK.FE EQU 0010000B 173X USK.PE EQU 0001000B 174X USK.PE EQU 00001000B 175X USK.TXE EQU 00000100B 175X USK.TXE EQU 00000010B 177X USK.TXR EQU 00000001B	171X ** STATUS KEAU CUMMANU BITS 171X USR.FE EQU 0010000B 173X USR.OE EQU 00010000B 174X USR.FE EQU 0000100B 175X USR.TXE EQU 00000100B 175X USR.TXR EQU 00000010B	1710X ** STATUS KEAU CUMMANU BITS 172X USR.FE EGU COCICCOCB 173X USR.OE EGU COCOCICCOB 174X USR.TXE EGU COCOCICOB 175X USR.TXE EGU COCOCICOB 175X USR.TXR EGU COCOCCICOCOCOCICOB 175X USR.TXR EGU COCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCOCCICOCOCCICOCOCCICOCOCOCICOCOCCICOCOCCICOCOCCICOCOCCICOCOCOCCICOCOCCICOCOCCICOCOCCICOCOCO	171X	170X ** STATUS READ CUMMAND BITS 172X USR.FE EQU 0010000B 173X USR.PE EQU 0001000B 174X USR.FE EQU 0000100B 175X USR.FX EQU 0000001B 177X USR.TXE EQU 00000001B		×69	3 . 3 . 3		
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173X USR.DE EQU 0001000B 174X USR.PE EQU 0001000B 175X USR.TXE EQU 0000100B 175X USR.TXE EQU 00000100B 176X USR.TXE EQU 0000010B	173X USR.DE ENU 0001000B 174X USR.PE EQU 00001000B 175X USR.TXE EQU 00000100B 176X USR.TXE EQU 00000100B 176X USR.TXE EQU 00000010B 177X USR.TXE EQU 00000001B	173X USR. DE ENU 0001000B 174X USR. PE EQU 00001000B 175X USR. TXE EQU 00000100B 176X USR. FXE EQU 00000100B 177X USR. TXR EQU 00000010B	173X USR.PE EQU 0001000B 174X USR.PE EQU 00001000B 175X USR.TXE EQU 0000010B 175X USR.TXE EQU 0000001B 177X USR.TXE EQU 00000001B	173X USR. DE EQU 0001000B 174X USR. PE EQU 00001000B 175X USR. RXR EQU 0000010B 177X USR. TXR EQU 00000001B	173X USR. DE EQU 0001000B 174X USR. PE EQU 00001000B 175X USR. TXE EQU 0000010B 177X USR. TXR EQU 00000001B	000 000	راجن جد			
174X USR.PE EQU 00001000B 175X USR.TXE EQU 00000100B 176X USR.RXR EQU 00000010B 177X USR.TXR EQU 0000001B	174% USR.FE EQU 00001000B 175% USR.TXE EQU 00000100B 176% USR.ERE EQU 00000010B 177% USR.TXR EQU 00000001B	174% USR.PE EQU 00001000B 175% USR.TXE EQU 00000100B 176% USR.FXR EQU 00000010B 177% USR.TXR EQU 00000001B	174x USR. FE EQU 00001000B 175x USR. TXE EQU 00000100B 176x USR. TXR EQU 00000001B 177X USR. TXR EQU 00000001B	174X USR. FE EQU 00000100B 175X USR. TXE EQU 0000010B 174X USR. TXR EQU 0000001B	174x USR. FE EQU 00001000B 175x USR. TXE EQU 00000100B 176x USR. TXR EQU 0000001B	000.000	10.451	501	00010008	
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176X USR.RXR EQU 00000010B 177X USR.TXR EQU 00000001B	176X USR.RXR EQU 00000010B 177X USR.TXR EQU 00000001B	176% USR.RXR EQU 0000001B	176% USR.TXR EQU 00000001B	176% USR.TXR EQU 0000001B	176% USR.TXR EQU 0000001B	000.000	75X 11SB. TXF	FOIL	000001008	ALORE CONTRACTOR OF THE PROPERTY OF THE PROPER
177X USR.TXR EQU 00000001B	177X USR.TXR EQU 00000001B	177X USR.TXR EQU 00000001B	177X USR.TXR EQU 00000001B	177X USR.TXR EQU 00000001B	177X USR.TXR EQU 00000001B	000.000	74Y 1198 FYE	For	000000101	6474014 64740V
	0477 (0) (0) (0) (0) (0) (0) (0) (0) (0) (0)					000,001	USR. TXR		00000001B	TROUBLITTER STADES
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HARDWARE INTERRUPT VECTORS	· · · ·				13:23:25 01-APR-77 PAGE	PAGE &
	1181 181 * * *		NTERRUF	INTERRUPT VECTORS.		
	: :	: <u>-</u> :	LEVEL 0	- RESET		
	185 186 87		THIS (I)	'INTERRUPT' MAY NOT	T BE PROCESSED BY A USER	PROGRAM.
000 + 000	100	0:	ORG	,00A		
000,000 021 371 003 000,003 041 012 040 000,006 303 073 000 377,073	189 190 191 192 193		LXI LXI JMP ERRPL	D,FRSROM H,FRSRAM+FRSL-1 INIT-1000A	(DE) = ROM COPY OF PRS CODE (HL) = RAM DESTINATION FOR CODE INITIALIZE BYTE IN WORD 10A MUST BE 0	ODE OR CODE 0
	195 **		LEVEL 1	- CLOCK		
000.010	196 197 INT		E Q ()	100	INTERRUPT ENTRY POINT	
: :	198 199 200	: : :	ERRNZ CALL	*-110 SAVALL	INTO TAKES UP ONE BYTE SAVE USER REGISTERS	
514 026 000 516 303 201 201	201 203 203	Σ'n	AVI JAP ERRPL	D,0 CLGCK CLGCK-1000A	PROCESS CLOCK INTERRUPT EXTRA BYTE MUST BE 0	
	205 **	Ī	LEVEL 2	- SINGLE STEP		
	207 207 209 210	1100	IF THIS THEN IT (SINGLE)	INTERRUPT IS RE IS ASSUMED TO B STEPPING OK BRE GRAM IS ENTERED	IF THIS INTERRUPT IS RECEIVED WHEN NOT IN MONITOR MODE THEN IT IS ASSUMED TO BE GENERATED BY A USER FRUSHAM (SINGLE STEPPING OR BREAKPOINTING). IN SUCH CASE, THE USER PROGRAM IS ENTERED THROUGH (UIVEC+3	ik Mode; Ukram ; THE
000,000	211 212 INT2		EQU	20A	LEVEL 2 ENTRY	
000.000 000.021 315 132 000 000.024 032	215 215 215 216	⊞.0 <u>1</u>	ERRNZ CALL LDAX	*-21A SAVALL D	INTI TAKES EXTRA BYTE SAVE REGISTERS (A) = (CTLFLG)	
: :	217 . 218	ָּמּ הַ	SET	STPRTN	STEP RETURN	
	**************************************	I	I/O INTERRUPT	RRUPT VECTORS.		
	:	Ī	TERRUP	INTERRUPTS 3 THROUGH 7	7 ARE AVAILABLE FOR GENERAL	I/O USE.
	* * * * * * * * * * * * * * * * * * *	FZ	JESE IN TUER OC THROUGH	TERRUPTS ARE NO CUR UNLESS THE UIVEC)	THESE INTERRUPTS ARE NOT SUPPORTED BY PAM/8, AND SHOULD NEVER OCCUR UNLESS THE USER HAS SUPPLIED HANDLER ROUTINE (THROUGH UIVEC)	SHOULD ROUTINES

000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.050 000.05	303 045 040 229 INT3 JMP 064 064 064 231 INT3 JMP 230 050 040 234 INT4 JMP 100 112 107 235 INT5 JMP 241 ** ENTRY 242 ** ENTRY 243 ** ENTRY 244 ** ENTRY 244 ** ENTRY 245 ** ENTRY 245 ** ENTRY 246 ** ENTRY 247 ** ENTRY 248 ** ENTRY 248 ** ENTRY 249 ** ENTRY 240 ** ENTRY 240 ** ENTRY 241 ** ENTRY 242 ** ENTRY 242 ** ENTRY 244 ** ENTRY 245 ** ENTRY 246 ** ENTRY 247 ** ENTRY 248 ** ENTRY 248 ** ENTRY 257 ** ENTRY 258 ** ENTRY 259 ** ENTRY 250 ** ENTRY 260 ** ENTRY 270 ** ENT	JUMP TO USER ROUTINE HEATH PART NUMBER 444-12 JUMP TO USER ROUTINE JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT SAVE COUNT DONT SOUND HORN PROCESS AS HORN
303 045 040 229 INT3 JHP UIVECH6 JUHP TO USER ROUTINE 064 064 064 231	303 045 040 229 INT3 JHP 064 064 064 231 DB 100 112 107 235 INT4 JHP 100 112 107 235 INT4 JHP 100 112 107 235 DB 245 XX BLY - DE 245 XX BSI 245 XX BSI 245 XX BSI 247 XX BSI 257 JAP 257 JAP 257 JAP 258 JAT JHP 268 JAT - DE 249 XX BSI 258 JAT JAP 259 JAT - DE 240 XX BSI 250 040 253 INT6 JHP 250 056 040 255 INT6 JHP 250 056 050 JAP 250 076 320 051 050 250 JHP	JUMP TO USER ROUTINE HEATH PART NUMBER 444-1: JUMP TO USER ROUTINE JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT DONT SOUND HORN PROCESS AS HORN
064 064 064 231 DB 744137 HEATH PART NUMBER 444-13 303 050 040 233 INT5 JMP UIVECT? JUMP TO USER ROUTINE 100 112 107 234 INT5 JMP UIVECT? JUMP TO USER ROUTINE 225 INT5 JMP UIVECT? JUMP TO USER ROUTINE 226 X	064 064 064 231 DB 064 064 064 231 DB 303 050 040 234 INT4 JMP 100 112 107 236 DB 303 053 040 239 INT5 JMP 240 240 240 241 XXB 241 XX ENTRY 242 XX ENTRY 245 X ENTRY 245 X ENTRY 246 X USES 365 249 DLY FUSH 257 JMP 303 235 001 255 GG, MUI 303 235 001 257 JMP	HEATH PART NUMBER 444-1: JUMF TO USER ROUTINE JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT SAVE COUNT DONT SOUND HORN PROCESS AS HORN
303 050 040 233 INT4 0RG 404 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 107 236 100 112 112 112 114 10 10 10 10 10 10 10 10 10 10 10 10 10	303 050 040 234 INT4 JMP 100 112 107 235 INT4 JMP 100 112 107 236 INT5 JMP 240 240 240 240 JMP 241 ** ENITY 245 ** ENITY 303 143 002 250 JMP 303 143 002 255 GG, MVI 303 235 001 257 JMP 259 DKG 303 061 040 260 INT7 JMP	A.1078,1148,1008 SUPPORT JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT DONT SOUND HORN PROCESS AS HORN
303 050 040 234 IN14 JMF UIVECH? JUMP TO USER ROUTINE 100 112 107 236 DB 1000,1120,1070,1140,1000 SUPPORT O 303 053 040 239 INTS JMF UIVECH12 JUMP TO USER ROUTINE 241 ** DLY - DELAY TITE INTERVAL. 242 ** ENTRY (A) = HILLISECOND DELAY COUNT? 245 ** ENTRY (A) = HILLISECOND DELAY COUNT? 245 ** ENTRY (A) = HILLISECOND DELAY COUNT? 245 ** ENTRY (A) = HILLISECOND DELAY COUNT? 246 ** ENTRY (A) = HILLISECOND DELAY COUNT? 247 ** ENTRY (A) = HILLISECOND DELAY COUNT? 248 ** ENTRY (A) = HILLISECOND DELAY COUNT? 249 ** KXI T NONE 355 249 ** KYA T HE INTERVAL. 365 249 ** KYA T HE INTERVAL. 367 249 ** KYA T HE INTERVAL. 368 343 055 040 255 INT6 JMF FSW ARMO PROCESS AS HORN 369 343 055 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255 041 255	303 050 040 234 INT4 JMP 235 100 112 107 236 DB 303 053 040 239 INT5 JMP 240 *** ENTRY 241 ** ENTRY 245 ** ENTRY 246 ** ENTRY 247 ** ENTRY 248 DLY FUSH 257 249 DLY FUSH 257 249 DLY 303 143 002 256 GD, MUI 303 235 001 257 JMP 259 0041 040 250 INT7 JMP	JUMP TO USER ROUTINE JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT DONT SOUND HORN PROCESS AS HORN
100 112 107 236 DB 100011120.107011140.1000 SUPPORT C 238 DB 239 086 SOA 303 055 040 239 INT5 DBF 50A TIME INTERVAL. 243 ** ENTRY (A) = MILLISECOND DELAY COUNT.2 245 * ENTRY (A) = MILLISECOND DELAY COUNT.2 255 * ENTRY (A) = MILLISECOND DELAY	100 112 107 235 DB 303 053 040 239 INT5 JMP 241 ** ENTRY 242 ** ENTRY 245 ** ENTRY 245 ** ENTRY 246 ** USES 345 249 DLY - DE 247 ** ENTRY 248 DLY - DE 249 DLY - DE 247 ** ENTRY 248 DLY - DE 249 DLY - DE 257 249 DLY - DE 258 DLY - DE 258 DLY - DE 259 DLY - DE 250 DLY - DE 240 DLY - DE 241 DLY - DE 242 DLY - DE 245 DLY - DE 245 DLY - DE 246 DLY - DE 247 DLY - DE 248 DLY - DE 248 DLY - DE 248 DLY - DE 249 DLY - DE 250 DLY - DE 250 DLY - DE 251 DLY - DE 252 DLY - DE 253 DLY - DE 254 DLY - DE 255 DLY - DE 256 DLY - DE 257 DLY - DE 258 DLY - DE 259 DLY - DE 250 DLY - DE 250 DLY - DE 250 DLY - DE 251 DLY - DE 252 DLY - DE 253 DLY - DE 254 DLY - DE 255 DLY - DE 256 DLY - DE 257 DLY - DE 258 D	11078,1148,1000 SUPFORT JUMP TO USER ROUTINE INTERVAL. LISECOND DELAY COUNT/2 SAVE COUNT DONT SOUND HORN PROCESS AS HORN
303 061 040 258 INTS ORG 50A 303 053 040 239 INTS JMP UIVEC+12 JUMP TO USER ROUTINE 241 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 243 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 244 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 245 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 246 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 247 ** ENTRY (A) = HILLISECOND DELAY COUNTZ 248 DLY FUSH FSW DONT SOUNT HORN 303 143 002 250 AMP HRNO FRUCESS AS HORN 303 143 002 255 AMP HRNO DIVEC+15 JMP HRNO PROCESS AS HORN 303 255 OOI 255 INTS JMP ACE, SSI+CB.CLI+CB.SPK OFF HONITOR MODE 303 255 OOI 255 INTS JMP ACE, SSI+CB.CLI+CB.SPK OFF HONITOR MODE 303 255 OOI 255 INTS JMP UIVEC+18 JUMP TO USER ROUTINE 303 061 040 260 INTS JMP UIVEC+18 JMP TO USER ROUTINE	303 053 040 239 INT5 JMP 240 241 242 242 243	LISECO
303 053 040 239 INTS JAP UIVEC+12 JUHP TO USER ROUTINE 240 241 ** ENTRY (A) = MILLISECOND DELAY COUNT/2 243 ** ENTRY (A) = MILLISECOND DELAY COUNT/2 245 ** EXIT NONE 246 ** EXIT NONE 247 ** ENTRY (A) = MILLISECOND DELAY COUNT/2 248 DLY PUSH FSW SAVE COUNT 257 249 DLY PUSH FSW BODITOR HODE 303 056 040 253 INT6 JMP HRNO PROCESS AS HORN 255 DLY DIMP DIVEC+15 JUMP TO USER ROUTINE 255 DLY PUSH SSTILESCLIFCE.SPK OFF HONTOR HODE 303 235 001 257 DLM A,CB SSI+CE.CLIFCE.SPK OFF HONTOR HODE 303 064 040 255 INT7 JMP UIVEC+18 JUMP TO USER ROUTINE 303 064 040 260 INT7 JMP UIVEC+18 JUMP TO USER ROUTINE	303 053 040 239 INT5 JMP 240 241 243 ** BLY - DE 245 ** ENTRY 365 249 DLY FUSH 367 143 002 259 JMP 303 056 040 253 INT6 JMP 303 056 040 255 INT6 JMP 303 056 040 255 ORG 303 061 040 260 INT7 JMP	INTERU
241 ** DLY - DELAY TIME INTERVAL. 243 ** ENTRY 244 * ENTRY 245 * ENTRY 245 * USES 246 * USES 346	241 ** BLY - BE 243 ** ENTRY 244 * EXIT 245 * EXIT 246 * USES 247 * EXIT 246 * USES 247 * EXIT 248 BLY FUSH 257 249 BLY RAP 303 143 002 250 BRG 303 056 040 253 INT6 JMP 254 BLY BUSH 303 056 040 253 INT6 JMP 303 235 001 257 JMP 303 061 040 260 INT7 JMP	INTERVAL. LISECOND DELAY COUST SAVE COUNT DON'T SOUND PROCESS AS
243 * ENTRY (A) = MILLISECOND DELAY COUNT/2 245 * EXIT NONE 245 * EXIT NONE 246 * EXIT NONE 247 * EXIT NONE 248 DLY FUSH FSW 247 XRA A DONT SOUND HORN 303 143 002 250 JMP HRNO FROCESS AS HORN 257 249 DLY FUSH FSW 303 143 002 250 JMP HRNO FROCESS AS HORN 257 249 DLY FOWER 258 JMT6 JMP HRNO FROCESS AS HORN 258 259 JMT6 JMP UIVEC+15 JMP TO USER ROUTINE 303 235 001 257 JMP JMP JMP TO USER ROUTINE 303 061 040 260 JMT7 JMP JMP JMEC+18 JMMF TO USER ROUTINE	244 * ENTRY 245 * ENTRY 245 * EXIT 245 * EXIT 246 * USES 247	MILLISECOND DELAY COUNT/2 SAVE COUNT DONT SOUND HORN FROCESS AS HORN
245 * EXIT NONE 246 * USES AFF 247 247 248 DLY PSW 257 249 XRA A DONT SOUND HORN 257 249 XRA A DONT SOUND HORN 303 143 002 250 JMP HRNO PROCESS AS HORN 303 056 040 255 INT6 JMP UIVEC+15 JUNP TO USER ROUTINE 303 235 001 257 JMP SSTI 303 061 040 260 INT7 JMP UIVEC+18 JUNP TO USER ROUTINE	345 * EXIT 245 * USES 246 * USES 246 * USES 246 * USES 248 DLY FUSH 257 249 XRA 303 143 002 250 DRG 303 056 040 255 INT6 JMP 255 GD, MUI 303 235 001 257 JMP DRG 303 061 040 260 INT7 JMP	SAVE COUNT DONT SOUND HORN PROCESS AS HORN
365 247 PUSH FSW SAVE COUNT 257 248 DLY KRA A DONT SOUND HORN 257 249 XRA A DONT SOUND HORN 303 143 002 259 JMP HRNO PROCESS AS HORN 303 250 255 GO, MUI A,CB,SSI+CB,CLI+CB,SFK OFF MONITOR MODE 303 235 001 257 JMP SSTI RETURN TO USER FROGRAM 303 061 040 260 INT7 JMP UIVEC+18 JUMP TO USER ROUTINE	365 247 PUSH 257 249 XRA 303 143 002 250 JMP 303 056 040 253 INT6 JMP 254 076 320 255 076 320 256 60. MVI 303 235 001 257 JMP 303 061 040 260 INT7 JMP	SAVE COUNT DONT SOUND HORN FROCESS, AS HORN
257 249 XRA A DON'T SOUND HORN 303 143 002 250 JMP HRNO FROCESS AS HORN 303 056 040 253 INT6 JMP UIVEC+15 JUMP TO USER ROUTINE 255 255 255 255 255 255 255 255 255 25	257 XRA 303 143 002 250 JMP 303 056 040 253 INT6 JMP 254 255 076 320 256 GD, MUI 303 235 001 257 JMP 303 061 040 260 INT7 JMP	DON'T SOUND HORN FROCESS AS HORN
303 143 002 250 JMP HRNO FROCESS AS HORN 303 056 040 253 INT6 JMP UIVEC+15 JUMP TO USER ROUTINE 303 235 001 257 JMP A,CB.SSI+CB.CLI+CB.SFK OFF MONITOR MODE 303 235 001 257 JMP SST1 303 061 040 260 INT7 JMP UIVEC+18 JUMP TO USER ROUTINE	303 143 002 250 JMP 252 0RG 303 056 040 253 INT6 JMP 255 001 256 GD, MUI 303 235 001 257 DMP 259 0RG 303 061 040 260 INT7 JMP	PRUCESS AS HORN
303 056 040 253 INT6 JMP UIVEC+I5 JUMP TO USER ROUTINE 254 2076 320 256 GO. MVI A,CB.SSI+CB.CLI+CB.SFK OFF MONITOR MODE 303 235 001 257 JMP SSTI 259 0RG 70A 303 061 040 260 INT7 JMP UIVEC+18 JUMP TO USER ROUTINE	303 056 040 253 INT6 JMP 254 255 076 320 256 GD. MUI 303 235 001 257 JMP 303 061 040 260 INT7 JMP	
303 056 040 253 INT6 JMF UIVEC+15 JUMF TO USER KOUTINE 254 255 60. MVI A,CB.SSI+CB.CLI+CB.SFK OFF MONITOR MODE 303 235 001 257 JMP SST1 KETURN TO USER FROGRAM 303 061 040 260 INT7 JMP UIVEC+18 JUMF TO USER ROUTINE	303 056 040 253 INT6 JMF 254 255 076 320 303 235 001 257 BKG 303 061 040 260 INT7 JMF	
976 320 255 303 235 001 257 JMP SST1 303 235 001 257 JMP SST1 303 061 040 260 INTZ JMP UIVEC+18 JUMF TO USER ROUTINE	076 320 256 GD. MUI 303 235 001 257 JMP 303 061 040 259 DRG 303 061 040 260 INT? JMP	
303 061 040 260 INT? JMP UIVEC+18	303 061 040 260 INT? JMP	OFF MONITOR MODE TO USER PROGRAM

265 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 266 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 * 276 *	- LINI	- INITIALIZE SYSTEM	Ŧ
267 268 268 269 270 271 273 032 167 275 034 034	I TINI	S CALLED WHENEVER	INII IS CALLED WHENEVER A HARDWARE MASTER-CLEAR IS INITIATED.
270 271 272 273 274 032 167 275 054 057	SETUP DECODE ENTER	PAM/8 CONTROL CEL HOW MUCH MEMORY THE MONITOR LOOF.	SETUP FAM/8 CONTROL CELLS IN RAM. DECODE HOW MUCH MEMORY EXISTS, SETUP STACKFOINTER, AND ENTER THE MONITOR LOOP.
274 032 275 167 276 055 278	ENTRY	FROM MASTER CLEAR INTO PAM/8 MAIN LOUP	AR Loue
167 276 053 277 034 278	T LDAX	ū	COPY *PRSROM* INTO RAM
034		۲. ۲.	MOVE BYTE
302 073 000	INR	E INIT	HECKETEN FESTINALIUN INCREMENT SOURCE IF NOT DONE
		4000A	SEARCH INCREMENT
282 000,102 026 004 283 000,104 041 000 034 284	MUI	D.SINCR/256 H.START-SINCR	(DE) = SEARCH INCREMENT (HL) = FIRST RAM - SEARCH INCREMENT
2000 ×	DETERM	DETERMINE MEMORY LIMIT.	
F14N1 000 175 175 175 175 175 175 175 175 175 175	T+ **OE	3	BESTHER HALLE BEAR
110 031 289 111 176 290	:	£.4€	INCREMENT TRIAL ADDRESS (A) = CURRENT MEMORY VALUE
276 302 107 000		EE	TRY TO CHANGE II IF MEMORY CHANGED
053	:	Ι	
371 296 345 297		I	SET STACKPOINTER = MEMORY LIMIT -1 SET *FC* VALUE ON STACK
000,122 041 322 000 298 000,125 345 229	LXI FUSH	H,ERROR H	SET 'RETURN ADDRESS'
	CONFIG	CONFIGURE LOAD/DUMP UART	<u>.</u>
000,126 076 116 303 000,130 323 371 304	MVI OUT	A,UMI,18+UMI,L8 OP,TPC	A,UMI,1B+UMI.L8+UMI.16X OP.TPC SET 8 BIT, NO PARITY, 1 STOP, X16

	307	:	SAVALL	- SAVE ALL REGISTERS	RS ON STACK.
	308		141140		
	310	(*	SAVE TH	THE CONTENTS OF THE	INTERBUR! 1S ACCEPTED, IN ORDER TO REGISTERS ON THE STACK.
	311	•	CATEN	4.0.1404944.44.144	
	313	:	EXIT	ALL REGISTERS PUS	CALLET DIRECTLY FROM INTERRUPT RUDITME. ALL REGISTERS PUSHED ON STACK,
	314			IF NOT YET IN MON	ITOR MODE, REGPTR = AUDRESS OF REGISTERS
	316	:		(DE) = ADDRESS OF CTLFLG	CTLFLG
	317				
132	319	SAVALL	XTHL	U)	SET HIL ON STACK TOP
000,133 325	320		FUSH		
: 1321 1321	1.45 2.00 3.00		HNDL HVII û	24.0	
136 353	:		XCHG		(D.E) = RETHRN ADDRESS
137	000 324		LXI	10	A CARLON CONTRACTOR OF THE CON
1 4 7	200		UAU	1	H.L. * AUDRESS OF USERS SP
144 325			FUSH		SET ON STACK AS 'REGISTER' SET RETURN ADDRESS
145	040 328		LXI	DICTLFLG	
150	329		LIMX		(A) = CTLFLG
151	330		CMA		
154 310	100 272		ENT.	CE-MILTCE-551 5	SAVE REGISTER ADDR IF USER OR SINGLE-STEP
155	000		LXI	H, 23	ELUKN IF WAS INTERRUPT OF MONITOR LOOP
160 071			DAD		(H+L) = ADDRESS OF 'STACKPTR' ON STACK
000,161 042 035	040		SHLD RET	REGPTR	
	338	: :	cui – c	- CHECK FOR USER INTE	INTERRUPT PROCESSING.
	339	:	: ;		
	340	*:	CUI IS O	CALLED TO SEE IF THE USER HAS	HE USER HAS SPECIFIED PROCESSING
	342	:	Ξ.	CLOCK INTERRUPT.	
010	343 343				
000.165 012	345	CUII	LIMX		(A) = *MFLAG
1,44	246		EKKNZ	U0.CLK~1	DIE ASSUMED = 01
167 334 037	040 348			UIVEC	IF SPECIFIED, TRANSFER TO USER
	350	*	RETURN 1	TO FROGRAM FROM IN	INTERRUPT.
	351 352				REMOUS BAKE ATAOK BEGIGADER
•	353		FOF	715	
	354		P0P	A	
000-176 341	3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3		707	= I	
	357	:	EI		
	OC IN		وروا		

INTERRUPT		GLUCA, LS. ENTEREU, WHENEVER, A MILLISECOND, CLOCA INTERRUPT, IS. PROCESSED.	KY INTERRUPT.		INCREMENT TICCOUNT		ROPRIATE PATTERN ON THE	FRONT PANEL LEDS, THE LEDS ARE PAINTED IN REVERSE ORDER, ONE PER INTERRUPT, FIRST, NUMBER 9 IS LIT, THEN NUMBER 8.			: 1	EE IF FRONT PANEL REFRESH WANTED) = CIFFIG	(C) = 0 IN CASE NO FAMEL DISPLAY	IF NOT (H,L) = (REFIND)		DECKEMENT DIGIT INDEX IF NOT WRAP-AROUND	AP DISPLAY AROUND	(H,L) = ADDRESS OF PATTERN) = C1LNL6	(A) = INDEX + FIXED BITS SELECT DIGIT		SELECT SEGMENT	DISPLAY VALUES.		EVERY 32 INTERRUPTS	DATE FRONT PANEL DISPLAYS		:	(A) = CTLFLG
PROCESS CLOCK INTE		ED.	TICCNT IS INCREMENTED EVERY INTERRUPT	TICCNT		PANEL.	DE DISPLAYS THE APP	NEL LEDS, THE LEDS INTERRUPT, FIRST,		H, MFLAG		- O3	3 HFLAG-1		:	VD-CTLFL6-1				CyE * (A	914			TIME TO DECODE DISP	L, #TICCNT	370 EVI	UFD	CLOCK INTERRUPT.	BACTUELG	
CLOCK -	0	FROCESSI	TICCNT	LH	SHLD	REFRESH	THIS CO	FRONT P.	ETC.	LXI	70k	INI INX	ERRNZ	MOV	INX	ERRNZ	JNZ	NOI	DAE	EQU	OUT			SEE IF T		> H	CZ	EXII. CLQ	:	LDAX
*	* 1	· * *	€.₩	CLOCK		*	* *:		*									: 4	CLNK	CLK3				*				*		
361	362	364	366	368	371	372	374	376	378	380 381	382 383	384 385	386	388	390	391	368	394	2.65 2.65 2.66 2.66 2.66 2.66 2.66 2.66	398	399	401	402	404 405	406	408	409	411.2	413	414 415
				052 033 040	042 033 040					041 010 040		346 100 043	7.6	: [043 237 000	in the second se	02 234 000	011	031	3	261 323 360				056 033	37	14, 161, 003		01. 011. 040.	012 346 040
				; ;	000.205					• .	213 214	000.215 3	220 220	.221	V:00 V:01 V:01	000	227	232	10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.0	237	:	00 242	04 14 100		245	000.250 3	70.N		:	000.260 0

7.7															л. — — — — — — — — — — — — — — — — — — —						
		(A) = ,MFLAG		SKIP IT	ECK FOR HALT	(A) = INDEX OF *P* REG	LOCATE REGISTER ADDRESS		(D,E) = PC CONTENTS		CHECK FOR HALT IF HALT, BE IN MONITOR MODE	NITORY KEY ENTRY.			SEE IF 'O' AND '#' IF NOT. ALIGN INSERSEING OF CLOCK						
	8 8	בורורם יוויראם י	U0.HLT-2000	CLK4	NOT IN MONITOR MODE. CHECK FOR HALT	A,10	LRA.		Σ Δ			CHECK FOR "RETURN TO MONITOR" KEY ENTRY	*	IP.FAD	560 CHT1						
	DCX	LIMX	ERRNZ RAL	20	NI TON	MUI	CALL	X	300 200 200 200 200 200 200 200 200 200	LDAX	CP.I	CHECK	Eau	Z	O N						
	417	419	420 421	4 222 232	424 *	426	427	429	430 431	4 80 80 80 80 80 80 80 80 80 80 80 80 80	4 4 4 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	4 4. 4 4 0. 0 10. 0	43/ 438 CLK4		440 441					 	
	01.3	012	727	332 313 000			15 052 003	43	126 033		12 322 000			36	376 056 302 165 000						
	000,266 0	•		000.271 3		•	•		000.303		•		000,313		000.315 3						

		44.0	*	ERROR	- COMMAND ERROR.	
		446 447	* *	ERROR	ERROR IS CALLED AS A	(BAIL-0UT: ROUTINE.
		4 4 8 4 4 9 6	* *	IT RES	IT RESETS THE OPERATION	OPERATIONAL MODE, AND RESTORES THE STACKFOINTER.
		450 451 101	**	ENTRY		
		4 4 500 500	**	EXIT	TO MTR LOOP CTLFLG SET	
		4 4 6 6 6 6	* *	USES	+MFLAG CLEARED	
		456 457				
322		458	ERROR	EQU	**************************************	
21:0 21:0 21:11	41 010 040	459			A, M	(A) = , MFLAG
326	46.275	461	:	PNI	3770-UO.DDU-UO.NFR	:6
330 331		462 463		X X X X X X	ξ. T	KEFLALE
000,332 000	99 390	464 465		HUI	M,CB,SSI+CB,M CTLFLG-,MFLAG	M,CB,SSI+CB,MTL+CB,CLI+CB.SPK RESTORE *CTLFLG* CTLFLGMFLAG-1
334		466		: a	á Hác và	
5555 240::	52 035 040	46/		SPHL	7) LOIL	"RESTOKE STACK POINTER TO EMPTY STATE
341	15 136 002	469		CALL	ALAKM	ALARM FOR 200 MS
		471	* * *	Σ Τ Ι	- MONITOR LOOF.	
		473	*	THIS I	S THE MAIN EXEC	THIS IS THE MAIN EXECUTIVE LOOP FOR THE FRONT PANEL EMULATOR.
000.344		475 476	X T.R.	EGU	*	
00.344	373	477		H		
345	41.345.000	479	MTRI	LXI	H;MTR1	SET 'MIR1' AS RETURN ADDRESS
351	01.007.040	481		LXI	B. DSPMOD	(BC) = #DSFACD
000,355	346.001	4.4 483 583		ANI	G →	(A) = 1 IF ALTER
360	62.006.040	484 485		STA	DSPROT	ROTATE LED PERIODS IF ALTER
		486	*	- READ KEY	EY	
363	26	4 4 9 9 9 9 9 9 9		CALL	RCK ABUSS	READ CONSOLE KEYPAD
371	376 012 322 005 001	491		CPI	10 MTR4	IF IN 'ALWAYS VALID' GROUF
376):):):	493		MOV	E,A	SAVE VALUE
000.377 0	012	4 95 4 95	•	LIPAX	B	(A) = DSPMOD
		0 1		3.		THE PERSON MODE

13:23:37 01-AFR-77 FAGE 13	MOV ArE (A) = CODE	HAVE.A.COMMAND.(NOT.A.WALUE)		ERROR E,A		0.40	E M (Hall)	DAD D (H.L.) = ADDRESS OF PROCESSOR XTHL SET ADDRESS. (H.) = (ARDES)	D, REGI (D, E) = ADDRESS OF	DSFNOE	2 SET	RET JUMP TO PROCESSOR		A X III	DB GO-* 4 - 60 DB IN-* 5 - INFUT	- 9 9 9 9 9 9	メールコーのの	WMEM-* 9 - CASSETTE	1 i + 1	ABORT-* *	DB KEME* + L DISPLAY/ALTER	REGM-*		FROCESS MEMORY/REGISTER ALTERATIONS.		THIS CODE IS ENTERED IF	1) AM IN ALTER MODE, AND			A,E	
		*	MTR4											MTRA										**	:	* *	:	:	MTRS		
Ē.	498	494 000	501 502		505	507	505	010	i	513	515	517	5.518 5.18	520	521 522	523	: :::0 ::0 ::0 ::0	526	527	529	531	532		534	533	536	538	 540	541	542	543
MTR - MAIN EXECUTIVE LOOP.			004	32	0.35 001	000.			005 040		002																			047 001	3
XECU	173		326	332	345	.026	136	343	021	012	346	311			165 141	143	250 250 250 250 250 250 250 250 250 250	332	104	102	116	034	:	:	:		:	:	017	173 332	1
	001.004		005	007	013:	.017.	022	023		030	.031			035	001.035 1	037	041	042	0443	045	:	050								001.052 1	

Section Sect	.PAMZ8HB.FRONI.PANEL.MONIJOR MTR - MAIN EXECUTIVE LOOP.		0.000		HEATH X8ASM V1,0 02/18/27 13:23:39 01-APR-77 PAGE 14
942 024 040 553 SAE SHLD ABUSS 311 555 * ALTER REGISTER 556 HTR6 PUSH FSW 345 347 003 560 CALL LRA 342 322 000 561 10x H ANA ANA ASRON 343 362 003 565 JMP 10A 363 062 003 565 JMP 10A		: : :		SIOKE ABUSS AND. (HL) = ABUSS V TO (RET)	. EXIT VALUE
3655 * ALTER REGISTER 365 315.047 003 559 MTR6 PUSH FSW 247 247 561 ANA A 247 247 562 562 ANA A 341 322 000 562 JZ FRROR 361 564 INP IDA 363 062 003 565 JMP IDA	042 024 040 311	S.	SHLD RET	ABUSS	
345 97 003 550 MTK6 PUSH FSW 315 047 003 560 CALL LRA 341 322 000 562 JR AROR 361 JR HSW 361 JR HSW 363 062 003 565 JMF IDA		:	ALTER	REGISTER	
247 561 ANA A 342 322 000 562 JZ FRROK 563 INX H H H H H H H H H H H H H H H H H H H	365	Σ	PUSH	PSW LRA	SAVE CODE LOCATE REGISTER ADDRESS
363 FDNX H	247	561 562	ANA JZ.	A ERROR	NOT ALLOWED TO ALIER STACKFOINTER
	043 361 303 062 003	563 564 565	INX POP PN PN	H FSW IOA	RESTORE VALUE AND CARRY FLAG INPUT OCTAL ADDRESS

75.9 ** REGN - RHIER REGISTER DISPLAY REGISTER MODE	CITHTR-// THUE ID
S71	
002 574 REGH HUI A:2 002 576 STAX BSPHOD 003 578 KRA 013 578 KRA 014 6.2 015 578 KRA 015 578 KRA 025 581 CALL 025 582 CALL 025 582 CALL 026 CALL 027 582 CALL 028 584 CALL 028 584 CALL 029 584 CALL 020 585 CALL 020 586 CALL 020 587 KRA 020 600 ** KRA 020 600 ** KRA 020 600 KRA 020 600 KRA 020 600 KRA 030 CALL 030 C	
STAX BSPMODD STAX	ISTER MODE
002 576 STAX BSPMOD-DSPROT-1 013 578 DEKNO BSPMOD-DSPROT-1 257 STAX B 315 260 003 581 CALL RCK 075 582 CALL RCK 075 583 CALL RCK 022 583 CALL RCK 022 584 DECK 022 585 STAX B 311 588 R\$W R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W R\$W R\$W R\$W - TOBGLE DISPLAY/ALT 590 *** R\$W	
013 578 100	ISTER MODE
315 260 003 580 STAX B 315 260 003 581 CALL RCK 376 506 582 CPL 6 322 322 000 584 JNC ERROR 002 585 CPL 6 002 585 CPL 6 002 585 CPL 6 002 586 RET REGI 311 589 ** R*W - TOGGLE DISPLAY/ALT 591 ** RET REGI 592 ** R*W - TOGGLE DISPLAY/ALT 594 ** R*W - TOGGLE DISPLAY/ALT 595 ** R*W - TOGGLE DISPLAY/ALT 596 ** R*W - TOGGLE DISPLAY/ALT 597 ** R*W - TOGGLE DISPLAY/ALT 598 ** R*W - TOGGLE DISPLAY/ALT 599 ** R*W - TOGGLE DISPLAY/ALT 590 ** R*W - TOGGLE DISPLAY/ALT 599 ** R*W - TOGGLE DISPLAY/ALT 590 ** R*W - TOGGLE	
315 260 003 581 CALL RCK 375 260 582 DCR A 376 583 CFI 377 ERCI 378 585 CFI 378 ERCI 379 *** K*W - TOGGLE DISPLAY/ALT 591 *** K*W - TOGGLE DISPLAY/ALT 592 ** K*W - TOGGLE DISPLAY/ALT 593 ** K*W - TOGGLE DISPLAY/ALT 594 *** K*W - TOGGLE DISPLAY/ALT 595 ** K*W - TOGGLE DISPLAY/ALT 597 *** K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 594 *** K*W - TOGGLE DISPLAY/ALT 594 K*W - TOGGLE DISPLAY/ALT 595 *** K*W - TOGGLE DISPLAY/ALT 597 K*W - TOGGLE DISPLAY/ALT 597 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 599 *** K*W - TOGGLE DISPLAY/ALT 597 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 599 *** K*W - TOGGLE DISPLAY/ALT 597 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 598 K*W - TOGGLE DISPLAY/ALT 599 K*W - TOGGLE DISPLAY/ALT 590 K*	NO
075 086 582 DUCK 6 322 322 000 584 JNC ERROR 002 586 RLC DSP AND B STAX D STA	
322 322 000 584 JNC ERROR 0022 585 RLC 0022 586 STAX 311 588 REGI 591 ** REGI 592 ** REGI 593 ** REGI 594 ** REGI 594 ** REGI 595 ** RET 595 ** RET 596 SET 597 ** RET 598 SET 599 ** RET 599 ** RET 599 ** RET 599 ** RET 599 SET 599 ** RET 599 SET	
022 585 STAX D 587 SET REGI 511 588 RÉT 590 **	
311 588	
590 ** K\$W - TOGGLE DISPLAY/ALT 591 ** ENTRY (A) = DSPMOD 592 ** ENTRY (A) = DSPMOD 593 ** ENTRY (A) = DSPMOD 593 ** ENTRY (A) = DSPMOD 594 SET DSPMOD 130 002 597 K*W KET 131 311 598 K*W (HL) = (ABUSS) 602 ** ENTRY (HL) = (ABUSS) 603 ** ENTRY (HL) = (ABUSS) 604 ** NEXT - INCREMENT DISPLAY 605 ** ENTRY (HL) = (ABUSS) 604 ** ENTRY (HL) = (ABUSS) 605 ** ENTRY (HL) = (ABUSS) 606 ** ENTRY (HL) = (ABUSS) 607 ** ENTRY (HL) = (ABUSS) 608 ** IS REGISTER MODE. 608 ** IS REGISTER MODE. 608 ** IS REGISTER MODE. 609 ** IS REGIST	
590 **	
992 * ENTRY (A) = DSPHOD 593 * 595 * ENTRY (A) = DSPHOD 594 . SET 685 0 695 696 . SET 696 ENTRY (BC) = ADDRESS 0 602 . SPR 601 598 ENTRY (HL) = (ABUSS) 603 * ENTRY (HL) = (ABUSS) 603 * ENTRY (HL) = (ABUSS) 603 * ENTRY (HL) = (ABUSS) 604 . SPR 604 . SPR 605 . SPR 606	
007 594 SET DSFMOD 126 356 001 596 K\$W XRI 130 002 599 K\$W XRI 131 311 599 KET 600 ** NEXT - INCREMENT DISPLAY 601 * ENTRY (HL) = (ABUSS) 602 * ENTRY (HL) = (ABUSS) 603 * ENTRY (HL) = (ABUSS) 604 * ENTRY (HL) = (ABUSS) 605 * ENTRY (HL) = (ABUSS) 606 * ENTRY (HL) = (ABUSS) 607 * ENTRY (HL) = (ABUSS) 608 * ENTRY (HL) = (ABUSS) 609 * ENTRY (HL) = (ABUSS) 600 * ENTRY (HL) = (ABUSS) 600 * ENTRY (HL) = (ABUSS) 601 * ENTRY (HL) = (ABUSS) 602 * ENTRY (HL) = (ABUSS) 603 * ENTRY (HL) = (ABUSS) 604 * ENTRY (HL) = (ABUSS) 605 * ENTRY (HL) = (ABUSS) 606 * ENTRY (HL) = (ABUSS) 607 * ENTRY (HL) = (ABUSS) 608 * ENTRY (HL) = (ABUSS) 609 * ENTRY (HL) = (ABUSS) 600 * ENTRY (HL) = (ABUSS)	
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131 311 598 RET 600 ** NEXT - INCREMENT DISPLAY 601 * 602 * 603 * (DE) = ADDRESS 0 604 RET 132 043 605 NEXT INX H 133 312 063 001 606 JZ SAE 608 * IS REGISTER MODE. 609 SET REGI 136 032 611 LDAX D 137 306 002 611 LDAX D 141 022 613 STAX D 142 376 014 614 CPI 12 615 665	
600 ** NEXT - INCREMENT DISPLAY 601 * 602 * ENTRY (HL) = (ABUGS) 603 * (DE) = ADDRESS 0 604 * INX H 605 NEXT INX H 606 NEXT INX H 607 SET REGISTER MODE. 608 * IS REGISTER MODE. 609 SET REGI 609 SET REGI 609 SET REGI 609 SET REGI 610 CPI 611 CPA 612 ADI 613 STAX 614 CPI 615 CPI 616 CPI 617 CPI 618 CPI 619 CPI	
601 * 602 * 602 * 602 * 602 * 602 * 602 * 603 * 604 * 605 * 604 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 * 605 *	
602 * ENTRY (HL) = (ABUSS) 603 * (DE) = ADDRESS 0 604 NEXT INX H 605 NEXT INX H 606 SOS 001 606 JZ SAE 608 * IS REGISTER MODE. 608 * IS REGISTER MODE. 608 * GET REGI 609 SET REGI 600 611 HDAX D 611 ADIT C 612 ADIT C 613 STAX D 614 SZ6 014 615 REGI 615 ADIT C 616 STAX D 617 ADIT C 618 SZ6 014 615 REGI 619 ADIT C 619 SZ76 SZ6 615 SZ6 D 619 SZ76 SZ6 SZ6 615 SZ6 D 619 SZ76 SZ6 SZ6 615 SZ6	
132 943 604 NEXT INX H 133 312 063 001 606 JZ SAE 607 608 * IS REGISTER MODE. 609 8ET REGI 136 932 611 LPAX D 141 922 613 STAX D 142 376 014 614 CFI 12 144 330 615 KC	
133 312 063 001 606 JZ SAE 607 IS REGISTER MODE. 608 * IS REGISTER MODE. 609 SET REGI 136 032 610 EDAX 137 306 002 611 EDAX 141 022 613 STAX 142 376 014 614 EPI 144 330 46 615 REGI 15	
608 * IS REGISTER MODE. 609	ARUSS AND EXIT
136 032 610 SET REGI 136 032 611 LDAX D 137 306 002 612 ADI 2 141 022 613 STAX D 142 376 014 614 CPI 12	
135 932 611 LDAX D 137 306 002 612 ADI 2 141 922 613 STAX D 142 376 014 614 CPI 12 142 330 4615 RC A	
137 306 002 612 ADI 2 141 922 613 STAX D 142 376 014 614 CPI 12 152 330 14 615 KC	
142 376 014 614 CPI 12 144 330 615 RC A	4NEX
7	++>0
T 124 010 101 101	1. FA.
.146. 022 617 STAX .147 311 618 ABORT RET	

STATE STAT	
-222 303 063 000 679 GD JHP GO225 681 ** SSTEP - SINGLE STEP INST 682 * ENTRY NONE 683 * ENTRY NONE 684 * ENTRY NONE 685 STEP EDU * -225 363 686 DI -225 363 686 DI -225 363 540 699 SST1 FRTN - SINGLE STEP RET -240 341 040 699 SST1 FRTN - SINGLE STEP RET -244 362 020 699 SST1 FRTN - SINGLE STEP RET -244 362 020 699 SST1 FRTN - SINGLE STEP RET -244 362 020 699 SST1 FRTN - SINGLE STEP RET -244 325 040 040 699 STT FRTN - SINGLE STEP RET -244 325 040 040 699 STT FRTN - SINGLE STEP RET -244 336 040 040 040 040 040 040 040 040 040 04	
481 ** STEP - SINGLE STEP INST 682 ** ENTRY NONE 682 ** ENTRY NONE 683 ** ENTRY NONE 684 ** ENTRY NONE 685 STEP EQU * 685 STEP EQU * 686 D1 640 688 XRI CB. SSI 686 XRI CB. SSI 687 LPLG 687 LPR CB. SSI 688 XRI CB. SSI 689 SST1 CLFLG 691 692 CST1 CLFLG 694 ** STPRTN - SINGLE STEP RET 694 ** STPRTN - SINGLE STEP RET 694 ** STPRTN - SINGLE STEP RET 694 ** STPRTN - SINGLE STEP RET 695 STPRTN - SINGLE STEP RET 696 STPRTN - SINGLE STEP RET 697 CTL 697 CTL 697 CTL 697 CTL 698 CTPRTN EQU 689 CTL 699 SST1 CTLFLG 690 SST1 CTLFLG 690 CTL 690 CTL 691 CTL 691 CTL 692 CTL 693 CTL 694 ** STPRTN - SINGLE STEP RET 695 STPRTN - SINGLE STEP RET 696 CTL 697 CTL 697 CTL 698 CTL 699 SST1 CTLFLG 690 CTL 690 CTL 6	
225 363 468 ** SSTEP - SINGLE STEP INST 682 ** ENTRY NONE 683 ** ENTRY NONE 684 685 SSTEP EQU * 625 362 686 687	ROUTINE IS IN WASTE SPACE
\$\begin{array}{cccccccccccccccccccccccccccccccccccc	RUCTION.
225 363 684 587EF EQU ** 225 363 686 587EF EQU ** 225 363 686 687 LDA CTLFLG 688 XRI CB.SSI CTLFLG 689 CTLFLG 689 CTLFLG 689 CTLFLG 690 SST1 STA CTLFLG 690 SST1 STA CTLFLG 690 SST1 STA CTLFLG 691 STA CTLFLG 691 STA CTLFLG 692 GP2 GP3 TO THE TAIL CTLFLG 692 GP3 TO THE TAIL CTLFLG 693 GP4 ** STFRTN - SINGLE STEF RET 693 GP4	
225 363 686 101	
231 356 026 688	
-231 356 020 688 XRI CB.SSI -233 323 360 689 ST1 CF.FLG -240 341 691 POP CTLFLG -241 303 172 000 692 JMP INTXIT -244 366 020 698 STFRTN - SINGLE STEP RET 694 ** STFRTN - SINGLE STEP RET 695 STFRTN - SINGLE STEP RET 244 366 020 698 OUT CB.SSI -246 323 360 698 OUT CB.STL -251 346 040 701 ANI CB.MTL -251 346 040 701 ANI CB.MTL -253 303 042 040 703 JMP UIVEC+3 -254 042 040 703 SHLD TFERRX -264 042 031 040 709 SHLD TFERRX -264 042 031 040 709 SHLD TFERRX	JISABLE INTERRUPTS UNTIL THE RIGHT TIME
241 363 172 000 699 SST1 FOR CTLFLG 694 SST1 STA CTLFLG 694 SST1 STA CTLFLG 1240 699 SST1 STA CTLFLG 1341 303 172 000 692 JMP INTXIT 694 ** STFRTN - SINGLE STEP RET 695 STFRTN EQU * 696 STFRTN EQU * 696 STFRTN EQU * 697 STAX DUT 699 STAX DUVEC+3 CTLFLG 1251 346 040 701 ANI 68 HT	CLEAR SINGLE STEP INHIBIT
244 363 360 699 SST1 STA CTLFLG 244 366 020 692 JMF INTXIT 244 366 020 699 SFRTN EQU * 244 368 020 699 SFRTN EQU * 244 368 020 699 SFRTN EQU * 255 302 344 000 000 SFT SFRTN EQU * 255 302 344 000 000 SFT SFRTN EQU * 255 302 344 000 000 SFT SFRTN EQU * 255 302 344 000 000 SFT SFRTN EQU * 255 302 344 000 000 SFT SFRTN EQU * 256 303 342 040 000 SFT SFRTN EQU * 257 368 699 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 342 040 000 SFT SFRTN EQU * 258 303 042 031 040 000 SFT SFRTN EQU * 259 SFRTN EQU * 250	RIME SINGLE STEP INTERRUPT
241 303 172 000 692 JMP INTXIT 244 366 020 695 STPRTN EQU * 244 366 020 699 OUT OF CTL 011 699 SET CTLFLG 251 346 040 701 ANI CB.MTL 253 302 344 000 702 JMZ MTR 254 303 042 040 703 JMP UIVECH3 255 303 042 040 703 JMP UIVECH3 256 303 042 040 703 JMP UIVECH3 257 041 244 002 708 KMEM LXI HITABT 264 042 031 040 709 SHLD TPERKX 264 042 031 040 709 SHLD TPERKX 265 303 042 031 040 709 SHLD TPERKX 265 304 042 031 040 709 SHLD TPERKX 266 042 031 040 709 SHLD TPERKX	SET NEW FLAG VALUES
244 366 694 ** STPRTN - SINGLE STEP RET 695 695 676 876	CLEAN STACK RETURN TO USER ROUTINE FOR STEP
244 366 020 696 STPRTN EQU * 244 366 020 697 081 CB.SSI 246 323 360 698 0UT 0P.CTL 250 022 040 701 ANI CB.MTL 255 302 344 000 702 UNZ MTR 255 303 042 040 703 UNZ MTR 255 303 042 040 703 UNZ MTR 256 303 042 040 703 WZ MTR 264 041 244 002 708 KMEM LXI H; FABT 264 042 031 040 709 SHLD TPERKX 710 * UMP LOAD	Z.
244 366 020 697 0RI CB.SSI 244 323 360 698 0UT OF.CTL 011 0P.CTL 250 022 700 STAN D 251 346 040 701 ANI CB.MTL 255 303 042 040 703 JMP UIVEC+3 255 303 042 040 703 JMP UIVEC+3 256 303 042 040 703 JMP LXI H,TFABT 261 041 244 002 708 RMEM LXI H,TFABT 264 042 031 040 709 SHLD TFERX 710 * JMF LOAD	
250 022	DISABLE SINGLE STEP INTERRUPTION
250 022 700 STAX DILLLLUGG STAX DILLLUGG STAX DILLUGG STAX DI	TURN OFF SINGLE STEP ENABLE
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256 303 042 040 703 JMP UINEC+3 705 ** RMEM - LOAD MEMORY FROM 706 * 261 041 244 002 708 RMEM LXI H; FRABT 264 042 031 040 709 SHLD TPERKX 710 * JMP LOAD	SEE IF IN MONITOR MODE
705 ** RMEM - LDAD MEMORY FROM 706 * 207 ** TXI HJTFABT 264 042 031 040 709 SHLD TFERX 264 042 031 040 710 * JMP LDAD	TRANSFER TO USER'S ROUTINE
705 ** RMEM - LOAD MEMORY FROM 706 ** 706 ** 707 707 707 708 RMEM LXI H+TFABT 264 042 031 040 709 8HLD TFERX 710 * JMF LOAD	
261 041 244 002 708 RMEM LXI H;TFABT 264 042 031 040 709 SHLD TPERRX 710 * JMP LOAD	APE.
264 042 031 040 709 RMEM LXI HTFABT 264 042 031 040 709 SHLD TPERRX 710 * JMF LOAD	
710 * OMP LOAD	THIS COURSE ASSESSED
	SEIUF EKKUK EXII AUIKESS

HEATH X8ASM VI.1 06/21/77 15:44:12 01-AFR-77 FAGE 12	LOAD MEMORY FROM TAPE.	READ THE NEXT RECORD FROM THE CASSETTE TAPE.	LOAD ADDRESS IN THE TAPE RECORD.		USER F-REG (IN STACK) SET TO ENTRY ADDRESS TO CALLER IF ALL ON	ERRUR EXIL IF LAFE ERRURS DETECTED.		10006-RT.MI*256-256 (BC) = - REGUIRED TYPE AND #	S SCAN FOR RECORD START (HL) = COUNT	(DE) = COUNT, (HL) = TYPE AND # C (C) = - NEXT #			JA SAVE TYPE CODE			ERR IF NOT RIGHT TYPE OR SEQUENCE		(BC) = P-REG ADDRESS			SET P-REG IN MEM		READ ADDRESS	SET (HL) = ADDRESS, (DE) = COUNT		READ BYTE	JSS SET ABUSS FOR DISPLAY			A TE MORE TO GO		CHECK TAPE CHECKSUM	31 OCK	(A) = FILE TYPE BYTE	(BC) = -(LAST
ō	LOAD - LOAD	READ THE NE	USE THE LOA	ENTRY CHL	EXII			:	CALL SRS		:	PUSH B	:		:	UNE TPERR		MOV C. A	:	CALL LRA.	#07 #00 #,0	INX H		ANDV LAA	:		SHLD ABUSS	INX		ORA E	JAK	CALL	READ NEXT BLOCK		
#01.00.00	* *	:	:	:	**	*	LOAD	:	LOAO																:	LOA1							*		
MONITOR TAPE	712	714	716	718	719	722	724	725	726	728	730	732	733	734	736	737	739	740	:	:	747	746	748	÷	i	752	754	755	757	758	9,5,	761	763	764 765	766
ANEL FROM	:							975.999	8					77	0.2	05.002	00		¥.	52 003			25 002	000		331 002	24 040			0	344.001	72.002			
FRONT P				:		:				353 015	011	174 305		346 1		302			325	315 0	321 161	160	315 3				042 0	043	172	263		315.1		361	301
РАМ/8 — НВ F1 LOAD — LOAD)										001.276																4 K	W	M:	0 IO	001.354	3	001,360			001.364

FAM/8 - H8 FRONT PANEL MONITOR #01.00.00.	.01.00.00.	HEATH X8ASH VI.1 06/21/77 15:44:21 01-APR-77 PAGE 20
001,366 332 133 002 768 001,371 393,272 001 769	JC TFT JMP. LDAO.	
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PAM/8 - HB DUMF - DUMF	FRONT	PANEL	MAG/PAFE	7 F	101.00.00.			HEATH X8ASM VI.0 02/18/77 13:23:47 01-APR-77 PAGE 21	
				: :		DUMP	DUME MEMORY TO	MAG TAPE.	
		:	72		<u> </u>	DUMF. SFE	ECIFIED MEMORY.	SPECIFIED MEMORY RANGE TO MAG TAFE.	
				:	Ш	ENTRY	(START) = STAR	T ADDRESS	
		:	777	* * *	: :	EXIT	(ABUSS) = END / USER PC = ENTR' TO CALLER.	= END ADDRESS = ENTRY FOINT ADDRESS R.	
:	:	:	3,2	•	:				
001.374	041	244 0	0.25	WHEN 33		EQU	* H,TPABT	ATTAC CONTRACTOR	
	:	0.51	5: 5:	4 : Μ	n:	SHLE	LFERRA	SELUT EKKUK EALI	
	:		78	SE DUMP	:	MŲI	A, UCI, TE		
			78	Σ α	c) Σ	00T	OF.TPC A,A.SYN	TAPE	
002.010	046	040	78	36 10 10 10 10 10 10 10 10 10 10 10 10 10		MOI CAL	H, 32	(H) = # OF SYNC CHARACTERS	
	:		:			1. 1. 1.			
	:	012	90.	ŭ,iù	3) E	JNZ	WME 1 A, A, STX	WALLE STA HELLER	
	:	024	500	4.	0	CALL	EX.3	WRITE STX	
		027		ស៍ភ	ac on	SHLD	L,H CRCSUM	(HL) = 00 CLEAR CRC 14	
	:	001	:	, , , , , , , , , , , , , , , , , , ,		LXI	H.RT.MI+80H*256+	6+1 FIRST AND LAST MI RECORD	
	- :	017	2003	φ: <u>c</u>	<u></u>	CALL	4.23 4.23 4.23 4.23 4.23 4.23 4.23 4.23	WRITE HEADER	
		000		, 0	×L	CHG	0 BK	(D,E) = STARI ADDRESS	
	:	024 0	04	<u> </u>	→ ⊦	LHLD	ABUSS	(H*L) = STOP ADDR COMBUTE WITH STOP+1	
	:	:	8	w.w	+.Σ	MOV	A,L		
	:		φ.α	4 ii	ம :Σ	SUB	ш <u>.</u>		
			8	3 20	Σ	¥0.	I, A		
	:) & 	, 8	ທΣ	SEE	₩. 1.	100 = CON	
	:	017	003 80	0.00		ALL	よれる	WRITE COUNT	
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		7	81 007 81	Σ	Æί	70K	L'A	(HL) = CONTENTS OF PC	
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	:	017	003 82			CALL	ENF.		
	:		ο ο	22 5.8 · · · 1086#5	:	MOU	X		
2.10	31	024	03	3		OV ALL	: A	WRITE BYTE	
002.110	0.0 4.4 1.0 4	024		វូស្វ	r.co ⊢	SHLD	ABUSS	SET ADDRESS FOR DISPLAY	
•	5		3	0	4	<u>۲</u>	E .		

889 ** CIC - VERIFY CHECKSUM. 881 * 883 * 884 * 884 * 885 * 885 * 886 * 886 * 887 * 887 * 888 * 889 * 889 * 889 * 889 * 889 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 880 * 88	FEAD NEXT FAIR RETURN OF OK CHECKSUM ERROR (B) = CODE FROR. ILOW BYTE OF ABUSS DONY ALLOW *
315 325 002 885 ** 885 ** 885 ** 886 ** 886 ** 886 ** 886 ** 887 ** 889 CTC 052 027 040 893 056 001 893 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899 ** 899	READ NEXT FAIR READ NEXT FAIR RETURN OF OK CHECKSUM ERROR (B) = CODE ERROR. LLOW BYTE OF ABUSS DONY ALLOW #
315 325 002 886 87 886 886 886 886 886 886 886 887 887	READ NEXT FAIR RETURN OF OK CHECKSUM ERROR (B) = CODE (B) = CODE ERROR. LOW BYTE OF ABUSS DONY ALLOW *
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174 265 310 076 001 893 894 ** 897 ** 899 ** 890 ** 800 **	RETURN OF OK CHECKSUM ERROR (B) = CODE (R) = CODE (R) = CODE FROR. FROR. DONY MLLOW **
265 310 076 076 002 062 062 062 062 062 062 06	RETURN OF OK CHECKSUM ERROR (B) = CODE (B) = CODE ERROR. LOW BYTE OF ABUSS ALLOW *
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900 * 901 * 901 * 902 * 903 * 904 905 TPERR 107 906 TPERR 107 909 910 *	DON'T ALLOW *
902 * 904 904 904 905 TPERK 107 024 040 906 TPERK 315 133 002 908 910 *	
904 904 905 024 040 906 TPERK STA 107 907 MDU 315 133 002 908 CALL 909 IS #,	
062 024 040 906 TPERR STA 107 907 MDU 315 133 002 908 CALL 909 IS #,	
315 133 002 908 CALL 909 910 * IS *	(B) # CODF
* SI *	TÜRN OFF TAPE
	TY ERROR)
346 912 DB MI.ANI 170 913 TER3 MOV A,B	FALL THROUGH WITH CARRY CLEAR
914 915 RRC	
30	KETUKN IF OK
	NUMBER
334 136 002 920 TER1 CC	ALAKM IF PROPER TIME
333 360 904 741 CHEL 333 360 922 IN	0th 1t
376 057 923 CPI	CHECK FOR *
00 5	/C' SET IF 1/2 SECOND

**************		,			13;23;32 01-AFK-// FAGE 24
		929	*	TPABT	- ABORT TAPE LOAD OR DUMP.
		931 931 932	* * *	ENTERE IS STR	ENTERED WHEN LOADING OR DUMFING, AND THE '*' KEY IS STRUCK,
244	257	933	TPABT	XRA	Æ
245	323 371 303 322 000	936		UNT UNF	OP.TPC OFF TAPE ERROR
		939	* *	TFXIT	TPXIT - CHECK FOR USER FORCED EXIT.
		941	·**	TFXIT THE TAM	TPXIT CHECKS FOR AN '*' KEYPAD ENTRY. IF SO, TAKE THE TAPE DRIVER ABNORMAL EXIT.
		943	**	ENTRY	NONE
		945	**	EXIT	TO *KET* IF NOT **: (A) = PORT STATUS
		947	* *	USES	TO (TPERKX) IF *** BOWN
:		949	:		
:		951	TFXIT	X1.	IP.FAD
	333 371	953		I.V.	IP.TPC READ TAPE STATUS
:	•	954		RNE	
002.264	052 031 040 351	956		FCHL	IPERKX ENTER (TPERRX)
		. 958. 959	* * *	3 3. S	SKS SCAN RECORD START
		960	**	SKS KE	SRS READS BYTES UNTIL IT RECOGNIZES THE START OF A RECORD.
		962	*	THIS RE	THIS REQUIRES
		963 964 965	***	AT LEAS	AT LEAST 10 SYNC CHARACTERS 1 STX CHARACTER.
		996	* *	THE CRO	THE CRC-16 IS THEN INITIALIZED.
		896	***	· ENTRY ·	
		970	* * *	EXT	IAFE FUSITIONEH (ANI MUVING), CRUSUM =0 (ME) = HEADER BYTES (MA) = REFORD CHINT
		972	*	USES	
		974		100	*
002.265 0	026 000 142	976	SRS1	1 1 2 X	D 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
270		٠,			

002-321 315-331 002 979 SRS2 GAL, RMR READ WEXT RITE 002-324 378-026 989 GRS
257 312 271 002 982 UE SKS2 256 002 984 UNE SKS1 112 372 265 002 986 WU 113 372 265 002 988 UND 115 372 265 002 988 UND 116 372 265 002 988 UND 117 372 042 027 088 UND 118 372 265 002 990 UND 118 372 365 002 990 UND 119 372 042 027 040 989 UND 119 372 042 027 040 989 UND 119 372 042 027 040 989 UND 119 373 124 992 WU 119 1001 WU 119 1010 WU 119 W
995
17 272 986 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987 987
112 322 265 002 988 JNC SKS1 NOT END 115 042 027 040 989 5HLD CRCSUM 124 137 992
12
224 137 992 ** MOV E+A 992 ** MOV E+A 995 ** RNP - READ NEXT PAIR. 995 ** RNP - READ NEXT PAIR. 996 ** RNP READS THE NEXT TWO BYTES FR 998 ** RNP READS THE NEXT TWO BYTES FR 999 ** ENTRY 1000 ** ENTRY 1001 **
995 ** RNF - READ NEXT FAIR, 996 ** RNF - READ NEXT TWO BYTES FR 998 ** RNF - READ NEXT TWO BYTES FR 999 ** ENTRY NONE 1001 ** USES A,F,H 1001 ** USES A,F,H 1006 ** UNF RNB READS THE NEXT BYTE 1006 ** UNF RNB READS THE NEXT BYTE 1006 ** UNF RNB READS THE NEXT BYTE 1007 ** RNB - READ NEXT BYTE 1008 ** RNB - READ NEXT BYTE 1009 ** RNB READS THE NEXT SINGLE BYTE 1001 ** ENTRY NONE 1001 ** ENTR
995 ** RNP - READ NEXT FAIR. 997 * RNP READS THE NEXT TWO BYTES FR 998 * RNP READS THE NEXT TWO BYTES FR 998 * EXIT (H:A) = RYTE FAIR 1001 * 1002
996 *
999 * ENTRY NONE 1000 * LOSES A,F,H 1001 * LOSES A,F,H 1002 1003 330 147 1004 RNP CALL RNB READ NE 1006 * LOSES A,F,H 1010 * RNB READS THE NEXT SINGLE BYTE 1010 * RNB READS THE NEXT SINGLE BYTE 1011 * EXIT (A) = CHARACTER 1011 * EXIT (A) = CHARACTER 1014 * EXIT (A) = CHARACTER 1015 * USES A,F 1016 1016 333 323 371 1019 335 325 302 1020 RNB1 CALL TEXIT
999 * ENTRY NONE 1000 * LSES A+F+H 1001 * USES A+F+H 1002 1003 330 147 1004 RNP CALL FNB READ NE 1006 * LMP RNB READ NEXT BYTE 1006 * LMP RNB READ NEXT BYTE 1000 * RNB READ NEXT BYTE 1010 * RNB READS THE NEXT SINGLE BYTE 1011 * RNB READS THE ROWE FOR THE CALL THE CHECKSUM IS TAKEN FOR THE CALL TO THE CALL
1002
325 315 331 002 1005
325 315 331 002 1004 RNP CALL FNR READ NE S30 147 1005 * JMP RNB READ NE READ NE TO NE 1006 * LOS RNB - READ NEXT BYTE 1010 * RNB READS THE NEXT SINGLE BYTE 1010 * THE CHECKSUM IS TAKEN FOR THE CHECKSUM IS TAKEN FOR THE CONTRY NONE 1011 * EXIT (A) = CHARACTER 1011 * EXIT (A) = CHARACTER 1011 * USES A) F CHARACTER 101
##
1008 ** RNB - READ NEXT BYTE 1010 * RNB READS THE NEXT SINGLE BYTE 1011 * THE CHECKSUM IS TAKEN FOR THE C 1013 * ENIT (A) = CHARACTER 1014 * EXIT (A) = CHARACTER 1015 * USES A) F 1016 333 323 371 1019 WUI OP, IFC 335 315 252 002 1020 RNBI CALL IPXII
10008 ** RNB - READ NEXT BYTE 1010 * RNB READS THE NEXT SINGLE BYTE 1011 * THE CHECKSUM IS. TAKEN FOR THE C 1013 * ENTT (A) = CHARACTER 1014 * EXIT (A) = CHARACTER 1015 * USES A,F 1016 ** HO! A,UCI.RO+UCI.ER+UCI.RE 333 323 371 1019
1010 * RNB READS THE NEXT SINGLE BYTE 1011 * THE CHECKSUM IS TAKEN FOR THE G 1012 * ENTRY NONE 1013 * EXIT (A) = CHARACTER 1014 * EXIT (A) = CHARACTER 1015 * USES A,F 1016 * HVI A,UCI.RO+UCI.ER+UCI.RE 333 323 371 1019 UUT OP.TEC 335 315 252 002 1020 RNB1 CALL TEXIT CHECK F
1011 * THE CHECKSUM IS TAKEN FOR THE G 1012 * ENTRY NONE 1014 * EXIT (A) = CHARACTER 1015 * USES A,F = CHARACTER 1016 * USES A,F = CHARACTER 1016 * USES A,F = CHARACTER 1017 * USES A,F = CHARACTER 1018 * WVI A,UCI.KO+UCI.ER+UCI.RE 333 323 371 1019 * UVI OP.TEC CHECK F 335 315 252 002 1020 RNB1 CALL TEXIT
1012 * 1013 * 1013 * 1013 * 1014 * EXIT NONE 1014 * EXIT (A) = CHARACTER 1015 * 1016 * 056 A*F = CHARACTER 1016 * 056 A*F = CHARACTER 1016 * 0017 A*UCI.*ROUCI.*ER+UCI.*RE 333 323 371 1019 0UT 0F.TFC CHECK F 335 315 252 002 1020 KNR1 CALL TFXIT CHECK F
1014 * EXIT (A) = CHARACTER 1015 * USES A) F = CHARACTER 1016 331 076 064 1018 RNB MVI A,UCI,RO+UCI,ER+UCI,RE 333 323 371 1019 CUT QP,TEC CHECK F
1015 * USES A.F 1016 331 076 064 1018 RNB MUI A.UCI.RO+UCI.ER+UCI.RE 333 323 371 1019 0UT 0P.TFC 335 315 252 002 1020 RNBI CALL TEXIT
331 076 064 1018 RNB MVI A.UCI.RO+UCI.ER+UCI.RE 333 323 371 1019 0UT 0F.TFC CHECK F
333 323 371 1019 0UT 0F.TFC CHECK F
335 315 252 002 1020 RNB1 CALL TPXIT CHECK FOR ** READ
340 340 002 1041 HAI USK+KAR
342 312 335 002 1022 JZ RWB1
1024 **

10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.00 10.0	FROM THE POLYNOMIAL S A DIVISION REMAINDER, BE HEDITIN
1030 ** 1031 ** 1033 ** 1033 ** 1033 ** 1034 ** 1035 ** 1035 ** 1035 ** 1035 ** 1035 ** 1035 ** 1035 ** 1035 ** 1037 ** 1037 ** 1038 ** 1038 ** 1039 ** 1040 ** 1041 ** 1041 ** 1042 000 000 000 000 000 000 000 000 000	S A DIVISION REMAINDER,
1032 ** 1033 ** 1033 ** 1033 ** 1034 ** 1034 ** 1035 ** 1035 ** 1036 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1040 ** 1041 ** 1042 ** 1043 ** 1044 ** 1045 ** 1046 ** 1052 345 007 004 004 004 004 005 005 005 005 005 005	S A DIVISION REMAINDER,
1033 ** 1034 ** 1035 ** 1035 ** 1035 ** 1037 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1038 ** 1039 ** 1040 ** 1041 ** 1044 CRC 1045 CRC 1045 CRC 1046 CRC 1051 CRC 1052 CRC 1053 CRC 1052 CRC 1053 CRC 1054 CRC 1055 CRC 1056 CRC 1057 CRC 105	DE MEGLETER DV DINING
1036 ** 1037 ** 1037 ** 1037 ** 1037 ** 1038 ** 1038 ** 1041 ** 1044 ** 1044 ** 1044 ** 1044 ** 1044 ** 1044 ** 1044 ** 1045 ** 1045 ** 1046 ** 1050 ** 1050 ** 1050 ** 1051 ** 1052 ** 1053 ** 1053 ** 1054 ** 1055 ** 1057 ** 1057 ** 1057 ** 1058 ** 1059 ** 1050 ** 1050 ** 1051 ** 1051 ** 1052 ** 1053 ** 1054 ** 1055 ** 1056 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 1057 ** 105	EL VENTITIED DI NOMMING RUNNING THE PREVIOUSLY OBTAINED TÂNT FOLEKRIN RICH N. Y. Y.
1039 * EXIT (CRCSUM) UFDATED 1040 * (A) = BYTE 1041 * (B) = BYTE 1042 * (A) = BYTE 1043 * (A) = BYTE 1044 * (A) = BYTE 1044 * (A) = BYTE 1044 * (A) = BYTE 1045 * (A) = BYTE 1045 * (A) = BYTE 1046 * (A) = BYTE 1047 * (A) = BYTE 1048 * (A) = BYTE 1049 * (A) = BYTE 1040 * (A) = BYTE 1041 * (A) = BYTE 1	Checkbor Should be O.
1039 * EXIT (CRCSUM) UFDATED 1040 *	ED8V.
347 305 1041 * USES F H WILLINGERS F 1042 * 1042	
347 305 1043 CRC FUSH B SAVE S52 345 1044 CRC FUSH B F B SAVE HVSI H F B B SAVE S52 345 1046 FUSH H F B B SAVE CRCSUH CRCSUH CRCSUH CRCSUH CRCSUH CRCSUH CRCSUH AU A L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L A L L L L A L L L L A L L L A L L L A L L L L A L L L L A L L L L L A L L L L L L L L A L L L L L L L L L A L L L L L L L L A L L L L L L L L A L L L L L L A L L L L L L L L L L L L L L L L L L L L	
350 006 010 1045	
353 052 027 040 1047	BIT COUNT
356 007 1048 CRC1 RLC 6,4 (C) = 1049 CRC1 RLC 1049 CRC1 RLC 1050 MUV 6,4 (C) = 1050 MUV 6,4 (C) = 1050 MUV 6,4 (C) = 1051 MUV 6,4 (C) = 1052 MUV 6,4 (C) = 1053 MUV 6,4 (C) = 1054 MUV 6,4 (C) = 1055 MUV 6	
357 117 1049 MOV C,A (C) = 360 175 1049 MOV C,A (C) = 361 120 1050 MOV C,A (C) = 362 157 1055 MOV C,A (A,H A) C,C (C) = 362 157 1055 MOV C,A (A,H A) C,C (C) = 364 027 1055 MOV C,C (C) C,C (C	
1051 1051 1052 1052 1052 1052 1052 1052 1053 1053 1054 1053 1054 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055 1055	
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1053 MOV A+H 1053 MOV A+H 1054 1055 MOV H+A 1055 MOV M	
1055	
1056 NA C C C C C C C C C	
XFA C	
771 322 004 003 1059 JNC CRC2 IF NOT 77 132 004 003 1059 JNC CRC2 IF NOT 75 356 200 MOV A,H MOV A,H MOV A,L MOV A,L MOV A,L MOV A,C MO	
7.5 356 200 1060 MUV A,H 7.5 356 200 1061 MV H,A 7.7 147 1062 MUV A,L 7.8 1063 MOV A,L 7.8 1064 MOV L,A 7.8 1066 CRC2 MOV A,C 7.8 1067 MOV A,C 7.9 1067 MOV A,C 7.9 1068 MOV A,C 7.9 1069 MOV A,C 7.9 1070 MOV A,C	TO XOR
7.77 147 1062 MOV H,A 00 175 1063 MOV A,L 03 1356 005 1064 XRI 50 04 171 1066 CRC2 MOV A,C 05 005 1068 MOV A,C 06 302 356 002 1068 MOV A,C 11 042 027 040 1069 SHLD CRCSUM 14 341 1070 PDF B RESTORE 16 331 1072 RFT	
00 175 1063 MOV A,L 01 356 005 1064 XRI 50 03 157 1065 MOV L,A 04 171 1066 CRC2 MOV A,C 05 005 1067 DCR B 06 302 356 002 1068 JNZ CRC1 11 042 027 040 1069 SHLD CRCSUM 13 301 1071 POP B 15 301 1072 RFT E	
03 157 1065 MOV L,A 04 171 1066 CRC2 MOV A,C 05 005 1067 1067 LCR 11 042 027 040 1069 SHLD CRCSUM 14 341 1070 POP H RESTORE 15 301 1071 POP B RESTORE 16 311	
04 171 1066 CRC2 MOV A,C 05 005 1067 1068 B 04 342 356 002 1068 SHLD CRC5UM 14 341 1071 POP B 16 311 1072 RFT ESTORE	
10 30 356 002 1068 JNZ CRC1 IF MORE 11 042 027 040 1069 SHLD CRCSUM RESTORE 15 301 1071 POP B RESTORE 16 311 1072 RFT	
11 042 027 040 1069 SHLD CRCSUM 14 341 1070 POP H RESTORE 15 301 1071 POP B RESTORE 16 311 1072 RFT	
15 301 1071 FUF H RESTORE 16 311 1072 RFT E RESTORE 16 311 1072 RFT EVIT); -;
16 311 1072 RFT EVIT	(量)
· · · · · · · · · · · · · · · · · · ·	(BC)

1074 ** WINTER MEXT FAIR. 1075 *	HEATH X8ASM VI.0 02/18/77 13:23:58 01-AFR-77 FAGE 27
1075 * WFT WRITES THE NEXT TWO 1077 * ENTRY (H,L) = BYTES 1078 * ENTRY (H,L) = BYTES 1078 * ENTRY (H,L) = BYTES 1081 WN GOV GALL WNB 1082 WN WNB CALL WNB 1085 WNB CALL WNB 1086 * WNB CALL WNB 1087 * WNB CALL TEXT 1088 WNB CALL TEXT 1089 W	
1029 * ENTRY (H,L) = BYTES 1079 * EXIT WRITTCN, 1079 * EXIT WRITTCN, 1081 WNP HOU A,H 1083 WNP HOU A,H 1084 WNP HOU A,H 1086 * WNB HOU A,H 1086 * WNB HOU A,L 1086 * WNB HOU A,L 1087 * WNB HOU A,L 1089 * WNB HOU A,L 1089 * WNB HOU A,L 1089 * WNB HOU HOUR BYTE 1089 * WNB HOU A,L 1089 WNB HOU HOUR HOUT HE HOUT HOU	BYTES TO THE CASSETTE DRIVE.
1280 * USES 616 1081 1081 1083 1083 1084 1085 1086 1086 1086 1086 1086 1086 1086 1087 1089 1089 1089 1089 1089 1089 1089 1089	
174 1083 WNP MOU A'H 315 024 003 1084 CALL WNB 175 1086 ** UMP CALL WNB 1086 ** UMP WNB 1089 ** WNB - WRITE BYTE 1089 ** WNB WRITES THE NEXT BYTE 1097 ** EXIT NONE. 1097 ** EXIT NONE. 1097 ** EXIT NONE. 1099 WNB CALL TPXIT 346 001 1099 WNB CALL TPXIT 353 371 1102 WNB CALL TPXIT 353 371 1105 WNB CALL TPXIT 353 371 WNB CALL TPXIT 353 WNB CALL TPXIT 354 WNB CALL TPXIT 355 WNB CALL TPXIT 355 WNB	
175 1085 * MUN	
1088 ** WNB - WRITE BYTE 1089 * 1089 ** 1089 ** WNB WRITES THE NEXT BYTE 1081 ** ENTRY (A) = BYTE 1084 ** USES F 1084 ** USES F 1085 F 1085 F 1085 F 1086 WNB CALL FFXIT 346 001 1086 WNB CALL FFXIT 346 001 1099 WNB CALL FFXIT 347 001 1103 WNB CALL FFXIT 353 371 1102 WNB CALL FFXIT 353 371 1103 WNB CALL FFXIT 354 WNB CALL FFXIT 355 WNB CALL FFXIT 357 WNB CALL FFXIT 357 WNB CALL FFXIT 358 WNB CALL FF	BYTE
1097 * WIND WRITES THE NEXT BYTE 1091 * ENTRY (A) = BYTE 1093 * ENTRY (A) = BYTE 1094 * USES 1094 * USES 1094 * USES 1094 * USES 1095 WNB FUSH 1096 WNBI CALL TPXIT 1097 WNB FUSH 1098 WNBI CALL TPXIT 1102 WNBI CALL TPXIT 1103 WNBI CALL TPXIT 1104 WNBI CALL TPXIT 1105 WNBI CALL 1106 WNBI CALL 1107 WNBI CALL 1108 FOR FSW 1109 WNBI CALL 1109 WNBI	
1091 * ENTRY (A) = BYTE 1093 * EXIT 1094 * USES F 1095 * USES F 1095 * USES F 1096 WNB CALL FFXIT 346 001 1099 WNB1 CALL FFXIT 346 001 1099 WNB1 CALL FFXIT 346 001 1099 WNB1 CALL FFXIT 347 002 1100	SSETTE TAPE.
1093 * EXII NONE: 1094 * USES F 1095	
1094 * USES F 1095 WNB FUSH FSW 315 252 002 1098 WNB1 CALL TFXIT 315 025 003 1100 JN WNI 323 371 1101 WVI A-UCI.FE 323 371 1102 DUT DF.TPC 361 1103 FOF FSW 363 370 1104 DUT OF.TPC 363 370 1104 DUT OF.TPC 363 370 1105 DUT OF.TPC 363 371 1105 DUT OF.TPC 363 377 002 1105 JMF CRC	
1096 365 1097 315 326 301 326 302 303 346 361 362 363 363 363 363 363 363 363	
315 252 002 1098 WNB1 CALL TPXIT 346 001 1099 ANI USR-TXR 346 001 1099 ANI USR-TXR 076 021 1100 MUI A-WB1 076 021 1101 MUI A-WB1 323 371 1102 DUT 0P-FPC 361 1103 POP PSU 363 347 002 1105 UMP CRC	
312 025 003 1100 JZ WWB! ON TE COMPANY OF TE COMPANY ON THE COMPANY OF TE COMPANY OF T	CHECK FOR *, READ STATUS
076 021 1101 MUI A,UCI.ERHUCI.TE 323 371 1102 DUT DF.TFC 323 370 1103 DUT DF.TFD 303 347 002 1105 UMP CRC	0.0
363 371 1103 FOF FSW 323 370 1103 OUT OF TFD	ZONI TIER
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1169 ** HORN 1169 ** FINE COTAL DISPLAY. 1169 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1172 ** FINE COTAL DALUE 1174 ** FINE COTAL DALUE 1175 ** FINE COTAL DALUE 1175 ** FINE COTAL DALUE 1176 ** FINE COTAL DALUE 1177 ** FINE COTAL DALUE 1178 ** FINE COTAL DALUE 1179 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1170 ** FINE COTAL DALUE 1171 ** FINE COTAL DALUE 1172 ** FINE COTAL DALUE 1173 ** FINE COTAL DALUE 1174 ** FINE COTAL DALUE 1175 ** FINE CO	11.00 14.0 10.2 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00 11.00	3.112 302 070 003 116 3.115 076 017 116	NO	10B1 A,30/2	IF NOT DONE BEEF FOR 30 MS
1168 ** DOD DECOLE FOR OCTAL DISPLAY. 1169 ** ENTRY (H-L) = ADDRESS OF LED REFERENH AREA 1170 ** EXIT (H-L) = ADDRESS OF LED REFERENH AREA 1171 ** (A) = ADGR* PATTERN TO FORCE ON BARS 1172 ** EXIT (H-L) = NEX DIDIT ADDRESS 1173 ** EXIT (H-L) = NEX DIDIT ADDRESS 1174 ** USES A **B C **P ATTERN TO FORCE ON BARS 1175 ** EXIT (H-L) = NEX DIDIT ADDRESS 1175 ** OS	1168 ** 100 - DECOLE FOR OCIAL DISPLAY. 1170 *	017 700 047 000 71140		2000	
1168 ** 1000 DECOME FOR OCTAL DISPLAY. 1170 * ENTRY (H.L.) = ADDRESS OF LED REFRESH AREA 1171 * EXIT (H.L.) = ADDRESS OF LED REFRESH AREA 1172 * EXIT (H.L.) = NEX DIGIT ADDRESS 1174 * USES A.B.C.D.H.L. 1175 * USES A.B.C.D.H.L. 1175 * USES A.B.C.D.H.L. 1176 DODA/256 1177 DODA/256 1178 MVI D.DODA/256 1179 MVI D.DODA/256 1179 MVI C.3 ADDRESS OF LED REFRESH AREA 1176 DODA/256 1179 MVI D.DODA/256 1170 DODA/256 1170 DODA/25	1169 ** FORT (H.L.) = ADDRESS DE LED REFRESH AREA 1170 ** ENTRY (H.L.) = ADDRESS DE LED REFRESH AREA 1172 ** EXIT (H.L.) = ADDRESS DE LED REFRESH AREA 1172 ** EXIT (H.L.) = NEX POTICE ON BARS 1172 ** USES A.B.C.PI-H-L DIST ADDRESS 1173 ** USES A.B.C.PI-H-L DIST ADDRESS 1174 ** USES A.B.C.PI-H-L DIST ADDRESS 1175 DOD TO				
1167 * ENTRY (H,L) = ADDRESS OF LED REFRESH AREA 1172 * EXIT (H,L) = ADDRESS OF LED REFRESH AREA 1173 * EXIT (H,L) = REX DOIT ADDRESS 1173 * EXIT (H,L) = REX DOIT ADDRESS 1174 * USES A,BFC,D,H,L 1175 * MYI 1176 * MYI 1177 * MYI 117	110	τ−1;•	101 ·	DECOLE	DISPLAY.
1171 *	1171 * (B) = *06* PATTERN TO FORCE ON BARS 1172 * (A) = 007AL VALUE 1173 * EX.17 (H.L.) = NEX DIGIT ADDRESS 1174 * USES A.B.C.D.H.L DIGIT ADDRESS 1175 0.20 0.3 11.79 PUSH D. DODA/256 1175 0.20 11.79 PUSH D. DODA/256 1175 0.20 11.79 PUSH D. DODA/256 1175 0.20 11.79 PUSH PSW SAVE FOR NEXT DIGIT ADDRESS 1175 0.20 11.80 PUDL FALL FALL FALL FALL FALL FALL FALL FA			(H)T)	S OF LED REFRESH AREA
1172 * (A) = OCTAL VALUE 1174 * (B) = OCTAL VALUE 1174 * (B) = OCTAL VALUE 1174 * (B) = OCTAL VALUE 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175 1175	1172 ** (A) = 0CTAL VALU 1173 ** USES AFB,C,D,H+L 1174 ** USES AFB,C,D+H+L 1175 100 FUSH D 1176 AFL 1177 100 FUSH D 1177 100 FUSH D 1180 D0D1 RAL 1181 RAL 1181 RAL 1182 365 1183 AFC,D+H+L 1181 RAL 1181 RAL 1182 AFL 1183 AFL 1184 AFL 1185 AFL 1185 AFL 1186 AFL 1187 AFL 1188 AFL 1188 AFL 1189 AF	,		(B) =	N TO FORCE ON BARS
1174 * USES A,B,C,D,H,C, L175 100D MOIS 1176 1177 100D MOI C,3 1176 1177 100D MOI C,3 1176 1177 100D MOI C,3 1179 1179 1179 1179 1179 1179 1179 117	1174 * USES AFB,C,D,HC, Ling Ling Ling Ling Ling Ling Ling Ling	~~! `~~		#: (♥)	LUE GIT Applicase
1175 1176 1177 1177 1177 1177 1177 1177	1175 1176 1177 1177 1177 11010 1177 11010 1177 11010 1177 11010 1177 11010 1177 11010 1177 11010 1177 11010 1177 11010 1177 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 1107 11	-		AYBYC	OI HEENESS
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			120	:	UFD -	UPDATE FRONT	PANEL DISFLAYS.
			120	գր. * *			
			120		UFD I	S CALLED BY TH	UFD IS CALLED BY THE CLOCK INTERRUPT PROCESSOR WHEN IT IS
	:		120	:	MAT L	TO UPDATE THE	DISPLAY CONTENTS, CURRENTLY, THIS IS DONE
			120	:	EVERT	32 INTERKUPIS	OR ARUU! 32 TIMES A SECOND.
			121	**	ENTRY	(H,C) =	ADDRESS OF REFCNT
			121	:	USES	:	
	:		121	:			<i>Y</i> .
ਜ: ਅ:	:	:	121	5 UFD	EGU	*	
003,161	076 240	002	121	:	MVI ANA	A,UO.DDU B	
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0; t	√ • €		7.0		200	D, G	
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4.0	031	0 ‡	-i -		LAI	H, USFA	
3,21	176		1238	:		E C	(11th) - HUUKESS UF KED NAME FALLEKNS
3.21	043		1239		××I	Ι	
003.217	146		1240) 0 0 2 2	E d	(H.)) = OFG NAME DATTEDN
3,22	343		1242		XTHL		:
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4 (A)	043		1244		N N	ž ČI	
222	146		1246) 00x	E.H.	
4: 4: •••): 1:		1248			# A J	(ML) = AUURESS UF REGISTER PAIR CONTENTS
	:		1.7.4.9 7.10.10	*	SETUP	DISFLAY	
3.22	365		1251	UFD1	PUSH	P.S.W	
003,230	353	7	1252		XCHG	Carlo	
0.10 M.C.	172	01.3.040	4.4		LXT MOU	HAREDS	
3.2	1 10	122,003	4 14		CALL	1011	FORMAT ARANK HIGH HALF
42.5	173		1256		¥0¢	A, E	(4,1)
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PAM∕8 - H8 FRONT PANEL MONITOR # UFD - UPDATE FRONT PANEL DISPLAYS	#01,00.00. 5.	HEATH X8ASM VI.0 02/18/77 13:24:04 01-APR-77 PAGE 31
	LDAX D JZ DOD IF MEMORY * IS REGISTER, SET REGISTER NAME.	IF MEMORY, DECODE BYTE VALUE SISTER NAME,
003,251 066 377 1264 003,253 341 1265 003,254 042 022 040 1265 003,257 311	MVI M,3770 POP H SHLD DLEDS+1 RET	CLEAR DIGIT

FOM S - HB FRONT FANEL MONITOR RCK - READ CONSOLE KEYFAD.	* :	01.00.00.		HEATH X8ASM VI.1 06/21/77 15:44:39 01-AFR-77 PAGE 32
	1271 **		- READ CONSOLE KEYPAD.	E KEYPAD.
	:	RCK	IS CALLED TO I	RCK IS CALLED TO READ A KEYSTROKE FROM THE CONSOLE KEYPAD. WHENEVER A KEY IS ACCEPTED.
	:	RCK	PERFORMS DEBOI	UNCING, AND AUTO-REPEAT, A *BIP* IS SOUNDED
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		1000	1111	
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	1294 *	00100	1111 -	
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	1315 *	USES	[10]	
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041 040			HIKCNA	
003.267 333 360	RC	K1 IN	IP.PAD	INPUT PAD VALUE

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321 017 1343	003.37	:	:	1541	7.87. 7.89.			
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TOTAL	EGMENT PATTERNS AND CONSTANTS.	6M/8 = H8 FRUNI FANEL MUNITUR #01.00.00.	AM/8 - H8 FRONT PANEL MONTOR #01.00.00.		
1359	1357 ** DISPLAY. SERVEYT. CODING: 1357 ** DISPLAY. SERVEYT. CODING: 1358 ** PITE * 76 543 210 1359 ** PITE * 76 543 210 1350	1372 1372 1373 1374 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375 1375	Col.	The color of the	TENNS FAME, MONITOR
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342 244 230 314 1372 DW	003.352 222 217 1378	### PATIENS AND CONSTANTS. ##	### FATTERNS AND CONSTANTS. #### ISST ### DISPLAY SEGMENT CODING: ##### BYTE = 76.543.210 ###################################	### FRONT PANEL, MUNITUR #01.00.00. ### FRONT PANEL, MUNITUR #01.00.00.00. ### FRONT PANEL, MUNITUR #01.00.00.00.00. ### ### ### ### ### ### ### ### ### #	### FRONT PANEL HONION #01.00.00.00. 1357 **
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342 244 230 1373 1372 1374 1374 1374 1374 1374 1375 1375 1375 1375 1375 1375 1375 1375	1357 ** DISPLAY SEGMENT CODING: 1358 * BYTE = 76 543 210 1360 * 1362 * 1364 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN 003:342 244 230 1373 DBPA DS 0 003:344 220 234 1374 DW 10011000100008 AF	### PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 15:44:42 01-AFR-77 PAGE 3 13:60 ** BYTE = 76:543.210 1360 ** BYTE = 76:543.210 1364 **	EGMENT PATTERNS AND CONSTANTS. 1356 ** DISPLAY SEGMENT CODING: 15:44:42 01-AFR-77 FAGE 3 1358 ** BYTE = 76.543 210 1360 ** BYTE = 76.543 210 1364 ** FYTE = 76.543 210 1365 ** FYTE = 76.543 210 1366 ** FYTE = 76.543 210 1366 ** FYTE = 76.543 210 1366 ** BYTE = 76.543 210 1376 ** BYTE = 76.543 210 1377 *	### FRONT FAMEL MONITOR #01.00.00. #### FRONT FAMEL MONITOR #01.00.00. ############################	## FRONT FAMEL MUNITUR #01.00.00. ### FRONT FAMEL MUNITUR #01.00.00. ### ### ### ### ### ### ### ### ### #
1352 ** BYTE = 7 1358 ** BYTE = 7 1351 ** BYTE = 7 1362 ** BYTE = 7 1364 ** BYTE = 7 1365 ** BYTE = 7 1366 ** BYTE = 7 1372 DSPA DS 1373 DW	1357 ** DISPLAY SEGMENT CODING: 1358 * BYTE = 76 543 210 1364 * 1362 * 1365 * 543 210 1365 * 543 210 1366 * 565 543 210 1367 * 666 543 210 1368 * 666 543 210 1368 * 666 543 210 1370 ** REGISTER INDEX TO 7-SEGMENT FATTERN 003:342 244 230 1374 DW 1001110001001000 8 P	EUMENI FAITERNS AND CONSTANTS. L357 ** DISPLAY SEGMENT CODING: 1359 * BYTE = 76 543 210 1360 * 1364 *	EGMENT PATTERNS AND CONSTANTS. 1350 ** 1350 ** 1350 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1360 ** 1370 ** 1370 ** 1371 ** 1371 ** 1372 ** 1373 ** 14 220 234 230 ** 15 1373 ** 16 10011000100100B SF	### FRUNT FANEL, MONITOR #01.00.00.00. EDMENT PATTERNS AND CONSTANTS. #### FRUNT FANEL, MONITOR #01.00.00.00. #########################	## FRONT FAMEL MUNITUR #01.00.00. ### FRONT FAMEL MUNITUR #01.00.00. ### ### ### ### ### ### ### ### ### #
1359 ** 1358 ** 1358 ** 1361 ** 1362 ** 1364 ** 1365 ** 1366 ** 1366 ** 1370 ** 1371 DS 1372 DSPA DS	1355 ** BYTE = 76 543 210 1360 ** BYTE = 76 543 210 1361 ** BYTE = 76 543 210 1362 ** BYTE = 76 543 210 1364 ** FEGISTER INDEX TO 7-SEGMENT PATTERN 1372 NSPA DS 0 003.342 244 230 1373 DW 1001100010018 SP	EUMENI FAITERNS AND CONSTANTS. 1358 * BYTE = 76 543 210 1360 * 1364 * 543 210 1364 * 543 210 1365 * 543 210 1365 * 543 210 1365 * 543 210 1365 * 543 210 1366 * 1366 * 1370 1370 ** REGISTER INDEX TO 7-SEGMENT FATTERN 1370 ** REGISTER INDEX TO 7-SEGMENT FATTERN 1371	EGMENT PATTERNS AND CONSTANTS. 1359 ** DISPLAY SEGMENT CODING: 1359 ** BYTE = 76 543 210 1362 ** 1364 ** 1364 ** 6003.342 1366 ** 5543 210 1366 ** 1370 ** REBISTER INDEX TO 7-SEGMENT PATTERN 1370 ** REBISTER INDEX TO 7-SEGMENT PATTERN 1371 DSPA DS 003.342 244 230 1373 DW 10011000100100B SP	### FRUNT FANEL MONITOR #01.00.00. ############################	### FRUNT FAMEL, MUNITOR #01.00.00. ############################
1359 ** 1358 ** 1359 ** 1359 ** 1360 ** 1364 ** 1365 ** 1366 ** 1366 ** 1370 ** 1372 DSPA DS	1357 ** DISPLAY SEGMENT CODING: 1358 * BYTE = 76.543.210 1361 * 1362 *	EGMENT PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1350 ** BYTE = 76 543 210 1362 ** BYTE = 76 543 210 1364 **	EGMENT PATTERNS AND CONSTANTS. 1358 ** DISPLAY SEGMENT CODING: 1358 ** BYTE = 76 543 210 1362 ** EYTE = 76 543 210 1363 ** EGISTER INDEX TO 7-SEGMENT PATTERN 1364 ** SEGISTER INDEX TO 7-SEGMENT PATTERN 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN 1371 DSPA DS 0	## FRONT FANEL MUNITOR #01.00.00. ### FRONT PATTERNS AND CONSTANTS. ### FRONT PATTERNS AND CONSTANTS. #### ### FRONT PATTERNS AND CONSTANTS. #### ### FRONT PATTERNS AND CONSTANTS. #### ### FRONT PATTERN	### FRUNT FAMEL, MUNITOR #01.00.00. ### FRUNT FAMEL, MUNITOR #0.00.00. ### FRUNT FAMEL, MUNITOR ### FAMEL # F
1352 ** 1358 ** 1358 ** 1361 ** 1362 ** 1364 ** 1365 ** 1366 ** 1366 ** 1370 ** 1371 ** 1371 ** 1371 ** 1371 ** 1371 **	1352 ** DISPLAY SEGMENT CODING: 1360 * BYTE = 76 543 210 1362 * BYTE = 76 543 210 1364 * BYTE = 76 543 210 1365 * BYTE = 76 543 210 1366 * BYTE = 76 543 210 1367 ** BYTE = 76 543 210 1368 ** BYTE = 76 543 210 1368 ** BYTE = 76 543 210 1368 ** BYTE = 76 543 210 1370 ** BYTE = 76 543 210 1371 ** BYTE = 76 543 210 1371 ** BYTE = 76 543 210 1371 ** BYTE = 76 543 210 1372 ** BYTE = 76 543 210 1371 ** BYTE BYTE = 76 543 210 1371 ** BYTE	EGMENT PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1358 * BYTE = 76 543 210 1360 * 1364 * BYTE = 76 543 210 1364 * 1365 * ATTENDEX TO 7-SEGMENT PATTERN 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN	EGMENT PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1358 * 1358 * 1357 ** 1356 * 1365 * 1365 * 1366 * 1366 * 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN 1372 DSPA DS O	### FRONT FAMEL, MONITOR #01.00.00. #### FRONT FAMEL, MONITOR #01.00.00. ############################	### FRONT FANEL MONITOR #01.00.00. EGHENT PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1359 * 1350 * 1362 * 1364 ** 1365 * 1365 * 1365 * 1365 * 1366 * 1366 * 1370 ** REGISTER INDEX TO 7-SEGMENT FATTERN 1371 DSP DSP DS O
1359 ** 1359 ** 1359 ** 1361 ** 1362 ** 1365 ** 1366 ** 1360 **	1352 ** 1358 * 1359 * 1360 * 1361 * 1362 * 1364 * 1365 * 1365 * 1365 * 1365 * 1365 * 1366 * 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN	EGMENI PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1360 * 1362 * 1364 * 1365 * 1366 * 1366 * 1366 * 1366 * 1370 ** REGISTER INDEX TO 7-SEGMENT PATTERN	### BYTE AND CONSTANTS. ### BYTE = 76 543 210 ### BYTE = 76 543 210 #### BYTE = 76 543 210 ###################################	### FRONT FAMEL, MONITOR #01.00.00. ### FRONT FAMEL, MONITOR #01.00.00. ############################	### FRONT FANEL MONITOR #01.00.00. EGMENT PATTERNS AND CONSTANTS. 1358 ** 1350 ** 1361 ** 1362 ** 1364 ** 1365 ** 1365 ** 1365 ** 1365 ** 1366 ** 1370 ** REGISTER INDEX TO 7-SEGHENT PATTERN
35.7 35.8 35.8 35.9 35.9 35.9 35.9 35.9 35.9 35.9 35.9	1352 ** DISPLAY SEGMENT CODING: 1358 ** BYTE = 76 543 210 1362 ** 666	EGMENI PATTERNS AND CONSTANTS. 1358 ** DISPLAY SEGMENT CODING: 1360 ** BYTE = 76 543 210 1362 ** 1363 ** 1365 ** 1366 ** 1366 ** 1370 ** REBISTER INDEX TO 7-SEGMENT PATTERN	EGMENT PATTERNS AND CONSTANTS. 1358 ** DISPLAY SEGMENT CODING: 1359 ** BYTE = 76 543 210 1362 ** 1364 ** 5 1365 ** 5 1370 ** REBISTER INDEX TO 7-SEGMENT FATTERN	### FRONT PANEL MONITOR #01.00,00. EGMENT PATTERNS AND CONSTANTS. 1357 ** DISPLAY SEGMENT CODING: 1358 * 1350 * 1361 * 1364 * 1365 * 1365 * 1365 * 1365 * 1365 * 1365 * 1365 * 1365 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 * 1366 *	## FRONT PANEL MONITOR #01.00.00. ## FRONT PATTERNS AND CONSTANTS. ## BYTE = 76 543 210 ## \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$
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AM/8 - HB FRONT PANEL MONITOR #01.00.00.	AM/8 - H8 FRONT PANEL MONITOR #01.00.00.				
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PAM/8 - H8 FRONT PANEL MONITOR CONSTANTS AND TABLES.	0	1,00,00,		HEATH X8ASM VI.1 06/21/77 15:44:44 01-APR-77 PAGE 35	
003.374 000 003.375 000 003.376 012 003.377 311	11406 1406 1407 1408 1409 1410	DB DB DB DB ERRNZ	0 10 MI.RET *-4000A	DSFMOD DSPROT REGI	

					10:11:11 OI HTT // THUE 30
	1413				
	1414	* *	MONITO	THE FOLLOWING ARE CONTROL MONITOR.	ONTROL CELLS AND FLAGS USED BY THE KEYPAD
040.000	1416		ORG	400004	8197
040.000	1418	START	DS	C1 C	DUNE STARTING ADDRESS
040,004	1420	FRSRAM	EGU	v *-	FOLLOWING CELLS INITIALIZED FROM ROM
**************************************	1421		ns.		RET
040.005	1423	REGI	DS	ref	INDEX OF REGISTER UNDER DISPLAY
040.000	1424	DSPROT	50 10 10 10 10 10 10 10 10 10 10 10 10 10	ਜ ਜ	PERIOD FLAG BYTE DISPLAY MODE
040.010	1426	.MFLAG	DS	-	USER FLAG OPTIONS
	1428	*	:		SEE *UO:XXX* BITS DESCRIBED AT FRONT
040.011	1430	CTLFLG	50		FRONT PANEL CONTROL BITS
000.000	1432	PRSL	EQU	*-FRSRAM	END OF AREA INITIALIZED FROM ROM
040.013	1434	FPLEDS	ERU	*	FRUNT FANEL LEU FATTERNS
040.013	1435	ALEDS	ņs	==	0
040.014	1436		DS 70	-i-	ADDR 1
× * * * * * * * * * * * * * * * * * * *	1438				
040.012	1434		200		ADDR 3
040.020	1441		ng DS	1 F1	
040.021	1442	DLEDS	25	ų.	
040.022	1444		DS.		
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040,024	1446	ABUSS	DS.	N	ADDRESS BUS
040.026	1448	RCKA	DS:	C	ROK SAVE AREA
040.031	1450	TPERM	50	4.0	TAPE FRENE FXTT ANNERS
040.033	1451	TICCNT	DS	I (N	CLOCK TIC COUNTER
040,035	1452	REGPTR	DS	CI.	REGISETE CONTENTS POINTER
040.037	1454 1455	UIVEC	DS	0	. 2
040.037	1456		IIS	М	::-
040.04M	1457			M 64	JUMP TO SINGLE STEP PROCESSOR
0.40.4050	1459		13 13 13 13 13 13 13 13 13 13 13 13 13	מיני	
040.053	1460		DS	1 01	101
040.061	1462		27.0	ว.ห	27.5
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040.064 SSFME/ V COMBUSTE	1464		ENI		
1464 STATEMENTS					
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CROSS REFI	FRENCE	TABLE.	:	XREF V1.	O PAGE	37						
CTLFLG	04001	2175	3448	4945	5138	5745	5865	5948	5609	6305	6455 6995	:
A STX	04001	344 93E	381 793	386 983	418	4 0 0	4¢5	1427L				
A SYN	000026	92E 509	788	931								
ABUSS	04002	490	(I) (650	754	801	825	906	1229	1447L		
ALARM ALFIS	040013	4.00 100 100 100 100	14304E	0.7%								
BLKSIZ	00200	ı ™.										:
CB.CLI	00000	906 986	136 331	256 415	464 464	701						:
CB.SPK	000000	100E	256	464	956	007	697					
CLK2	00023	393	395L		ri or ri							
CLK3	00023	389	398E 438E									:
CLOCK	00000	⊏ाः ।	203	3695								:
CRC	00234	1044L	1105									
CR02:	00300	1059	1066L									:
CRCSUM	04002	796	834	889	989	1047	1069	1449L				:
CTC	00217	76 1 217	888L 308	386	391	413	4	465	289	989	699 861	871
7. 7.	•	1430L	,).).								
CUI1 DLEDS	040021	345L 1266	441 1443L									:
DLY	•	248L	1328									:
E. E.		104E										
DM. RK	:	106E										
00.KE	•	107E 1177L	1255	1257	1260							
0001	•	1180L	1198									:
DODA		1178 1236	1185 1372L	1382L								
DSFMOD		481	494	513	574	576	594	645	647	1226	1425L	
DUMP	•	786	0,70	0.47		0.						
ERROR	:	298	434	458E	503	562	583	937	1153			:
FPLEDS 60		1434E 521	7629									
.09		256L	629	7								
HERO	002143	250	858	G + G +								:
HRNZ	002160	869L	870									:
INIT	00011//	192	660L 193	275L	279							:
INITO	000000	190L 2881	293									
INITZ	000117	295										
	0000010	1976										
INTS	020000	212E 229L										
INT4	000040	234L										
INTS	0000050	239L							:			:
INT6	000000	253L 240t										
\ - \ - \ - \ - \ - \ - \ - \ - \ - \ -												

	70000	565	651	1134L							
IOB IOB1	003066	545 1150L	1134	1149L	· · · · · · · · · · · · · · · · · · ·						
IOWRK IP.PAD		666 81E	667 439	1419L 922	951	1325					
IP.TPC IP.TPD		85E 87E	953 1023								
LAST LOAO		528 726L	625L 769								
LOA1 LOAD	001342 001267	752L 724E	759								
LKA LRA.	003047	560 427	1119C 743	1234	11201						
LST2 MEMM	001154	631L 531	644L	: : : : : : :	; ; ; ; ;						
MI.ANI	000346	128E 123F	912								
MI.IN	000333	125E	999								
MILXID	000021	129E	661								
MI.OUT	000323	126E	662							:	
MTR	000344	476E	702								
MTRI	000345	479	479L								
317X	001005	497	502L 5411								
MTR6	001067	543	559L								
MTRA NEXT	001035	506	520E 604L								
UP.CTL	092390	82£	489	869							
	000360	83E	400								
OP.TPC	000371	86E	304	787	845	936	1019	1102			
	000370	88E 523	1104								
-	000000	191	1432E				:				
PRSRAM	040004	191	1420E	1432							
	001126	530	1401E 595L								
	003260	489	580	1150	1319E						
	003310	1331	1337	1040							
	003326	1340	1346L								
	040028	391	1448L 1431L								
	040005	512	586	609	630	1119	1423L				
-	001104	50.00	573L								
	001261	า เก เก	40/ 708L	7771	1433L						
	002331	752	979	1004	1018L						
	002325	738	748	888	066	1004L					
RTOT	00000	114E									
RIONI	000001	1126	7.25						:		
SAVALL	000132	200 200	2 5 2 10 2 10 2 10	026 319L	0/9						
CINCE											

SRS	•	726	975E							
020 020 020 030 030 030 030 030 030 030	002265	7926 0201	984	886						
5571	:	257	7069							
SSTEP	,	524	685 E							
START		284	750	299	1418L					
STPRIN		218	696E							
TER1		920L	927							
777		74.8	7.4.4	000						
TICCNT		369	371	404	20 20 20 20 20 20 20 20 20 20 20 20 20 2	80 O	1.45.11			
TPABT	:	708	783	935L);););		4+00+4			
TPERR	:	737	7906							
TPERRX		209	784	955	1450L					
1771	•		7766	1020	1098					
OCI.ER		165E	1018	1101						
1. T.	:	1475								
UCI.RE		166E	1018							
UCI.RO	:	164F	101							
UCI.TE		168E	786	1101						
:	:	409	1215E							
UFD1	003227	1230	1251							
UIVEC	040037		234	239	253	260	348	703	1455L	
UMI + 16	X 000002	158E	303							
UMI:1R	000100	148E	303							
XI. IND	000001	157E								
UMI.ZE		1505								
T L X		1075								
UMILES		1 53F								
UMI.L6	000004	190								
UMI.L7	00000	155E								
UMI.L8	000014	156E	303		:					
UMI.PA	00000	152E								
UMITE		151E								
OO CLK	•	138E	346							
000.000	000000	13/E	461	1216						
IN NEE	:	1276	460	464						
USR.FE		172F		5						
USK.OE	:	173E								
USR. PE	•	174E								
USR. RXI		176E	1021							
USR. TXI	:	175E								
USR. TXR	\$ 000001	177E	1099							
1.00 E	•	790L	792							
EXE		842 104	7830							
122	ACC100	0.00	7045		*00,					
WYE WNB1	003025	V 0	1100	ю А	1084	10971				
- ANA	003017	``	809	818	821	835	836	1083L		

APPENDIX B

Demo: PAM-8

This program shows the advanced features of PAM-8 and, as such, should not be evaluated as either an efficient or useful routine. The program uses the H8 clock, keyboard, display and interrupt capabilities to create an accurate interval timer that lets you enter an integer value from zero through nine seconds. When the program has counted down to zero, an audio alert is sounded, ending the program and returning control to PAM-8.

Use the H8 keypad to enter the machine code, set the program counter, and execute the program. While the program is being executed, the front panel display will be turned off and the computer will wait for you to enter a digit from the keypad. A single digit corresponding to the integer you selected is displayed and decremented until control is returned to PAM-8.

The timer is typical of a program you might create. An interval timer, a clock, or even a game requires that you communicate with the H8. The keypad lets you communicate with the CPU, and the CPU uses the LED display to communicate with you. The computer understands the selected time interval when you press a decimal key on the front panel. The job status, or decremented time interval, is relayed to you by the front panel displays. This interaction between you and the machine is characteristic of most software applications.

The program uses the PAM-8 firmware. Although it appears simple enough, you must study both the program and the PAM-8 listing ("Appendix A") in order to understand what happens when the program is operating. We suggest that you take a course in assembly language programming, such as the Heath EC-1108, if you have difficulty understanding the program.

The program source listing was prepared on an H8 computer system using the text editor (TED-8) and the assembler (HASL-8). NOTE: Your programs can be handwritten and assembled if you have only an H8.

The Sample Program

This program initially blanks the LED display and waits for you to enter an integer value. The computer verifies that the value you selected is permissible and then increments and stores the integer. The value was incremented because the display routine always decrements the count by one when it is called.

The most subtle part of this program is the interrupt service routine.* The H8 requires that you initialize the interrupt service routine by loading an instruction and address into the user interrupt vector (UIVEC) before executing the interrupt. After UIVEC is initialized, the program will jump to the service routine after the next interrupt signal is generated.

The main body of the program is a "do-nothing" loop that holds the program in a wait status until the interval timer has reached zero. You could replace the loop with another program which would execute simultaneously with the clock counter. When the countdown is complete, the program returns the H8 computer to its original status before halting.

*NOTE: Basically, an interrupt is a CPU response to a control signal. This signal directs the software to automatically save the current CPU status and transfers program control to a specified routine, called an interrupt handler. When the interrupt handler completes the routine, program control returns to its original status and normal program execution continues.

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		DEMO: PAMB			
	* * *	SYSTER	SYSTEM DEFINIT)	INITIONS	:
	•				
040,100	ERROR		40100A 322A	100A 26 RESET PARB	:
0002 140	HORN		2140A	:	
003.260	RCK DODA		3260A 3356A		
040.010	. MFLAG	:	40010A	USER FLAG OFTIONS	:
040.037	FPLEDS	:	40013A 40037A	013A FRONT PANEL L.E.D. PATTERNS 0374 USER TATERRIPT OPPTOR	
000,001	UO.CLK		# # # # # # # # # # # # # # # # # # #	ALLOW C	
000,002	Udd. DM		2A 303A	MACHINE DISELAY UPDANA MACHINE INSTRUCTION	:
000,377	::b	Eau		:	
	** ***	*****	***********	*	
	* * *	DISABL AND TU	DISABLE UPDATIN AND TÜRN OFF'L;	DATING OF L.E.D. DISPLAY	
040,100 076 002	* FAMB	MUI	A,00.br	\$I.Q	
•		LXI	H, FPLED		:
006.011		MOI	B, 9		
076	BI ANK	1 0 2 X	A, LEDOF	<u>.</u>	:
		INX	I		:
•		DCR N.7	2 A A A		
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	:				:
	× × +	READ A	IF NOT DECIMAL	Z: ₹ :	:
	* * *	STO	RETHEDI	I IME INTEGER (A PRUGRAM REGUIREMENT) **E DIGIT**	
315	€.	CALL	RCK	READ CONSOLE	:
•		E.	100	TEST IF ZERO THRU NINE	
		ING	אטאאים		
	* *	014 *****	######################################	*****	
		ATTIVI			:
		PROGRAM	REG.	KEGUIRES ONE INTERRUPT BEFORE DISPLAY	
36 041 001 000	.	LXI	H, 1	H=0 8 L=1	:
040*141042.252.040.	: *	SHLD	**************************************	.X. ***********************************	:
	€ *	TNITIAL	TNITIAL 17F SFRU	ANTHLICA FALSAGETAL BOLLOGIA	:
	***	WOT:		1 K L S	:
			L #176-A. J. J. J.	ANTOLE. JOSEN. CLULD. J.R. LEKKHF. J. J.	:

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ASM	C4	
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040.144 040.146	076 303 062 037 041 207	040 040	STA	A*MI.JMF UIOEC H*INTRP	SET-UP JUMP INSTRUCTION STORE 'JMP' INSTRUCTION USER INTERRUPT ADDRESS
	042 040 076 003 062 010	: *: *:	SHLT UIVEC TOT A.UO * SHD ************************************		+1 DDU+UO.CLK G
040,164 040,164 040,171	072 254 0 376 000 302 164 0	* * * - *	WAIT FOR CLOC CFI 0 01GIT LOOF **********************************		K TO REACH ZERO DO NOTHING LOOF. MAIT FOR END OF COUNT DOWN. ***********************************
		***	RETURN		MAL INTERRUPT STATUS & HALT, Terrupt & turn on speaker
040,174 040,174 040,201 040,203 040,203	076 062 062 076 315 315 140	* * *	MVI A JUD. STA MFLA MVI A A JOO CALL HORN H.I. **********	6,UD.ppU .MFLAG 6,500/2 HORN ************************************	DDU DISABLE UPDATE & CLOCK INTERRUPT // 2 250 MS BEFP ***********************************
		* * *	INTERRUPT RO	UTINE DISPLAY	INTERRUPT
040,207 040,212 040,213	052.252 053 042.252	* .040INTRF .040.	LHLD DCX SHLD		.GET.CRUNT.(BETWEEN.Q.8.500) TICK=TICK-1 SIGRE.COUNT
040,216 040,217 040,220	175 264 300	: * : *	MOV A,L (15A RNE RNE *******************************		TEST FOR ZERO COMPGRE WITH H EXIT IF .NE. 0 ************************************
		**	UPDATE.L.E.D.		DISFLOY. FOR. 'NEW' DIGIT.
040,221 040,224 040,225		*	LDA DCR STA	DIGIT TO THE PROPERTY OF THE P	GET.INTEGER BIGIT=DIGIT-I SAPE INTEGER BEFORE DISPLAY ANDRESS
040,233	205.		A DOM	:	KI.
040.236 040.236	366 200	0.40.	ORI	2000 FPLEDS+4	##ST TURN OFF D.F. TURN-DN-THE-LIGHIS
040,243 040,244 040,251	041 364 042.252 311	0.40.	LXI SHLD RET	H,500 TICK	RESTORE COUNT WITH 500.
040.252 040.254 040.255	000	TICK	DB. END	S.LUKHUE. AKER	LAE. AREA 4. END. ASDENBL.
	OO130.STAIEMENTS.ASSEMRLED 12275 BYTES FREE .NO.ERRORS.DETECTED	MRLED			

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