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MTR90-1 - H/2-89 MONITOR #09.02.01.

015.334 315 173 015 4269 CALL CCH
015.337 077 4270 CMC
015.340 330 4271 RC
4272 IF VALD

015.341 076 007 4273 MVI A,A,BEL
015.343 315 302 003 4274 CALL WCC
015.346 030 356 4275 JR IROCH
4276

4277 * TYPE BYTE REPLACEMENT

015.350 361 4278 THB POP PSM
015.351 365 4279 THB1 PUSH PSM
015.352 346 360 4281 ANI 11100008
015.354 017 4282 RRC
015.355 017 4283 RRC

015.356 017 4284 RRC
015.357 017 4285 RRC
015.360 315 366 015 4286 CALL THB2
015.363 361 4287 POP PSM
015.364 346 017 4288 ANI 0000111B
4289

DO HIGH NIBBLE FIRST

4290 * THB1 - TYPE NIBBLE
4291

015.366 306 060 4292 THB2 ADI 10'
015.370 376 072 4293 CPI 19*+1
015.372 070 002 4294 JR C,THB3
015.374 306 007 4295 ADI 7
015.376 303 302 003 4296 THB3 JMP WCC

4297 * THA - TYPE HEX ADDRESS
4298

016.001 361 4299 THA POP PSM
016.002 076 015 4300 MVI A,A,CR
016.004 315 370 005 4301 CALL MCR.
4302

016.007 174 4303 THA1 MOV A,H
016.010 315 351 015 4304 CALL THB1
016.013 175 4305 MOV A,L
016.014 315 351 015 4306 CALL THB1
016.017 076 040 4307 MVI A,*
016.021 303 302 003 4308 JMP WCC

MEMORY - MEMORY DIAGNOSTIC

MEMORY IS THE PREFACE TO THE MEMORY

DIAGNOSTIC UTILITY

016.024 041 043 016 4315 MEMORY LXI H,MSG,HEM
016.027 315 100 006 4316 CALL TYPMSG
016.032 072 071 040 4317 MEMORY. LDA RADFLG
4318 * EXX

016.035 331 4319 DB MI,EXX
016.036 157 4320 MOV L,A
4321 * EXX

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016.037 331 4322 DB MI.EXX
016.040 303 116 007 4323 JMP DYMEH
016.043 145 163 164 4325 MSG.MEM DB 'est Memory',A,CR,A,LF,0

4327 ** GETBND - GET BOUNDRIES
4328 *
4329 * GETBND GETS THREE ADDRESS BOUNDRIES, RETURNING
4330 * THE FIRST IN HL, THE SECOND IN DE AND THE THIRD
4331 * IN BC.
4332 *
4333 *
016.060 041 003 040 4334 GETBND LXI H,IOWRK+1
016.063 026 054 4335 MVI D,'0'
016.065 315 023 015 4336 CALL IOA GET FIRST
4337
016.070 052 002 040 4338 LHL D IOWRK
016.073 345 4339 GETBND. PUSH H ENTRY POINT FOR DE,BC ONLY
4340
016.074 041 003 040 4341 LXI H,IOWRK+1
016.077 026 054 4342 MVI D,'0'
016.101 315 023 015 4343 CALL IOA
4344
016.104 052 002 040 4345 LHL D IOWRK SAVE SECOND
016.107 345 4346 PUSH H
016.110 303 024 001 4347 JMP GETBND1 CONTINUE ELSEWHERE

4349 ** INTOX0 - EXTENSION TO INTOX
4350 *
4351 * INTOX0 CLEANS UP SOME OF THE RAM CELLS
4352 *
4353 *
016.113 257 4354 INTOX0 XRA A
016.114 062 071 040 4355 STA RADFLG
016.117 041 377 377 4356 LXI H,-1
016.122 042 072 040 4357 SHLD VEHLD
016.125 001 200 076 4358 LXI B,16000
016.130 303 050 004 4359 JMP INITOX1

4361 ** BOOT7 - EXTENSION TO BOOT ROUTINE
4362 *
4363 * THIS ROUTINE HANDLES BOOTING FROM DEVICE
4364 * ZERO WITH COMMAND LINES
4365 *
4366 *
016.133 376 040 4367 BOOT7 CPI ' '
016.135 050 007 4368 JR Z,BOOT71 TYPED SPACE, MUST WANT COMMAND LINE

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016.137 376 072 4369 CPI '':
016.141 050 012 4370 JR Z,800T72 TYPE :, HERE COMES COMMAND
016.143 376 060 4371 CPI '0'
016.145 311 4372 RET OTHERWISE, MAYBE UNIT NUMBER
016.146 315 302 003 4373 800T71 CALL WCC
016.151 257 4374 XRA A ENTER CCL AS UNIT 0
016.152 303 263 013 4375 JMP CCL
4376
4377 * HE ALREADY STARTED THE COMMAND LINE, LETS CATCH UP!
4378
016.155 257 4379 800T72 XRA A
016.156 062 000 040 4380 STA START SAVE UNIT NUMBER
016.161 042 002 040 4381 SHLD IOWRK SAVE DEVICE ADDRESS
016.164 061 200 042 4382 LXI SP,42200A SET UP STACK
016.167 041 062 041 4383 LXI H,A10.DIR
016.172 016 035 4384 MVI C,PRIM-A10.DIR-1 ECHO THE COLON
016.174 076 072 4385 MVI A,: CONTINUE FROM HERE
016.176 303 343 013 4386 JMP CCL4

```

```

4388 ** EUC - ENTER USER CODE
4389 *
4390 * EUC ENTERS THE USER BOOT CODE, AFTER RE-VECTURING
4391 * THE CLOCK INTERRUPT REQUEST VECTORS
4392 *
4393 * THE H17 RAM CONSTANTS ETC. ARE ALSO MOVED IN
4394 *
4395
016.201 001 130 000 4396 EUC LXI 8,800TAL SET THE COUNT TO MOVE IN CONSTANTS AND VECTORS
016.204 021 132 037 4397 LXI D,800TA SET THE SOURCE ADDRESS
016.207 041 110 040 4398 LXI H,D.COM SET THE DESTINATION ADDRESS
016.212 315 252 030 4399 CALL $MOVE MOVE IT
4400 * ENTRY POINT FROM H17 (CONSTANTS ALREADY MOVED IN)
4401 *
4402 *
016.215 363 4403 EUC. DI STOP CLOCK
016.216 041 031 034 4404 LXI H,CLOCK17 LOAD CLOCK ROUTINE ADDRESS
016.221 042 040 040 4405 SHLD UIVEC+1 SET IT INTO VECTOR LOCATION
016.224 373 4406 EI
4407 * Zero out H67 operating system info
4408 *
4409
000.000 4410 ERRNZ S,052-S,051-2 MUST BE CONTIGUOUS BYTES
016.225 041 126 041 4411 LXI H,S,051
016.230 006 005 4412 MVI 8,1+1+3
016.232 315 212 031 4413 CALL $ZERO Zero area
4414
016.235 303 200 042 4415 JMP USERFHA GOTO BOOT CODE

```

4417 ** DYBTX

4418 *

4419 *

4420 *

4421 *

4422 *

4423 *

4424 *

4425

016.240 117

4426 DYBTX MOV C,A SAVE BYTE

4427 * EXX

016.241 331

4428 DB MI,EXX

016.242 175

4429 MOV A,L GET RADIX FLAG

4430 * EXX

016.243 331

4431 DB MI,EXX

016.244 247

4432 ANA A '2' SET IF OCTAL

016.245 171

4433 MOV A,C RESTORE A

016.246 040 012

4434 JR NZ,DYBTX IF IN HEX

4435

016.250 117

4436 DYBTX MOV C,A

016.251 346 300

4437 ANI 11000008

016.253 007

4438 RLC

016.254 007

4439 RLC

016.255 346 003

4440 ANI 00000018

016.257 303 163 003

4441 JMP DYBTX,1 FINISH UP OLD OCTAL ROUTINE

4442

4443 *

IS HEX

4444

016.262 117

4445 DYBTX MOV C,A

016.263 346 360

4446 ANI 11110008

016.265 017

4447 RRC

016.266 017

4448 RRC

016.267 017

4449 RRC

016.270 017

4450 RRC

016.271 346 017

4451 ANI 00001118

016.273 306 060

4452 ADI '0'

016.275 376 072

4453 CPI '9'+1

016.277 070 002

4454 JR C,DYBTX1

016.301 306 007

4455 ADI 7

016.303 375 041

4456 DYBTX1 DB MI,LOVA,MI,LDY8

016.305 312 016

4457 DM DYBTX2

016.307 303 143 003

4458 JMP DYASC SET RETURN ADDRESS

4459

016.312 171

4460 DYBTX2 MOV A,C

016.313 346 017

4461 ANI 00001118

016.315 306 060

4462 ADI '0'

016.317 376 072

4463 CPI '9'+1

016.321 070 002

4464 JR C,DYBTX3

016.323 306 007

4465 ADI 7

016.325 375 041

4466 DYBTX3 DB MI,LOVA,MI,LDY8

016.327 334 016

4467 DM DYBTX4

016.331 303 143 003

4468 JMP DYASC

4469

4470 *

4471 JP (IX)

016.334 335 351

4472 DYBTX4 DB MI,JIXA,MI,JIX8

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4474 ** DYMMS
4475 *
4476 * DETERMINE NUMBER OF BACKSPACES
4477 * TO TYPE FOR EACH CHARACTER OUTPUT
4478 *
4479 *

016.336 331 4480 DYMMS DB MI.EXX
016.337 175 4481 MOV A,L
016.340 331 4482 DB MI.EXX
016.341 247 4483 ANA A
016.342 076 010 4484 MVI A,A,BKS
016.344 046 003 4485 H,3
016.346 312 221 007 4486 JZ DYM5.5
016.351 045 4487 DCR H
016.352 303 221 007 4488 JMP DYM5.5

4490 ** CONVERT - BASE CONVERSION
4491 *

4492 * CONVERT CONVERTS THE INPUT IN THE OPPOSITE
4493 * RADIX AND CHANGES IT TO THE CURRENT RADIX
4494 *
4495 *

016.355 041 014 017 4496 CONVERT LXI H,MSG.CON
016.360 315 100 006 4497 CALL TYPMSG
016.363 041 003 040 4498 LXI H,IOWRK+1
016.366 026 015 4499 MVI D,A,CR
016.370 315 112 015 4500 CALL CHKRAD
016.373 050 005 4501 JR Z,CONV.0 IF OCTAL
4502

016.375 315 062 003 4503 CONV.H CALL IOAD
017.000 030 003 4504 JR CONV.E
4505

017.002 315 245 015 4506 CONV.0 CALL IHA.
4507

017.005 052 002 040 4508 CONV.E LHLD IOWRK
017.010 353 4509 XCHG
017.011 303 064 015 4510 JMP TOA
4511

017.014 157 156 166 4512 MSG.CON DB 'convert',0

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 H17 Extension routine 10:43:46 17-FEB-82

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4515 ***      H17X - H17 Extension routine
4516 *
4517 *      H17x is the extension to the H17 Abort command
4518 *
4519 *
017.024 315 366 033 4520 H17X CALL R.ABORT
4521
4522 *      Step the head out 10 tracks
4523
017.027 315 044 002 4524 CALL R.SDP      Set up device
017.032 076 012 4525 MVI A,10
017.034 062 240 040 4526 STA D,17      Set target track to 10
017.037 315 166 040 4527 CALL D.SDT      Seek Desired track
4528
017.042 303 366 033 4529 JMP R.ABORT      Abort and return
  
```

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MTR90-1 - H/2-89 MONITOR #09.02.01.
Command Descriptor table

MTR - COMMAND DESCRIPTOR TABLE

THIS TABLE CONTAINS THE SINGLE LETTER COMMANDS
UNDERSTOOD BY MTR90. THE ENTRIES IN THIS TABLE
CONSIST OF A SINGLE LETTER (THE COMMAND KEY) FOLLOWED
BY A WORD ADDRESS OF THE ROUTINE.

NOTE: THIS TABLE WAS MOVED FROM UP FRONT BECAUSE
OF SIZE CONSIDERATIONS

4532	**	MTR	EQU	*	
4533	*		DB	'G'	*GO*
4534	*		DW	G088	
4535	*				
4536	*				
4537	*				
4538	*				
4539	*				
4540	*				
4541	*				
4542	*				
4543	MTR	EQU	*		
4544	*	DB	'G'		*GO*
4545	*	DW	G088		
4546	*				
4547	*	DB	'S'		*SUBSTITUTE*
4548	*	DW	SUBM		
4549	*				
4550	*	DB	'P'		*PROGRAM COUNTER*
4551	*	DW	PCA		
4552	*				
4553	*	DB	'B'		*BOOT*
4554	*	DW	BOOT		
4555	*				
4556	*	DB	'I'		*INPUT*
4557	*	DW	INPUT		
4558	*				
4559	*	DB	'O'		*OUTPUT*
4560	*	DW	OUTPUT		
4561	*				
4562	*	DB	'R'		*RADIX*
4563	*	DW	RADIX		
4564	*				
4565	*	DB	'T'		*TEST RAM*
4566	*	DW	MEMORY		
4567	*	DB	'V'		*VIEW*
4568	*	DW	VIEW		
4569	*				
4570	*	DB	'C'		
4571	*	DW	CONVERT		
4572	*				
4573	MTRAL	EQU	*MTR/3		
000.012					

NUMBER OF ENTRIES /JMT 790507/

81170, 81174 - 8001 TABLES

THESE TABLES DEFINE DEVICE DEPENDENT INFORMATION USED
TO DETERMINE WHICH DEVICE IS TO BE BOOTED FROM

THE ORGANIZATION OF THE TWO TABLES IS IDENTICAL:

BYTE 1 - PORT NUMBER OF THESE DEVICES
BYTE 2 - DEVICE 0 IMFG

4575	**				
4576	*				
4577	*				
4578	*				
4579	*				
4580	*				
4581	*				
4582	*				
4583	*				
4584	*				

	4585 *	BYTE 3	-	MAX UNITS
	4586 *	BYTE 4,5	-	BOOT CODE ADDRESS
	4587 *			
	4588 *	DEVICE 1 TMFG		
	4589 *	BYT 6	-	MAX UNITS
	4590 *	BYT 7	-	BOOT CODE ADDRESS
	4591 *	BYT 8,9	-	
	4592 *	etc., etc., thru DEVICE 3		
	4593 *			
	4594 *	NO END-OF-TABLE CHECK IS MADE; THEREFORE, 4 ENTRIES MUST EXIST PER TABLE		
	4595 *			
	4596 *			
	4597			
	017.103 174	BTH74 DB	174Q	PRT ADDRESS
	017.104	BTH74E EQU *		
	4600			
	017.104 000	BTH174 DB	0	TMFG = 0
	017.105 063	DB	H37	MAX UNIT = 3
	017.106 207 002	DW H17		BOOT ADDRESS
	4603			
	4604			
	017.110 001	BTH474 DB	1	
	017.111 064	DB	H47	
	017.112 372 001	DW Z47		
	4607			
	4608			
	017.114 000	BTH674 DB	0	
	017.115 064	DB	H47	
	017.116 174 012	DW H67		
	4611			
	4612			
	017.120 000	BTHFE4 DB	0	
	017.121 061	DB	H17	
	017.122 222 013	DW FEDEV		
	4615			
	4616			
	000.004	BTHL EQU *-BTHLE/H	INSURE ALL ENTRIES FILLED	
	000.000	ERRNZ BTHL-H-		
	4618			
	4619			
	017.124 170	BTHO DB	170Q	PRT ADDRESS
	017.125	BTHOE EQU *		
	4621			
	4622			
	017.125 000	BTH370 DB	0	
	017.126 064	DB	H47	
	017.127 221 011	DW H37		
	4625			
	4626			
	017.131 001	BTH470 DB	1	
	017.132 064	DB	H47	
	017.133 372 001	DW Z47		
	4629			
	4630			
	017.135 000	BTH670 DB	0	
	017.136 064	DB	H47	
	017.137 174 012	DW H67		
	4633			
	4634			
	017.141 000	BTHFEO DB	0	
	017.142 061	DB	H17	
	017.143 222 013	DW FEDEV		
	4637			
	4638			
	000.004	BTHLOL EQU *-BTHLOE/H		
	000.000	ERRNZ BTHLO-L-		
	4639			
	4640			

MTR90-1 - H/Z-89 MONITOR
Command Descriptor table

MUST NOT EXCEED 4K BYTES

ERRMI 2000A--*

4642

000.233

4645 ** THE FOLLOWING ARE CONTROL CELLS AND FLAGS USED BY THE KEYS
4646 * MONITOR.
4647

040.000 4648 ORG 40000A 8192
040.000 4649 START DS 2 DUMP STARTING ADDRESS
040.002 4650 IOWRK DS 2 IN OR OUT INSTRUCTION
040.004 4651 PRSRAM EQU * FOLLOWING CELLS INITIALIZED FROM ROM
040.004 4652 DS 1 RET
040.004 4653

040.005 4654 REGI DS 1 INDEX OF REGISTER UNDER DISPLAY
040.006 4655 DSPROT DS 1 PERIOD FLAG BYTE
040.007 4656 DSPMOD DS 1 DISPLAY MODE
040.007 4657

040.010 4658 *FLAG DS 1 USER FLAG OPTIONS
040.010 4659 * SEE *UO.XXX* BITS DESCRIBED AT FRONT
040.010 4660

040.011 4661 CTLFLG DS 1 FRONT PANEL CONTROL BITS
040.012 4662 REFINO DS 1 REFRESH INDEX (0 TO 7)
000.007 4663 PRSL EQU *-PRSRAM END OF AREA INITIALIZED FROM ROM
000.007 4664

040.013 4665 FPLEDS EQU * FRONT PANEL LED PATTERNS
040.013 4666 ALEDS DS 1 ADDR 0
040.014 4667 DS 1 ADDR 1
040.015 4668 DS 1 ADDR 2
040.015 4669

040.016 4670 DS 1 ADDR 3
040.017 4671 DS 1 ADDR 4
040.020 4672 DS 1 ADDR 5
040.020 4673

040.021 4674 DLEDS DS 1 DATA 0
040.022 4675 DS 1 DATA 1
040.023 4676 DS 1 DATA 2
040.023 4677

040.024 4678 ABUSS DS 2 ADDRESS BUSS
040.026 4679 RCCA DS 1 RCC SAVE AREA
040.027 4680 CRCSUM DS 2 CRC-16 CHECKSUM
040.031 4681 TPERRX DS 2 TAPE ERROR EXIT ADDRESS
040.033 4682 TICCNT DS 2 CLOCK TIC COUNTER
040.033 4683

040.035 4684 REGPTR DS 2 REGISCTR CONTENTS POINTER
040.035 4685

040.037 4686 UIVEC DS 0 USER INTERRUPT VECTORS
040.037 4687 DS 3 JUMP TO CLOCK PROCESSOR
040.042 4688 DS 3 JUMP TO SINGLE STEP PROCESSOR
040.045 4689 DS 3 JUMP TO I/O 3
040.050 4690 DS 3 JUMP TO I/O 4
040.053 4691 DS 3 JUMP TO I/O 5
040.056 4692 DS 3 JUMP TO I/O 6
040.061 4693 DS 3 JUMP TO I/O 7
040.061 4694

040.064 4695 ** H88/H89 RAM USAGE BEYOND THAT OF H8MTRF
040.064 4696 *
040.066 4697 NHIREI DS 2
040.066 4698 DATA DS 1 OUTPUT TO 362Q DATA SAVE
040.067 4699 BLKICH DS 2 H37 INTERRUPT RETURN ADDRESS
040.071 4700 RADFLG DS 1 RADIX FLAG
040.071 4701

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 RAM CELLS 10:43:53 17-FEB-82

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040.072      4701 VEHLD DS      2
000.001      4702 IF          .DEBUG
            4703 DBFLG DS      1      FOR DEBUG RESULTS
            4704 ENDIF
040.074      4705 MEHL EQU      *
001.024      4706 ERRMI
041.120      4707 ORG      41120A-MEHL
041.120      4708 PRIM DS      1      PRIMARY DEVICE ADDR. PORT
041.121      4709 IMFG DS      1      TIMER INTERRUPT FLAG, =1 FOR Z47, =0 FOR H17
041.122      4710 HYCNT DS      1      COUNTER FOR TIMER INTERRUPT
041.123      4711 AUTOB DS      1      AUTO BOOT FLAG
041.124      4712 STK DS      2      STACK POINTER FOR RE-BOOT
            4713
041.126      4714 END

```

Assembly complete
 4714 statements
 0 errors detected
 22422 bytes free

CCL2	013322	3865								3867L										
CCL3	013327	3861								3872L										
CCL3.	013332	3873L								3926										
CCL4	013343	3863								3879L	3886	3890	4386							
CCL5	013346	3880L																		
CCL6	013370	3882								3894L										
CCL7	014004	3905L								3919										
CCL8	014026	3913								3918L										
CCL9	014033	3916								3923L										
CD8-H84	000001	627E																		
CD8-H85	000000	626E																		
CHAT2	011152	3372								3377L										
CHC	015234	4075								4166	4219L	4236								
CHKRAD	015112	2022								3265	3276	3335	3353	3981	4063	4095	4100	4106	4112	4118
		4124								4131L	4500									
CKAUT0	011135	1199								3370L										
CLASS0	000000	499E								509	510	511	512	513	514	515	516	517	518	519
		520																		
CLASS1	000040	500E								524										
CLASS6	000300	501E								528										
CLASSM	000340	497E																		
CLK4	000270	1111								1134E										
CLOCK	000201	894								895	1093L									
CLOCK17	034031	749E								2322	4404									
COM	006027	2610E								2691	3115	3129	3191							
COM1	006031	2597								2612L										
COM2	010156	2617								3208L										
COM3	010164	3212L								3213										
CMREQ	013060	3737L								3740	3753									
CON.CD	000000	324E								3541										
CON-DRQ	000002	313E								3499										
CON.DS0	000020	316E																		
CON.DS1	000040	317E																		
CON.DS2	000100	318E																		
CON.DS3	000200	319E																		
CON.EI	000001	312E								3438										
CON.MFM	000004	314E								3438	3508									
CON.M0	000010	315E								3438	3557									
CON.ST	000001	325E								3535	3538									
CONV.E	017005	4504								4508L										
CONV.H	016375	4503L																		
CONV.O	017002	4501								4506L										
CONVERT	016355	4496L								4572										
CRCSUM	040027	4680L																		
CTIFLG	040011	910								1097	1102	1107	1182	1372	1375	1386	1582	2127	2625	4661L
CUI1	000165	1066L								1123	1135									
D.COM	040110	574L								1635	4398									
D.CPB	000040	524E																		
D.CTF	000005	514E																		
D.DAT	000171	340E																		
D.F8S	000007	516E																		
D.FFD	000300	528E																		
D.FOR	000004	513E																		
D.FT	000006	515E																		
D-DECNT	040264	758E																		
D.RAM	040240	577L																		
D.RAML	000037	746E																		
		1684																		

MTR90-1 - H/I-89 MONITOR
Cross Reference Table

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XREF V1.2.1

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D.REA	000010	517E	3673						
D.REC	000001	510E	3644	3665					
D.RSE	000003	512E							
D.RSY	000002	511E							
D.SDP	040206	752E	1642						
D.SDT	040166	753E	4527						
D.SEK	000013	520E	3656						
D.STA	000170	339E	340						
D.TDR	000000	509E	3624						
D.TT	040240	754E	4526						
D.VEC	040130	576L							
D.WPS	000011	518E							
D.WRI	000012	519E							
DAT	006023	2595E	2693	2695	3117	3132	3135		
DATA	040066	2107	2227	4698L					
DD.BOOT	000000	378L							
DD.CPY	000013	389L							
DD.OS	000202	413L							
DD.FRM0	000014	390L							
DD.FRM1	000015	391L							
DD.FRM2	000016	392L							
DD.FRM3	000017	393L							
DD.LSC	000003	381L	3128						
DD.RAD	000004	382L							
DD.RAS	000002	380L	3114						
DD.RDBL	000205	416L							
DD.REA	000005	383L							
DD.REAB	000007	385L	2690						
DD.RDL	000203	414L							
DD.RRDY	000020	394L	3190						
DD.RST	000001	379L							
DD.SDC	000200	411L							
DD.SPF0	000020	400L							
DD.SPF1	000021	401L							
DD.SPF2	000022	402L							
DD.SPF3	000023	403L							
DD.SPF4	000024	404L							
DD.SPF5	000025	405L							
DD.ST	000201	412L							
DD.WDLB	000210	419L							
DD.WRD	000011	387L							
DD.WRDB	000012	388L							
DD.WRI	000006	384L							
DD.WRIB	000010	386L							
DD.WTBL	000206	417L							
DD.WTDL	000207	418L							
DD.WTL	000204	415L							
DEV1.	002377	1720	1730L						
DEV170	002355	1714L							
DEV174	002367	1710	1724L						
DEV2	003006	1718	1728	1736	1740L				
DEVICE	002273	1351	1408	1677E					
DF.CLR	000376	595E							
DF.DI	000040	220E							
DF.DSO	000002	216E							
DF.DSI	000004	217E							
DF.DS2	000010	218E							

MTR90-1 - H/Z-89 MONITOR #09.02.01.
Cross Reference Table

SUBM1	005013	2353L	2366	2373	
SUBM10	014352	2385	4063L		
SUBM10.	014370	4072L	4084		
SUBM11	014373	4064	4075L		
SUBM2	005027	2359L	2380		
SUBM3	005042	2365L	2390		
SUBM4	005046	2363	2368L		
SUBM5	005053	2371L	2393		
SUBM6	005062	2369	2375L		
SUBM7	005075	2360	2382L		
SUBM8	005077	2384L	2387		
SUBM9	005105	2386L	2400		
SYDD	040130	575E			
TO.DNR	000004	536E			
TO.DMS	000005	537E			
TO.MDS	000007	539E			
TO.NIS	000001	533E			
TO.NSC	000002	534E			
TO.MST	000000	532E			
TO.NTO	000006	538E			
TO.WFT	000003	535E			
T1.B8F	000011	552E			
T1.CDE	000010	551E			
T1.DMNF	000003	546E			
T1.DTE	000006	549E			
T1.FE	000012	553E			
T1.ID	000000	543E			
T1.IONF	000002	545E			
T1.RNF	000004	547E			
T1.SKE	000005	548E			
T1.UDE	000001	544E			
T1.WP	000007	550E			
T2.IDA	000001	558E			
T2.IFN	000002	559E			
T2.ILC	000000	557E			
TFDATA	013173	3744	3796L		
TFREQ	013176	3798L	3801	3810	
THA	016001	4119	4299L		
THA1	016007	4303L			
THB	015350	4125	4279L		
THB1	015351	4280L	4304	4306	
THB2	015366	4286	4292L		
THB3	015376	4294	4296L		
TICCNT	040033	1093	1095	1586	1687
TMFG	041121	1753	2301	2309	4709L
TMOUT	004304	1703	2291E		
TMOUT1	004347	2314L	2316		
TMOUT2	004363	2307	2315	2320L	
TMOUT3	004365	2319	2322L		
TMOUT4	004323	2294	2301L		
TOA	015064	1309	2353	3352	4117L
TOA.	005305	2518L	2858		
TOAO	005300	2515L	4121		
TOB	015077	1550	2355	4021	4123L
TOB1	005332	2542L	2550		
TOB0	005322	2519	2521	2535L	4127
TPERRX	040031	4681L			

UMI.HB	000200	776E							
UMI.L5	000000	780E							
UMI.L6	000004	781E							
UMI.L7	000010	782E							
UMI.L8	000014	783E							
UMI.PA	000020	779E							
UMI.PE	000040	778E							
UNI.O	000000	424E							
UNI.1	000040	425E							
UNI.2	000100	426E							
UNI.3	000140	427E							
UNI.M	000140	429E							
UD.CLK	000001	203E	1067	1691					
UD.ODU	000002	202E	1178						
UD.HLT	000200	200E	1109						
UD.NFR	000100	201E	1178						
UP.OP	000174	228E							
UP.FC	000175	229E							
UP.SC	000176	231E							
UP.SR	000176	232E							
UP.ST	000175	230E							
UR.DLL	000000	816E	2081						
UR.DLM	000001	818E	2078						
UR.IER	000001	820E	2085						
UR.IIR	000002	826E							
UR.LCR	000003	830E	2066	2083					
UR.LSR	000005	849E	1868	1968	1989	2292			
UR.MCR	000004	842E							
UR.MSR	000006	858E							
UR.RBR	000000	812E	1972	2296					
UR.THR	000000	814E	1874	1994					
USERFHA	042200	586E	1494	1657	3513	3525	3546	3796	4415
USR	000001	768E							
USR.FE	000040	799E							
USR.OE	000020	800E							
USR.PE	000010	801E							
USR.RXR	000002	803E							
USR.TXE	000004	802E							
USR.TXR	000001	804E							
VEM.NPC	011024	3231	3254	3307L					
VEHLO	040072	3317	3327	3343	4357	4701L			
VIEW	001044	1245L	4569						
VIEW1	001055	1249L	3056						
VIEW12	011111	3055	3351E						
VIEW2	002066	1250	1549L	2579					
VIEW3	006000	1556	2579L						
VIEW3.	003340	1555	2021L	3238					
VIEW3.A	003352	2024	2026L						
VIEW3A	007351	1247	3053L						
VIEW3A.	007355	2582	3055L						
VIEW38	011127	3354	3357L						
VIEW3C	011131	3356	3358L						
VIEW4	007363	1553	3058L	3236					
VIEW5	010171	2034	3221L						
VIEW5.	010212	3227	3230L						
VIEW5A	010174	3223L	3239						
VIEW6	010224	3229	3235L	3262					

Appendix B

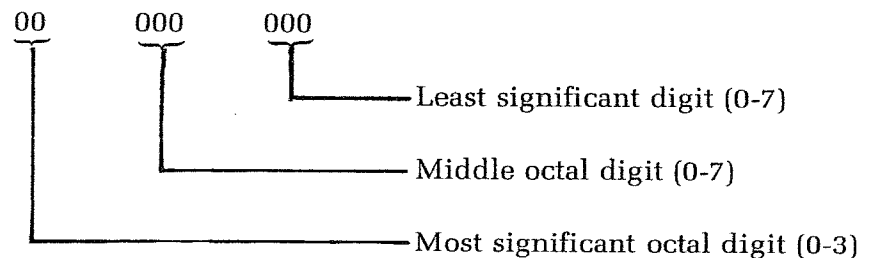
Octal Definitions

Binary numbers are converted to either hexadecimal or octal numbers for display, depending on the current radix setting. This section describes binary to octal conversion. For a description of binary to hexadecimal and octal to hexadecimal conversion, see Appendix C.

The following table shows binary to octal conversion.

<u>BINARY NUMBER</u>	<u>OCTAL DIGIT</u>
000	0
001	1
010	2
011	3
100	4
101	5
110	6
111	7

Each 8-bit byte is displayed as three octal digits as shown below. The octal numbers lie in the range 000 to 377 for binary numbers in the range 00000000 to 11111111.



Since there are only eight bits in a byte, the most significant octal digit only represents two bits and is therefore displayed as 0 to 3. If you inadvertently enter the octal digits 4 through 7 as the most significant digit, the most significant two bits will be interpreted in modulus 4. That is to say, entering the octal digits 4 through 7 will cause MTR-90 to interpret the digits as 0 through 3, respectively.

Also note that 16-bit numbers, such as memory addresses and certain register contents, are made up of two eight-bit binary numbers. The two groups of eight-bit numbers can be represented by two groups of three octal numbers in the range of 000 to 377. This representation of 16-bit binary numbers is known as offset octal or **split octal**. Where the current radix setting is octal, split octal is used consistently for displays of 16-bit numbers.

Split octal should not be confused with octal. For example:

11	111	111	11	111	111	A 16-bit binary number
3	7	7	3	7	7	Split octal representation (377 377)

1	111	111	111	111	111	A 16-bit binary number
1	7	7	7	7	7	True octal representation (177777)

The lower representation shows true octal representation of a 16-bit binary number. True octal representation is rarely used in standard Zenith Data Systems software. Occasionally, you will see split octal numbers printed with a decimal point separating the upper and lower bytes. For example:

		377.377	
	↖		↗
Hi byte			Low byte

Note that 001.000 follows 000.377.

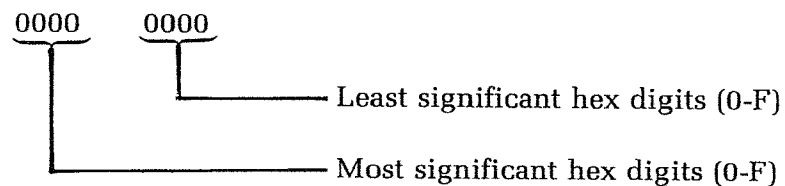
Appendix C

Hexadecimal Definitions

If the radix setting is hexadecimal, all display addresses are given in hexadecimal. The following table shows binary to hexadecimal conversion.

<u>BINARY NUMBER</u>	<u>HEX DIGIT</u>
0000	0
0001	1
0010	2
0011	3
0100	4
0101	5
0110	6
0111	7
1000	8
1001	9
1010	A
1011	B
1100	C
1101	D
1110	E
1111	F

Each byte is displayed as two hexadecimal digits as shown below. Hexadecimal numbers lie in the range 00 to FF for binary numbers 00000000 to 11111111.



Converting Split Octal to Hexadecimal

To convert a split octal number to hexadecimal, first convert the split octal number to binary. In split octal representation, each of the two bytes of a sixteen-bit number are converted independently to octal. Thus, the most significant split octal digit only represents two bits. The following illustrates the conversion to binary of the split octal value 377.377:

$\begin{array}{c} 3 \\ \\ 11 \end{array}$	$\begin{array}{c} 7 \\ \\ 111 \end{array}$	$\begin{array}{c} 7 \\ \\ 111 \end{array}$	$\begin{array}{c} 3 \\ \\ 11 \end{array}$	$\begin{array}{c} 7 \\ \\ 111 \end{array}$	$\begin{array}{c} 7 \\ \\ 111 \end{array}$
---	--	--	---	--	--

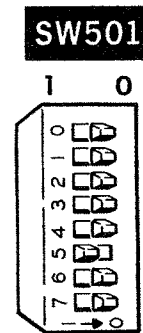
Having converted the split octal number to binary, regroup the bits into nybbles, then convert to hexadecimal. For example:

$\begin{array}{c} 11 \quad 11 \\ \hline 1111 \\ \\ F \end{array}$	$\begin{array}{c} 1 \quad 111 \\ \hline 1111 \\ \\ F \end{array}$	$\begin{array}{c} 11 \quad 11 \\ \hline 1111 \\ \\ F \end{array}$	$\begin{array}{c} 1 \quad 111 \\ \hline 1111 \\ \\ F \end{array}$
---	---	---	---

Appendix D

SW501 Switch Settings

DIP Switch SW501, located on the lower right of the Z-89/90 CPU logic circuit board, incorporates eight individual bits (0-7) which may be set to either one or zero. These switch settings perform the following functions:



SWITCH SECTION	DESCRIPTION
7 6 5 4 3 2 1 0	

Selects the device located at port 07CH (174Q). The settings of these two sections are:

X X X X X X 0 0	- Hard-sectored 5.25-inch disk
X X X X X X 0 1	- H/Z-47
X X X X X X 1 0	- H/Z-67 hard disk/floppy
X X X X X X 1 1	- No device

Selects the device located at port 078H (170Q). The settings of these two sections are:

X X X X 0 0 X X	- Soft-sectored 5.25-inch disk
X X X X 0 1 X X	- H/Z-47
X X X X 1 0 X X	- H/Z-67 Winchester disk-floppy
X X X X 1 1 X X	- No device

Determines whether the primary boot device is at port 07CH (174Q) or at 078H (170Q). The port not configured as primary becomes the secondary device.

X X X 0 X X X X	- Primary boot from device at 07CH (174Q)
X X X 1 X X X X	- Primary boot from device at 078H (170Q)

Disables/enables memory diagnostic on power up.

X X 0 X X X X X	- Initiate memory test on power up
X X 1 X X X X X	- Disable memory test on power up

Sets console (H/Z-19) baud rate.

X 0 X X X X X X	- Sets console baud rate at 9600
X 1 X X X X X X	- Sets console baud rate at 19,200 (not currently supported)

Selects type of boot process

0 X X X X X X X	- Normal boot (normal)
1 X X X X X X X	- Auto boot on power up or reset (not recommended)

Appendix E

CPU Jumpers

MEMORY JUMPERS

The memory decode ROM is located at U517. An older ROM (#442-42) allowed for a maximum of 48K of memory. A newer ROM allows for up to 64K of memory. Memory jumpers should be set as follows:

Older CPU boards have four jumpers.

When you are using the old ROM (#444-42):

	<u>JJ501</u>	<u>JJ502</u>	<u>JJ503</u>	<u>JJ504</u>
16K	0	0	0	0 (or B)
32K	1	0	0	0 (or B)
64K	0	1	0	0 (or B)

When you are using the new ROM (#444-66):

16K	0	0	**	0 (or B)
32K	1	0	**	0 (or B)
48K	0	1	**	0 (or B)
64K*	1	1	**	0 (or B)

* Requires WH-88-16 Accessory Board

** A jumper is required between the center pin of JJ503 and pin 17 of P509 — or P4 of WH-88-16 (which connects to pin 17 of P509). This jumper was permanently installed on some boards.

Newer CPU boards have three jumpers.

They also have the newer decoder ROM, and the jumper wire is part of the circuit foils. These boards should not be used with the old ROM (#444-42). The jumpers are set as follows:

	<u>JJ501</u>	<u>JJ502</u>	<u>JJ503</u>
16K	0	0	0 (or B)
32K	1	0	0 (or B)
48K	0	1	0 (or B)
64K	1	1	0 (or B)

SECONDARY ADDRESS DECODER — U516

MTR-90 requires the use of IC #444-83 at location U516 on the left-hand side of the CPU circuit board. Four nearby jumpers (JJ505, JJ506, JJ507, and JJ508 on older units; or JJ504, JJ505, JJ506, and JJ507 on newer units) must be set as follows:

Older Units	JJ505	JJ506	JJ507	JJ508
Newer Units	<u>JJ504</u>	<u>JJ505</u>	<u>JJ506</u>	<u>JJ507</u>
	1	*	1	1 (or B)

* MTR-90 requires a jumper (#134-1159) from the center pin of JJ506 (or JJ505) to pin 14 of P508.