

MYR89 - H89 MONITOR \$09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 GE.....58
 SUPPORT ROUTINES DAT 15:27:51 28-MAY-80

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2313 *** DAT - DATA BYTE OUTPUT TO Z-47
2314 *
2315 * ENTRY: (A) = BYTE TO OUTPUT
2316 *
2317 * EXIT: (A) = BYTE TO OUTPUT
2318 * (D) = S.DTR
2319 *
2320 * USE: AF, D
2321 *
2322 DAT *
2323 MVI D,S.DTR SET MATCH CONDITION TO DATA TRANSFER
2324 JR COM1 REQUEST BIT

```

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2326 *** COM - OUTPUT COMMAND BYTE TO Z-47
2327 *
2328 * ENTRY: (A) = COMMAND BYTE
2329 *
2330 * EXIT: (A) = COMMAND BYTE
2331 * (D) = S.DON
2332 *
2333 * USE: AF, D
2334 *
2335 COM *
2336 MVI D,S.DON SET MATCH CONDITION TO DONE BIT
2337 PUSH PSW
2338 CALL IN READ CONTROLLER STATUS REGISTER
2339 ANA D GET MATCH BIT ONLY
2340 JR Z,WTDON1 IF NO MATCH, WAIT
2341 POP PSW
2342 CALL OUT1 OUTPUT THE BYTE TO THE DATA PORT
2343 RET

```

```

006.045 ORG 6045A
2345 ** HRNX - HORN EXTENSION ROUTINE
2346 *
2347 * THIS IS AN EXTENSION TO *HORN* TO MAKE ROOM FOR A JUMP
2348 *
2349
006.046 056 011
2350 HRNX MVI L,*CTLFLG
006.047 163
2351 MOV M,E TURN OFF HORN
006.050 321
2352 POP D
006.051 341
2353 POP H
006.052 311
2354 RET

```

MTK89 - H89 MONITOR #09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 DE 59
 SUPPORT ROUTINES

NOISE 15:27:52 28-MAY-80

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2356 **      NOISE - DING BELL ON CONSOLE.
2357 *
2358 *      THIS IS A MODIFICATION TO ALLOW THE H88/H89 TO USE THE CONSOLE BELL.
2359
006.053 076.007 2360 NOISE      MOV     A,A,BEL
006.055 315.302.003 2361      CALL  WCC
006.060 303.140.003 2362      JMP   HORN          CONTINUE WITH NORMAL HORN DELAY.

2364 **      OUT.      - OUTPUT BYTE TO Z-47
2365 *
2366 *      ENTRY:  (A) = OUTPUT BYTE
2367 *
2368 *      EXIT:   NONE
2369 *
2370 *      USE:    NONE
2371
006.063      2372 OUT.      EQU     *
006.063 305      2373      PUSH  R
006.064 107      2374      MOV   R,A
006.065 072.120.041 2375      LDA   FRIM
006.070 117      2376 OUT.1
006.071 170      2377      MOV   C,A
006.072 355.171 2378 *      OUT   (C),A
006.074 301      2379      DB    3550,1710
006.075 311      2380      POP   R
2381      RET

006.100
2383      ORG    6100A
2384 **      TYPMSG - TYPE MESSAGE TO CONSOLE.
2385 *
2386 *      TYPMSG OUTPUTS AN ASCII MESSAGE FROM MEMORY TO THE CONSOLE.
2387 *      UNTIL A NULL IS SENSED
2388 *
2389 *      ENTRY  (H,L) = ADDRESS OF MESSAGE
2390 *      EXIT   NONE
2391 *      USES   A,H,L,F
2392
006.100 176      2393      TYPMSG MOV   A,M      GET CHARACTER
006.101 267      2394      ORA   A      SEE IF A NULL
006.102 310      2395      RZ          IF NULL, EXIT
2396
006.103 315.302.003 2397      CALL  WCC      ELSE OUTPUT CHARACTER TO CONSOLE
006.106 043      2398      INX   H      POINT TO NEXT CHARACTER
006.107 030.367 2399      JR     TYPMSG
2400      OUTPUT IT.

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MTR89 - H89 MONITOR #09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 GE 60
 SUPPORT ROUTINES 15:27:52 28-MAY-80

MSG.PR

2402 ** MSG.PR - MESSAGE FOR MONITOR PROMPT

2403 * CRLE, . HI.

2404 * CRLE, . HI.

2405 * CRLE, . HI.

2406 MSG.PR DB A.CR,A.LF, H: ,0

2409 ** RDBLCK - INPUT A BLOCK FROM Z-47

2410 * RDBLCK READS IN A BLOCK FROM THE DISK CONTROLLER

2411 * RDBLCK READS IN A BLOCK FROM THE DISK CONTROLLER

2412 * RDBLCK READS IN A BLOCK FROM THE DISK CONTROLLER

2413 * ENTRY:

2414 * HL = LOAD ADDRESS

2415 * R = COUNT

2416 * C = SIDE/UNIT/SECTOR

2417 * EXIT: NONE

2418 * EXIT: NONE

2419 * USES: ALL

2420 * USES: ALL

2421 * USES: ALL

006.121 EQU *
 006.121 076.007 MVI A,DC,REAR
 006.123 315 027 006 CALL COM
 006.126 257 XRA A
 006.127 315 023 006 CALL DAT
 006.132 171 MOV A,C
 006.133 315 023 006 CALL DAT

006.136 315 067 001 CALL FIN
 006.141 167 MOV M,A
 006.142 043 INX H
 006.143 020 371 DJNZ R02
 006.145 311 RET

006.136 315 067 001 CALL FIN
 006.141 167 MOV M,A
 006.142 043 INX H
 006.143 020 371 DJNZ R02
 006.145 311 RET

006.136 315 067 001 CALL FIN
 006.141 167 MOV M,A
 006.142 043 INX H
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 006.145 311 RET

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 006.142 043 INX H
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 006.145 311 RET

006.136 315 067 001 CALL FIN
 006.141 167 MOV M,A
 006.142 043 INX H
 006.143 020 371 DJNZ R02
 006.145 311 RET

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MTR89 - H89 MONITOR #09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
GE 61.
SUPPORT ROUTINES
INI. 15:27:53 28-MAY-80

2451 ** INI. - INPUT BYTE FROM (PRIM+1) PORT
2452 *
2453 * ENTRY: NONE
2454 *
2455 * EXIT: (A) = INPUT BYTE
2456 *
2457 * USE: A
2458 *
006.156 305 INI. EQU *
006.156 305 EQU PUSH B
006.157 072 120 041 LDA PRIM GET PORT ADDRESS
006.162 074 2461 INR A SET TO (PRIM+1)
006.163 030 007 JR IN.1 GO TO INPUT ROUTINE

006.165 2465 ORG 6165A
2466 ** MSG.GO - (G)O
2467 *
2468 * .GO
2469 *

006.165 157 040 000 2470 MSG.GO DB 'O',0

2472 ** IN. - INPUT BYTE FROM PORT (PRIM)
2473 *
2474 * ENTRY: NONE
2475 *
2476 * EXIT: (A) = INPUT BYTE
2477 *
2478 * USE: A
2479 *
006.170 EQU *
006.170 305 IN. EQU *
006.171 072 120 041 LDA PRIM GET PORT ADDRESS
006.174 117 2482 IN.1 MOV C,A SET ADDR. TO REG.C
2483 * IN A,(C)
2484 * DB 3550,1700 INPUT BYTE
006.175 355 170 2485 DB 3550,1700
006.177 301 2486 POP B
006.200 311 2487 RET

006.201 2489 ORG 6201A
2490 ** MSG.SUB - (S)UBSTITUTE
2491 *
2492 * .SUBSTITUTE
2493 *
006.201 165 142 163 2494 MSG.SUB DB 'ubstitute',0

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MTK89 - H89 MONITOR #09,01:00, Zenith Data Systems UNIX H8/H89 Cross Assembler PA
GE 62 MSG.PC 15:27:54 28-MAY-80
SUPPORT ROUTINES

2496 ** MSG.PC - (PROGRAM.COUNTER)
2497 *
2498 * PROGRAM.COUNTER
2499

006,214 162,157,147 2500 MSG.PC DB PROGRAM.COUNTER / 0

2502 ** MSG.BT - (B)DOT
2503 *
2504 * BDOT

006,234 157,157,164 2506 MSG.BT DB DOT / 0

MT889 - H89 MONITOR #09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 GE.....43.
 SPEED - ROTATIONAL SPEED TEST FOR H89 DISK DRIVE 15:27:54 28-MAY-80

2509 *** SPEED - ROTATIONAL SPEED TEST FOR 5.25 INCH DISK DRIVE
 2510 *
 2511 * *SPEED* IS USED ONLY FOR GROSS ADJUSTMENT OF DRIVE ROTATIONAL
 2512 * SPEED IF THE FIRST READ/WRITE TEST OF THE UNIT FAILS DURING SET UP.
 2513 *
 2514 * USE OF *SPEED* IS AS FOLLOWS:
 2515 *
 2516 * 1. ENTER *GO AND THE ENTRY ADDRESS OF *SPEED*
 2517 * 2. ADJUST DRIVE SPEED UNTIL DATA AT DISPLAYED
 2518 * EQUALS 200
 2519 * A. IF SPEED < 200, TURN ADJUSTMENT CLOCKWISE
 2520 * B. IF SPEED > 200, TURN COUNTERCLOCKWISE
 2521 *
 2522 * THE ABOVE TEST ADJUSTS SY0:, TO ADJUST SY1:, USE HD05

TABLE EQUIVALENCES

2524 ** I/O PORTS
 2525 *
 2526 * I/O PORTS
 2527 OP.DC EQU 1770 DRIVE CONTROL OUTPUT PORT
 2528 IF.IS EQU 1770 DRIVE STATUS INPUT PORT

MASKS

2530 *
 2531 *
 2532 DS.HOLE EQU 00000001E DRIVE STATUS SECTOR/INDEX HOLE

CONSTANTS

2534 *
 2535 *
 2536 ONTRO EQU 0220 TURN ON SY0:

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MTR89 - H89 MONITOR #09:01:00.
GE
SPEED - ROTATIONAL SPEED TEST FOR H89 DISK DRIVE
Zenith Data Systems UNIX H8/H89 Cross Assembler FA
15:27:54 28-MAY-80

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006,240 041 371 006 2538 SPEED LXI H,MSG,SPD OUTPUT SPEED MESSAGE
006,243 315 100 006 2539 CALL TYFMSG
006,246 076 000 2540 MVI A,0 SET FLAG AT IOWRK FOR 'WORKING' MESSAGE
006,250 062 002 040 2541 STA IOWRK TURN ON DRIVE ZERO
006,253 076 022 2542 MVI A,ANDRO
006,255 323 177 2543 OUT OP,DC
006,257 062 033 040 2544 SPEED1 LHLD TICCNT GET TICK COUNTER
006,262 174 2545 MOV A,H FORM TWO'S COMPLEMENT OF TICK COUNTER
006,263 057 2546 CMA (D,E) = NEGATIVE TICK COUNTER
006,264 127 2547 MOV D,A
006,265 175 2548 MOV A,L
006,266 057 2549 CMA
006,267 074 2550 INR A
006,270 137 2551 MOV E,A
006,271 322 275 006 2552 JNC SPEED2 IF NO CARRY FROM LSB
006,274 024 2553 INR D ELSE, INCREMENT MSR
006,275 001 000 000 2554 SPEED2 LXI B,0 ZERO REV COUNTERS
006,300 333 177 2555 IN IF,IS INPUT DISK STATUS
006,302 346 001 2556 ANI DS,HOLE MASK FOR SECTOR/INDEX PULSES
006,304 312 300 006 2557 JZ SPEED3 IF NO HOLE PRESENT
006,306 312 300 006 2558 JZ SPEED3
006,307 333 177 2559 * HOLE PRESENT, WAIT FOR IT TO LEAVE
006,308 333 177 2560 *
006,309 333 177 2561 *
006,310 333 177 2562 *
006,311 346 001 2563 IN IF,IS GET DISK STATUS
006,312 346 001 2564 ANI DS,HOLE GET HOLE PULSES
006,313 302 307 006 2565 JNZ SPEED4 WAIT UNTIL HOLE IS GONE AND WE HAVE MEDIA
006,314 004 2566 INR B INCREMENT HOLE COUNTER
006,317 170 2567 MOV A,B TEST FOR FIVE REVOLUTIONS
006,320 376 070 2568 CFI 56
006,322 302 300 006 2569 JNZ SPEED3 NOT FIVE, WAIT FOR MORE HOLES
006,323 302 300 006 2570 * HAVE FIVE REVS, DISPLAY DIFFERENCE OF TICK COUNTER AND EXPECTED TIME DIF
006,324 302 300 006 2571 *
006,325 052 033 040 2572 *
006,326 052 033 040 2573 LHLD TICCNT GET CURRENT TICK VALUE
006,327 031 2574 DAD D SUBTRACT START VALUE
006,328 031 2575 LXI D,377377A-500+1+200Q SUBTRACT 500 FOR REVS, +200Q FOR OFFSET
006,329 021 214 376 2576 DAD D (H,L) = OFFSET RESULT
006,330 031 2577 PUSH H SAVE RESULT
006,331 031 2578 LXI H,MSG,WRK POINT TO 'WORKING' MESSAGE
006,332 041 062 007 2579 LDA IOWRK GET 'WORKING' FLAG
006,333 072 002 040 2580 XRI 1 INVERT LOWER BIT
006,334 356 001 2581 STA IOWRK SAVE NEW VALUE
006,335 062 002 040 2582 JNZ SPEEDS IF TO DISPLAY 'WORKING'
006,336 302 357 006 2583 *
006,337 041 100 007 2584 LXI H,MSG,HSS POINT TO 'HOME', 'SPACES', AND SPEED MSG
006,338 315 100 006 2585 SPEEDS CALL TYFMSG OUTPUT MESSAGE
006,339 315 100 006 2586 POP H GET TEST RESULT
006,340 341 2587 CALL TOA OUTPUT RESULT TO CONSOLE
006,341 315 325 005 2588 JMP SPEED1 PERFORM ANOTHER SAMPLE
006,342 303 257 006 2589 *

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NTR89 - H89 MONITOR *09.01.00.
 GE 65
 SPEED - ROTATIONAL SPEED TEST FOR H89 DISK DRIVE

Zenith Data Systems UNIX H8/H89 Cross Assembler FA
 15:27:55 28-MAY-80

MSG.SPD

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2590 ** MSG.SPD - SPEED TEST MESSAGE
2591 *
2592 *
2593 * Disk drive rotational speed test.
2594 *
2595 *
2596 * Drive speed =
006.371 033 105 012 MSG.SPD DB A.ESC,'E',A.LF
006.374 011 104 151 DB ,
007.041 011 011 104 DB Disk drive rotational speed test.,A.CR,A.LF,A.LF
007.061 000 DB 0
2600

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2602 ** MSG.WRK - "WORKING" MESSAGE FOR SPEED TEST
2603 *
2604 * DISPLAYS "WORKING" AT HOME POSITION AND RETURNS CURSOR TO SPEED =
2605 MSG.WRK DB A.ESC,'H' CURSOR HOME
2606 DB 'Working'
2607 DB A.ESC,'Y>' CURSOR ADDRESS OF SPEED = VALUE
2608 DB 0 END MESSAGE
2609

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2611 ** MSG.HSS - BLANKS "WORKING" MESSAGE
2612 *
2613 MSG.HSS DB A.ESC,'H' CURSOR HOME
2614 DB BLANKS
2615 DB A.ESC,'Y>' CURSOR ADDRESS OF SPEED = VALUE
2616 DB 0 END MESSAGE
2617

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MTR89 - 'H89' MONITOR  #07:01:00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
GE.....66.
DYMEN - DYNAMIC MEMORY TEST 15:27:56 28-MAY-80

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2620 **      DYMEN - DYNAMIC MEMORY TEST
2621 *
2622 *      DYMEN TESTS THE DYNAMIC MEMORY IN THE H88/H89 BY PLACING
2623 *      A KNOWN PATTERN IN EACH DYNAMIC MEMORY CELL AND THEN
2624 *      PERFORMING A READ, INCREMENT, READ SEQUENCE WITH A DELAY
2625 *      BETWEEN EACH PASS OF THE TEST
2626 *
2627 *      ENTRY NONE
2628 *
2629 *      EXIT ON RESET
2630 *
2631 *      USES A,B,C,D,E,H,L,F,A',F',IX,IY
2632 *
2633
2634
2635 DYMEN MVI A,0 MAKE SURE CLOCK AND SINGLE STEP ARE OFF
2636 OUT H88.CTL
2637
2638 *      DETERMINE END OF MEMORY
2639 *
2640 IF .RAM.
2641 ELSE
2642 DYMEN1 LXI H,START
2643 ENDIF
2644
2645
2646 MVI A,1 SET RAM TO ZERO
2647 DYMEN2 MVI M,0 SET MEMORY TO ONE
2648 INR M SEE IF (A) = (H,L)
2649 CHF M IF NOT EQUAL, THE END OF RAM HAS BEEN REACHED
2650 JR NZ,DYMEN3
2651
2652 INX H ELSE, POINT TO NEXT LOCATION IN RAM
2653 JR DYMEN2
2654
2655
2656 DYMEN3 DCX H POINT TO LAST GOOD LOCATION
2657 XCHG PUT ENDING ADDRESS IN D,E
2658 LXI H,MMSG,RAM OUTPUT ENDING ADDRESS
2659
2660 *      LD IX,DY3.3 RETURN ADDRESS
2661 DB MI,LDXA,MI,LDXB
2662 DW DY3.3
2663
2664 JR DYMEN3
2665
2666 DY3.3 MOV A,D OUTPUT ADDRESS MSB
2667
2668 *      LD IX,DY3.5 RETURN ADDRESS
2669 DB MI,LDXA,MI,LDXB
2670 DW DY3.5
2671
2672 JMP DVBYT
2673
2674 DY3.5 MOV A,E LSB
2675
2676 *      LD IX,DY3.7 RETURN ADDRESS
2677 DB MI,LDXA,MI,LDXB

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MTK89 - H89 MONITOR *09.01.00.
GE 67
DYMEN - DYNAMIC MEMORY TEST

007.166 173 007 2678 DW DY3.7
2679
007.170 303 160 003 2680 JMP DYBYT
2681
007.173 023 2682 DY3.7 INX D (D,E) = LAST BYTE OF RAM + 1
2683
2684 * TEST MEMORY
2685 *
007.174 006 001 2686 MVI B,1 (B) = CONTENTS OF RAM AFTER SIZING
007.176 041 237 003 2687 LXI H,MSG,PAS OUTPUT PASS MESSAGE
2688
007.201 335 041 2689 LD IX,DYMEM4 RETURN ADDRESS
007.203 207 007 2690 DB MI,LDXA,MI,LDXB
2691
007.205 030 077 2692 JR DYMSG
2693
000.001 2694 IF .RAM.
2695
007.207 041 000 040 2696 DYMEN4 LXI H,START POINT BACK TO BEGINNING OF RAM
2697
007.212 176 2698 DYMEN5 MOV A,M READ CURRENT CONTENTS
2699
007.213 270 2701 CMP B SEE IF CORRECT CONTENTS STILL REMAIN
007.214 302 307 000 2702 JNZ DYMEN9 FAILURE, SEE IF AT END OF RAM
2703
007.217 074 2704 INR A INCREMENT RAM
007.220 167 2705 MOV M,A SEE IF WRITE WAS SUCCESSFUL
007.221 276 2706 CMP H
007.222 302 307 000 2707 JNZ DYMEN9
2708
007.225 043 2709 INX H
007.226 175 2710 MOV A,L GET LSR AND TEST FOR REACHING END OF RAM
007.227 273 2711 CMP E
007.230 040 360 2712 JR NZ,DYMEN5 IF LSR NOT EQUAL
2713
007.232 174 2714 MOV A,H CHECK LSR
007.233 272 2715 CMP D
007.234 040 354 2716 JR NZ,DYMEN5
2717
007.236 046 003 2717 * HAVE REACHED END OF MEMORY!
007.240 076 010 2718 * OUTPUT LAST VALUE TESTED
2719
2720 *
2721 MVI H,3 OUTPUT 3 BACKSPACES
2722 MVI A,A,BKS
2723
007.242 2724 DYMEN5 EQU *
2725
007.242 375 041 2726 * LD IX,DY5.53 RETURN ADDRESS
007.244 251 007 2727 DB MI,LDXA,MI,LDXB
2728
007.246 303 143 003 2729 JMP DYASC
2730
007.251 045 2731
007.252 040 366 2732 DY5.53 INR H
2733
2734 JR NZ,DYMEN5.5
2735

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Zenith Data Systems UNIX H8/H89 Cross Assembler PA

15:27:56 28-MAY-80

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MTR89 - H89 MONITOR  #09.01.00.
GE 68
DYMEN - DYNAMIC MEMORY TEST
.....
007.254 004 2735 INR B SHOW NEXT PASS VALUE
007.255 170 2736 MOV A,B VALUE TESTED
.....
007.256 335 041 2737 *
007.260 273 000 2738 * IX,DYMEM6 RETURN ADDRESS
007.262 303 160 003 2739 DB MI,LOXA,MI,LPXA
007.262 303 160 003 2740 DW DYMEM6
007.262 303 160 003 2741 JMP DYBYT
007.262 303 160 003 2742
007.262 303 160 003 2743
007.262 303 160 003 2744
.....
007.265 041 000 000 2745 ** !!THE DYNAMIC RAM TEST CONTINUES ELSEWARE!! **
007.270 006 002 2746 * !!AND THEN RETURNS TO HERE!!!!!!!!!!!!!! **
007.272 045 2747
007.273 040 375 2748
.....
007.275 055 2749 DY10.5 LXI H,Q DELAY AND DING BELL AGAIN
007.276 040 372 2750 MVI B,2 2 LOOPS
007.272 045 2751 DYMEM11 DCR H
007.273 040 375 2752 JR NZ,DYMEM11
.....
007.275 055 2753
007.276 040 372 2754 DCR L
007.276 040 372 2755 JR NZ,DYMEM11
.....
007.300 005 2756
007.301 040 367 2757 DCR B
007.301 040 367 2758 JR NZ,DYMEM11
.....
007.303 303 360 003 2759 JMP DYMEM10 AGAIN
007.303 303 360 003 2760
.....
.....
2762 ** DYMEN - DYNAMIC RAM TEST MESSAGE OUTPUT ROUTINE
2763 *
2764 * ENTRY (H,L) = MESSAGE ADDRESS
2765 * (IX) = RETURN ADDRESS
2766 *
2767 * EXIT TO (IX)
2768 *
2769 * USES A,H,L,F,IY
2770 *
.....
007.306 176 2771 DYMEN MOV A,M GET MESSAGE BYTE
007.306 176 2772 DYMEN
.....
007.307 375 041 2773 *
007.311 316 007 2774 * IY,DYMSG.5 RETURN ADDRESS
007.311 316 007 2775 DB MI,LOXA,MI,LPXA
007.311 316 007 2776 DW DYMSG.5
007.313 303 143 003 2777 JMP DYASC OUTPUT ASCII
007.313 303 143 003 2778
.....
007.316 267 2779 DYMEN.5 ORA A SEE IF NULL TO END STRING
007.317 043 2780 INX H POINT TO NEXT CHARACTER
007.320 040 364 2781 JR NZ,DYMSG IF NOT DONE YET
007.322 335 351 2782 *
007.322 335 351 2783 * (IX)
007.322 335 351 2784 * DB MI,LOXA,MI,LPXA
007.322 335 351 2785
.....

```

MT889 - H89 MONITOR #09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 GE 69
 DYNEM - DYNAMIC MEMORY TEST 15:27:58 28-MAY-80

```

2787 ** MSG,RAM - RAM TEST MESSAGE
2788 *
2789
007,324 033 105 MSG,RAM DB A,ESC,'E'
007,326 104 171 156 2791 DB 'Dynamic RAM test'
007,346 015 012 012 2792 DB A,CR,A,LF,A,LF
007,351 011 040 114 2793 DB LWA = '
007,361 000 2794 DB 0

```

```

2796 ** MSG,EQ - EQUALS MESSAGE
2797 *
2798
007,362 040 075 040 2799 MSG,EQ DB ' = '
007,365 000 2800 DB 0
007,366 107 101 103 2801 DB 'GAC.'
2802

```

```

MTR89 - H89 MONITOR #09.01.00, Zenith Data Systems UNIX H8/H89 Cross Assembler PA
GE 70
ENTRY POINTS FOR HARDWARE TESTS
15:27:59 28-MAY-80

```

```

2805 ** ENTRY POINT FOR FLOPPY DISK ROTATIONAL SPEED TEST
2806 *
2807 IF .RAM
2808 ELSE
2809 ERNZ 10000A-6-* MUST BE SIX BYTES BEFORE END
2810 ENDIF
2811
2812 ESPEED JMP SPEED

```

```

2814 ** ENTRY POINT FOR DYNAMIC MEMORY TEST
2815 *
2816 IF .RAM
2817 ELSE
2818 ERNZ 10000A-3-* MUST BE THREE BYTES BEFORE END
2819 ENDIF
2820
2821 EDYMEM JMP DYMEM
2822
2823 IF .RAM
2824 ELSE
2825 ERNZ *-10000A MUST NOT EXCEED 2K BYTES
2826 ENDIF
2827

```

MTK89 - H89 MONITOR *09.01.00. Zenith Data Systems UNIX H8/H89 Cross Assembler PA
 GE 71
 RAM CELLS

15:27:59 28-MAY-80

```

2830 ** THE FOLLOWING ARE CONTROL CELLS AND FLAGS USED BY THE KEYSET
2831 * MONITOR.
2832
2833     040.000     ORG     40000A     8192
2834     040.000     START   DS      2     JUMP STARTING ADDRESS
2835     040.002     IORUK   DS      2     IN OR OUT INSTRUCTION
2836     040.004     PRSRAM  EQU     *     FOLLOWING CELLS INITIALIZED FROM ROM
2837     040.004     DS      1     RET
2838
2839     040.005     REGI    DS      1     INDEX OF REGISTER UNDER DISPLAY
2840     040.006     DSPROT  DS      1     PERIOD FLAG BYTE
2841     040.007     DSPMOD  DS      1     DISPLAY MODE
2842
2843     040.010     MFLAG   DS      1     USER FLAG OPTIONS
2844     040.010     *       SEE *40.0XX* BITS DESCRIBED AT FRONT.
2845
2846     040.011     CILFLG  DS      1     FRONT PANEL CONTROL BITS
2847     040.012     REFIND  DS      1     REFRESH INDEX (0 TO 7)
2848     040.012     PRSL   EQU     *     END OF AREA INITIALIZED FROM ROM
2849     040.013     FLEDS   EQU     *     FRONT PANEL LED PATTERNS
2850     040.013     DS      1     ADDR 0
2851     040.013     ALEDS   DS      1     ADDR 1
2852     040.014     DS      1     ADDR 2
2853     040.015     DS      1     ADDR 3
2854
2855     040.016     DS      1     ADDR 4
2856     040.017     DS      1     ADDR 5
2857     040.020     DS      1     DATA 0
2858     040.021     DS      1     DATA 1
2859     040.022     DS      1     DATA 2
2860
2861     040.024     ABUSS   DS      2     ADDRESS BUSS
2862     040.024     RCCA    DS      1     RCC SAVE AREA
2863     040.026     CRCSUM  DS      2     CRC-16 CHECKSUM
2864     040.027     TFERRX  DS      2     TAPE ERROR EXIT ADDRESS
2865     040.031     TICCNT  DS      2     CLOCK TIC COUNTER
2866
2867     040.035     REGPTR  DS      2     REGISER CONTENTS POINTER
2868
2869     040.037     UIVEC   DS      0     USER INTERRUPT VECTORS
2870     040.037     DS      3     JUMP TO CLOCK PROCESSOR
2871     040.042     DS      3     JUMP TO SINGLE STEP PROCESSOR
2872     040.045     DS      3     JUMP TO I/O 3
2873     040.050     DS      3     JUMP TO I/O 4
2874     040.053     DS      3     JUMP TO I/O 5
2875     040.056     DS      3     JUMP TO I/O 6
2876     040.061     DS      3     JUMP TO I/O 7
2877
2878
2879
2880 ** H88/H89 RAM USAGE BEYOND THAT OF H8MTRE
2881 *
2882     040.064     NMIRET  DS      2
2883
2884     041.120     PRIM    DS      1     PRIMARY DEVICE ADDR. PORT
2885     041.120     ORG     41120A
  
```

```

MTR89 - H89 MONITOR #09.01.00.
RE.....72.
RAM CELLS

Zenith Data Systems UNIX H8/H89 Cross Assembler PA
15:28:00 28-MAY-80

041.121 ..... 2886 IMEG DS 1 ..... TIMER INTERRUPT FLAG, =1 FOR Z47, =0 FOR H17
041.122 ..... 2887 MYCNT DS 1 ..... COUNTER FOR TIMER INTERRUPT
041.123 ..... 2888 AUTOR DS 1 ..... AUTO ROOT FLAG
041.124 ..... 2889 STK DS 2 ..... STACK POINTER FOR RE-BOOT
2890 .....
040.066 ..... 2891 ORG 40066A .....
040.066 ..... 2892 DATA DS 1 ..... OUTPUT 3620 DATA SAVE AREA
040.067 ..... 2893 END .....

ASSEMBLY COMPLETE
2893 STATEMENTS
.....0 ERRORS DETECTED
14204 BYTES FREE

```

MYR89 - HB9 MONITOR 09.01.00.

..I:IN.FEEX

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CROSS-REFERENCE TABLE.

[illegible]

MT889 - H89 MONITOR *09.01.00. XREF VI.1
CROSS REFERENCE TABLE PAGE 75

ISPMOD	040007	2841L			
ISPROT	040006	2840L			
DY10.5	007265	1755	2749L		
DY3.3	007153	2662	2666L		
DY3.5	007163	2670	2674L		
DY3.7	007173	2678	2682L		
DY5.53	007251	2728	2732L		
DY9.3	003315	830	1717L		
DY9.4	003326	1722	1726L		
DY9.5	003335	1730	1734L		
DY9.8	003350	1739	1743L		
DYASC	003143	1577E	1611	1624	1634 1757 2730 2778
DYASCI	003144	1580L	1582		
DYBYT	003160	1597L	1724	1732	1749 2672 2680 2742
DYBYT.2	003202	1609	1613L		
DYBYT.4	003221	1622	1626L		
DYBYT.6	003235	1632	1636E		
DYME5.5	007242	2724E	2733		
DYMEH	007116	1830	2635L	2821	
DYMEM1	007122	2644L			
DYMEM10	003360	1747	1751L	2740	
DYMEM11	007272	2751L	2752	2755	2758
DYMEM2	007127	2647L	2653		
DYMEM3	007140	2650	2656L		
DYMEM4	007207	821	2691	2698L	
DYMEM5	007212	2700L	2712	2716	
DYMEM6	000273	815L	2740		
DYMEM7	000276	816L	819		
DYMEM9	000307	825L	2702	2707	
DYMSG	007306	832	1741	2664	2693 2772L 2782
DYMSG.5	007316	2776	2780L		
EDYMEM	007375	2821L			
EXIT	034027	350E	1413	1415	
ERFICNT	000012	351E	1231		
ERRMSG	001045	929L	1322		
ERROR	000322	654	801	852E	1326 1693
ESPEED	007372	2812L			
FFLEDS	040013	2850E			
GO	001222	1030	1054L		
GO.	000063	596L	1054	1997	
G088	001146	912	1015L		
G088.1	001177	1018	1027L		
H17	002207	1339E	1453		
H17A	002337	1357L	1359		
H88.CTL	000362	130E	1791	1959	2636
H88.SW	000362	134E	1253	1430	1798 1828
H88.CK	000002	131E	1790		
H88.SS	000001	132E			
H88.0	000014	140E	1462		
H88.4	000003	142E	1443		
H88.AT	000200	135E	1254		
H88.BK	000100	136E	1799		
H88.DV	000020	138E	1432		
H88.M	000040	137E	1829		
HRN	002140	1283L	2362		
HRNO	002143	585	1286L		
HRN2	002160	1297L	1298		
HRNX	006045	1300	2350L		

XREF V1.1
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WTR89 - HBV MONITOR *09.01.00.

CROSS-REFERENCE TABLE

IN.	006170	966	1205	2338	2480E
IN.1	006174	2463	2483L		
IN.1	006156	969	2459E		
INIT	000073	490	494	624L	628
INIT0	000000	488L			
INIT0.0	000003	489L	1838		
INITOX	004000	488	1790L		
INITOX1	004050	1821L	1822	1824	
INIT1	000107	637L	642		
INIT2	000117	644L			
INT1	000010	503E			
INT2	000020	528E			
INT3	000030	549L			
INT4	000040	559L			
INT5	000050	569L			
INT6	000060	593L			
INT7	000070	605L			
INIXIT	000172	745L	778	1072	
IOA	003062	1005	1026	1502L	2072
IOA1	005176	1502	2170L		
IOA2	005204	2174L	2192	2201	
IOA3	005242	2176	2179	2194L	
IOA4	005260	2195	2205L		
IOB	003066	1520L			
IOB1	003070	1521L	1528	1560	1567
IOB1.5	003126	1539	1557L		
IOB2	003135	1526	1564L		
IOC	005301	2081	2116	2230L	
IOWRK	040002	2070	2541	2579	2835L
IP_DS	000177	2528E	2556	2562	
IP_FAD	000360	124E			
IP_TPC	000371	147E			
IP_TFD	000370	149E			
IROC	005150	994	998	1017	2067
IROC1	005166	2149	2154L	2144L	2156
LRA	003047	1482L			
LRA...	003052	789	988	1022	1483L
MIANI	000346	184E			
MIEXAF	000010	191E	1114	1116	1122
		2043		1394	1433
				1435	1579
				1585	2027
				2040	
MI_HLT	000166	179E	795		
MI_IN	000333	181E	1929	1938	
MI_JIXA	000335	192E	1638	2785	
MI_JIXB	000351	193E	1638	2785	
MI_JIYA	000375	194E	1588		
MI_JIYB	000351	195E	1588		
MI_JMP	000303	186E	1411		
MI_LDA	000072	183E			
MI_LDXA	000335	187E	829	1721	1729
MI_LDXB	000041	188E	829	1721	1729
MI_LDYA	000375	189E	1608	1621	1631
MI_LDYB	000041	190E	1608	1621	1631
MI_LXID	000021	185E			
MI_OUT	000323	182E	1926	1945	
MI_RET	000311	180E	1778		
MSG_BT	006234	1988	2506L		
MSG_EQ	007362	1735	2799L		
MSG_ERR	001047	826	936L		

NTR89 - H89 MONITOR #09.01.00.

XREF VI.1

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CROSS. REFERENCE TABLE

MSG.G0	004165	1015	2470L
MSG.HSS	007100	2584	2614L
MSG.PAS	003237	1643L	2687
MSG.FC	006214	985	2500L
MSG.FR	006111	1259	2407L
MSG.RAM	007324	2658	2790L
MSG.SPD	006371	2538	2597L
MSG.SUB	006201	2065	2494L
MSG.WRK	007042	2578	2604L
MTR	000344	873E	1087
MTR.15	000354	880L	1260
MTR.2	000357	882L	897
MTR.3	000371	886L	893
MTR.4	001014	887	899L
MTR.1	000345	876E	877
MTR.1	001025	884	909E
MTR.1	000004	885	923
MYCNT	041122	1404	2032
NE7	001316	1115	1123L
NEOOT	001261	1105L	1992
NEOOT0	001262	1106L	1133
NMI	004116	705	1898L
NMI0.5	004154	1916	1919
NMI1	004173	1911	1936L
NMI1.5	004206	1939	1945L
NMI2	004212	1948L	
NMI2.2	004225	1955	1957L
NMI2.5	004236	1922	1927
NMI3	004237	1934	1930
NMIENT	000146	705L	1943
NMIRET	040064	1897	1906
NODEV	002171	1148	1207
NOISE	006053	1304	1321E
ONDR0	000022	2536E	2360L
OP.CTL	000360	135E	2542
OP.DC	000177	2527E	771
OP.DIG	000360	135E	1069
OP.SEG	000361	137E	2543
OP.IFC	000371	148E	
OP.TPD	000370	150E	660
OUT.	004063	1180	2372E
OUT.1	006070	2376L	2449
OUT.1	006146	2342	2444E
FCA1	001103	918	985L
FIN	001067	945E	1003L
FRIM	041130	1424	1188
FRSL	000007	489	2375
FRSRAM	040004	489	2447
FRSRAM	003371	1771E	2461
R.ABORT	033366	346E	2482
R.READ	034077	347E	2485L
R.SDF	002071	1230E	2848
R.SDF1	002110	1236	1834
RCC	003262	882	1367
RCC1	003262	1686L	1238L
RCC2	003270	1690L	1356
RCA	040026	2864L	1356
			1521
			1684E
			2144
			2174
			2350
			2304

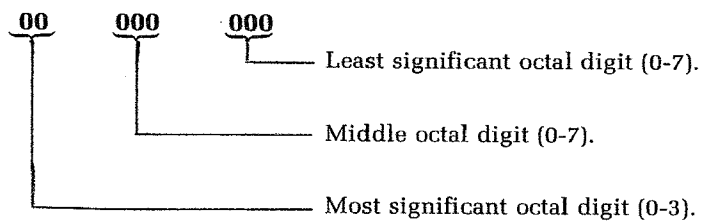
APPENDIX B

OCTAL DEFINITIONS

Binary numbers are converted to octal format for display. The following table shows binary to octal conversion.

<u>BINARY NUMBER</u>	<u>OCTAL DIGIT</u>
000	0
001	1
010	2
011	3
100	4
101	5
110	6
111	7

Each byte is displayed as two-and-one-half octal digits. The octal numbers lie in the range of 000 to 377 for binary numbers in the range 00000000 to 11111111, as shown below.



NOTE: As there are only eight bits in a byte, the most significant octal digit only represents two bits and is therefore displayed as 0 to 3. If the user should inadvertently enter the octal digits 4 to 7 into the most significant digit, the most significant bit is lost. Losing this bit converts 4 through 7 into the digits 0 through 3 respectively.

Also note that 16-bit numbers, such as memory addresses and certain register contents, are displayed as two eight-bit numbers. Therefore, the representation of 16-bit numbers is made up of **two** groups of three octal numbers in the range of 000 to 377. This representation of 16-bit binary numbers is known as offset octal or **split-octal**, and is used consistently for displays of 16-bit numbers.

Split-octal must not be confused with octal. For example:

<u>11</u>	<u>111</u>	<u>111</u>	<u>11</u>	<u>111</u>	<u>111</u>	A 16-bit binary number
3	7	7	3	7	7	Split-octal representation (377 377)

<u>1</u>	<u>111</u>	<u>111</u>	<u>111</u>	<u>111</u>	<u>111</u>	A 16-bit binary number
1	7	7	7	7	7	True Octal representation (177777)

The lower example shows true octal representation of a 16-bit binary number. True octal representation is never used in standard Zenith Data Systems software. Occasionally you will see split-octal numbers printed with a decimal point separating the upper and lower bytes. For example:

<u>377</u>	<u>377</u>
Hi Byte	Lo Byte

Note that 001.000 follows 000.377.

